



#### defect description:

DROE- is only active when the NVRAM is written by the CPU.

Read accesses fail because the transceiver U462 is not enabled.

It is assumed that the PLD U352 (PLS173) has an internal defect.

This circuit allows to read the NVRAM and also to access the vertical data directly using REDAT-.

#### Tek 2710 Display Storage Repair Circuit

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REVISION:

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