



**Step response:** Active low pass 1<sup>st</sup> order (analog and S/C)

**Time constant**  $T=1E-5$  sec = 10 $\mu$ sec

**S/C data:**

Both switched caps are realized as Euler-forward approximations

Clock frequency 1E6 Hz (1 MHz)

Input switched capacitor  $C_{SC1}=1E-9$  F

Feedback capacitor (fixed)  $C=1E-9$  F

Feedback switched capacitor: 1E-10 F

Comment: The small spikes are due to clock pulses which are not completely isolated from each other.