



**Step response:** Active low pass 1<sup>st</sup> order (analog and S/C)

**Time constant**  $T=1\text{E-}5 \text{ sec} = 10\mu\text{sec}$

**S/C data:**

Both switched caps are realized as Euler-forward approximations

Clock frequency 1E6 Hz (1 MHz)

Input switched capacitor  $C_{SC1}=1\text{E-}9 \text{ F}$

Feedback capacitor (fixed)  $C=1\text{E-}9 \text{ F}$

Feedback switched capacitor:  $1\text{E-}10 \text{ F}$

Comment: The small spikes are due to clock pulses which are not completely isolated from each other.