

**Job Title: SerDes Digital Design**

Staff Engineer or Senior Staff Engineer position

**Job Descriptions:**

1. Participate in high-level product specifications, microarchitecture and implementation of high-speed memory interfaces
2. Perform RTL coding, LINT checking and sanity testing on implemented designs
3. Work with the verification team for lab debugging
4. Work with the software team and/or customers to solve problems, debug and tune system performance

**Job Requirements:**

1. Bachelor's degree in communications, electronic engineering or computer engineering, master's degree preferred
2. More than 6 years (staff engineer) and 10 years (senior staff engineer) of ASIC design experience, familiar with ASIC development process
3. Good Verilog HDL coding skills and EDA tools such as synthesis and timing analysis
4. Familiarity with high-speed interfaces such as DDR, SerDes, PCIe is preferred
5. Ability to solve customer problems and deliver results in a timely manner
6. Strong organizational and communication skills where English is a must and Chinese is preferred

## **Job Title: SerDes Analog Design**

### **Job Descriptions:**

1. Participate in high-level product specifications, microarchitecture and implementation of high-speed memory interfaces
2. Responsible for delivery of AMS circuits in SerDes, including the evaluation of different circuit topologies
3. Worked on high-speed serial and parallel interface design, defining specifications and implementing modules such as bandgap, bias circuits, LDO regulators, amplifiers, comparators, switched capacitor circuits, ADCs, DACs, oscillators, filters, Tx/Rx equalization techniques and circuits such as deemphasis, CTLE, DFE, CDR architecture and implementation, high-speed digital circuits (serializers, deserializers, counters, dividers, etc.)
4. Work with the verification team for lab debugging
5. Work with the software team and/or customers to solve problems, debug and tune system performance

### **Job Requirements:**

1. Bachelor's degree in communications, electronic engineering or computer engineering, master's degree preferred
2. More than 4 years (senior engineer) and 6 years (staff engineer) of ASIC design experience, familiar with ASIC development process
3. Good AMS circuit design skills and EDA tools such as Cadence Virtuoso, Calibre, Hspice/Spectre, and static timing analysis tools (e.g., Nanotime)
4. Familiarity with high-speed interfaces such as DDR, SerDes, PCIe is preferred
5. Ability to solve customer problems and deliver results in a timely manner
6. Strong organizational and communication skills where English is a must and Chinese is preferred

**Job Title: Senior/Manager, SerDes Analog Design**

**Job Descriptions:**

1. Lead the team to complete the design, simulation and design document writing of analog circuits and chips
2. Guide the layout and routing of the layout to improve the efficiency and quality of layout design
3. Assist in product testing, debugging and application
4. Guide the work of junior analog designers and write detailed design documents

**Job Requirements:**

1. Master's degree or above in microelectronics or electronic engineering with more than 10 years of working experience
2. Solid understanding and experience in designing analog mixed-signal circuit blocks, including bandgap, bias circuits, LDO regulators, amplifiers, comparators, switched capacitor circuits, ADCs, DACs, oscillators, filters, Tx/Rx equalization techniques and circuits such as de-emphasis, CTLE, DFE, CDR architecture and implementation, high-speed digital circuits (serializers, deserializers, counters, dividers, etc.)
3. Knowledge of common high-speed interface protocols (such as PCIe, USB, SATA, DDR, and LPDDR, etc.) is required
4. In-depth understanding of analog mixed-signal design, experience with highspeed serial and parallel circuits in advanced processes down to 3nm, FinFet, SOI, and bulk CMOS
5. Extensive experience using Cadence Virtuoso, Calibre, Hspice/Spectre, and static timing analysis tools (e.g., Nanotime)
6. Excellent ability to solve customer problems and deliver results in a timely manner
7. Experience in team building and project management
8. Strong organizational and communication skills where English is a must and Chinese is preferred