

INPUT

P1

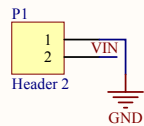
1
2

Header 2

VIN

GND

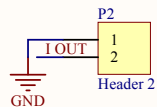
0.5 TO 2V



0.5 TO 2V

OUTPUT CURRENT

The diagram shows a component labeled 'P2' with two pins. Pin 1 is connected to a ground symbol labeled 'GND' via a blue wire labeled 'I OUT'. Pin 2 is labeled 'Header 2'.



Title		
Size A4	Number	Revision
Date:	8/26/2014	Sheet of
File:	D:\PCB DESIGN\1\SAMPLE.SchDoc	Drawn By:

Comment	Description	Designator	Footprint	LibRef	Quantity
0.1uF	Capacitor	C1, C4	CAP-0805	Cap	3
470nF	Capacitor	C2	CAP-0805	Cap	2
10nF	Capacitor	C3	CAP-0805	Cap	2
Header 2	Header, 2-Pin	P1, P2	TERMINAL 2	Header 2	4
Header 2	Header, 2-Pin	P3	HDR1X2	Header 2	2
PNP	PNP Bipolar Transistor	Q1	SOT-23B_N	PNP	2
MOSFET-P	P-Channel MOSFET	Q2	TO-92B	MOSFET-P	2
MOSFET-P	P-Channel MOSFET	Q3	SOT23	MOSFET-P	1
1K	Resistor	R1	R-0805	Res2	2
5.6K	Resistor	R2	R-0805	Res2	1
8.2K	Resistor	R3	R-0805	Res2	1
978.1E	Resistor	R4	AXIAL-0.4	Res2	1
978.1K	Resistor	R4	AXIAL-0.4	Res2	1
15E	Resistor	R5, R7	R-0805	Res2	4
22.56K	Resistor	R6	R-0805	Res2	2
100E	Resistor	R8	R-0805	Res2	1
978.1E	Resistor	R8	R-0805	Res2	2
XTR11		U1	MSOP-10	XTR111	2