

Conditdion: No bias and termination in all modules except Master.											
Idle mode				Transmit mode				Receive mode			
Current(mA)		Voltage(V)		Current(mA)		Voltage(V)		Current(mA)		Voltage(V)	
D+	1.2	D+/DG	1.85	D+	1.2	D+/DG	1.8	D+	3.6	D+/DG	1.9
D-	1.2	D-/DG	1.35	D-	1.2	D-/DG	1.5	D-	-2.0	D-/DG	2
DG	-2.25	D+/D-	0.485	DG	-2.45	D+/D-	0.3	DG	-1.8 ~ -2.25	D+/D-	0.012~0.04

Conditdion: End Module termination .No bias in all modules except Master											
Idle mode				Transmit mode				Receive mode			
Current(mA)		Voltage(V)		Current(mA)		Voltage(V)		Current(mA)		Voltage(V)	
D+	2.3	D+/DG	1.85	D+	2.5	D+/DG	1.9	D+	4.5	D+/DG	1.9
D-	0	D-/DG	1.58	D-	0.03	D-/DG	1.6	D-	2.57	D-/DG	1.6
DG	-2.36	D+/D-	0.15	DG	-2.46	D+/D-	0.15	DG	-2.4 ~ -1.4	D+/D-	0.09

Conditdion: End Module termination and Biased. No bias and Termination in all modules except Master											
Idle mode				Transmit mode				Receive mode			
Current(mA)		Voltage(V)		Current(mA)		Voltage(V)		Current(mA)		Voltage(V)	
D+	0.7	D+/DG	2.19	D+	-0.1	D+/DG	2.2	D+	2.2	D+/DG	2.2
D-	0.6	D-/DG	1.7	D-	1.7	D-/DG	1.9	D-	-0.9	D-/DG	1.9
DG	-1.34	D+/D-	0.48	DG	-1.5	D+/D-	0.375	DG	-1.17	D+/D-	0.05

Conditdion: End Module Biased. No bias and Termination in all modules except Master											
Idle mode				Transmit mode				Receive mode			
Current(mA)		Voltage(V)		Current(mA)		Voltage(V)		Current(mA)		Voltage(V)	
D+	-2.7	D+/DG	2.4	D+	-2.8	D+/DG	2.3	D+	-0.01	D+/DG	2.3
D-	4.02	D-/DG	1.5	D-	4.4	D-/DG	2	D-	0.88	D-/DG	2
DG	-1.24	D+/D-	0.9	DG	-1.5	D+/D-	0.69	DG	-1.13	D+/D-	0.2