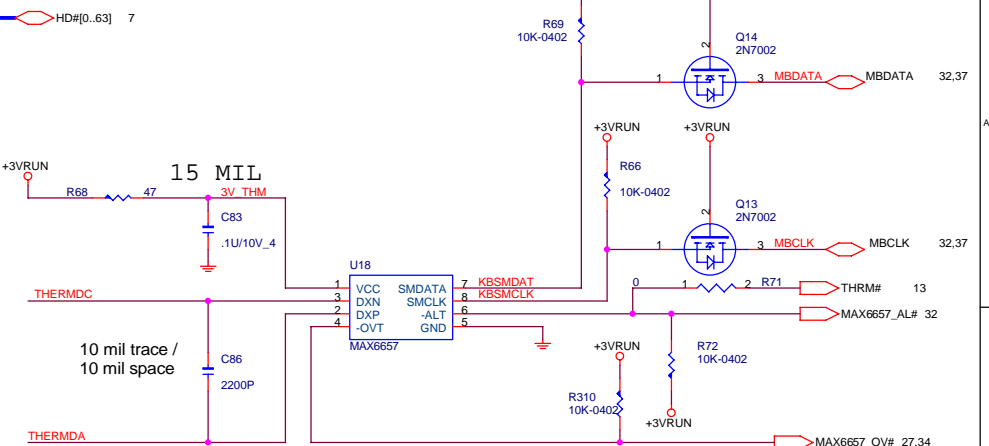
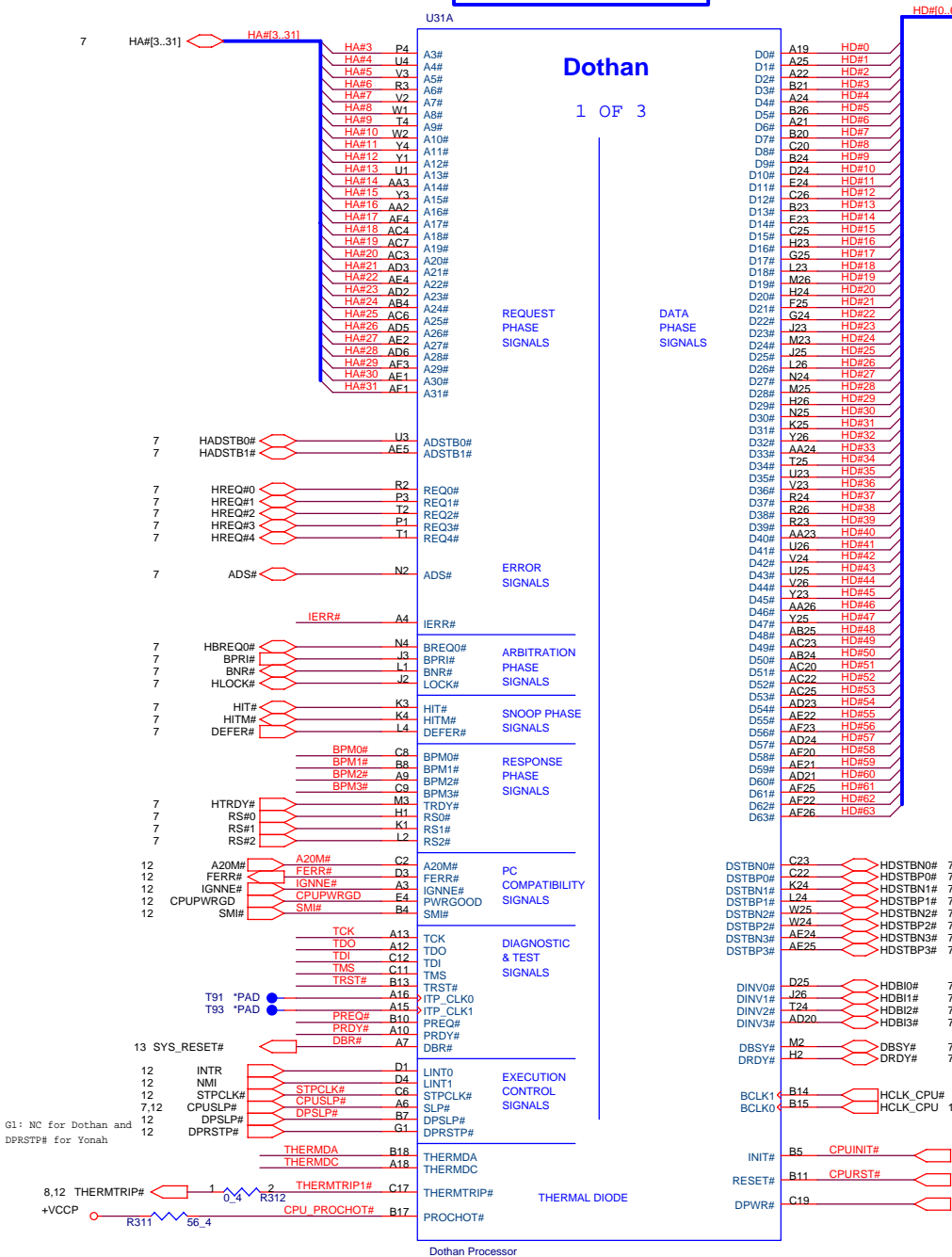


CT_0505: Change footprint to BGA479M-SOCKET from L100505 from MPGA479M

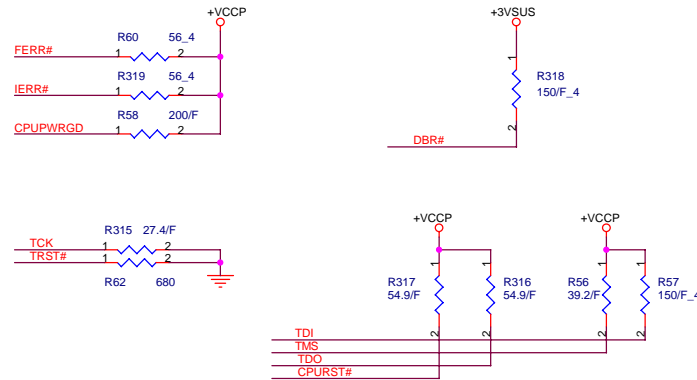
Dothan

1 OF 3

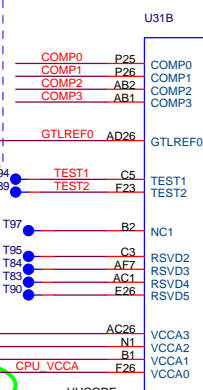
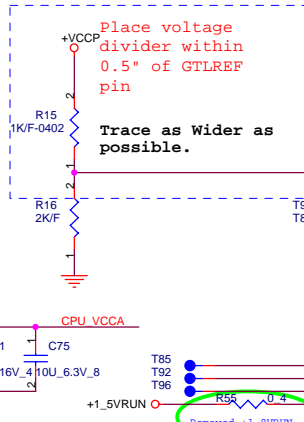
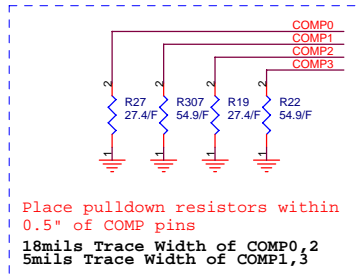


ITP disable guidelines			
Signal	Resistor Value	Connect To	Resistor Placement
TDI	150 ohm +/- 5%	VTT	Within 2.0" of the CPU
TMS	39 ohm +/- 5%	VTT	Within 2.0" of the CPU
TRST#	680 ohm +/- 5%	GND	Within 2.0" of the CPU
TCK	27 ohm +/- 5%	GND	Within 2.0" of the CPU
TDO	Open	VTT	Within 2.0" of the CPU

Note: Populate R58, R62 when ITP connector is populated.

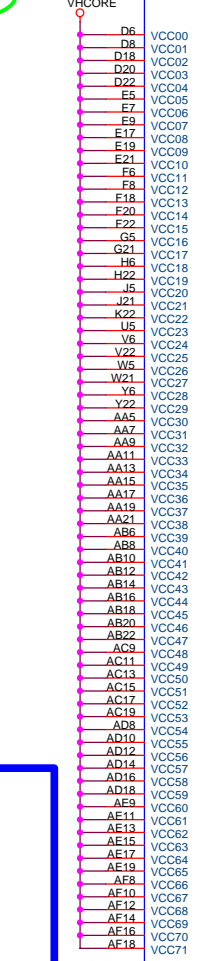


PROJECT : ED2L
Quanta Computer Inc.

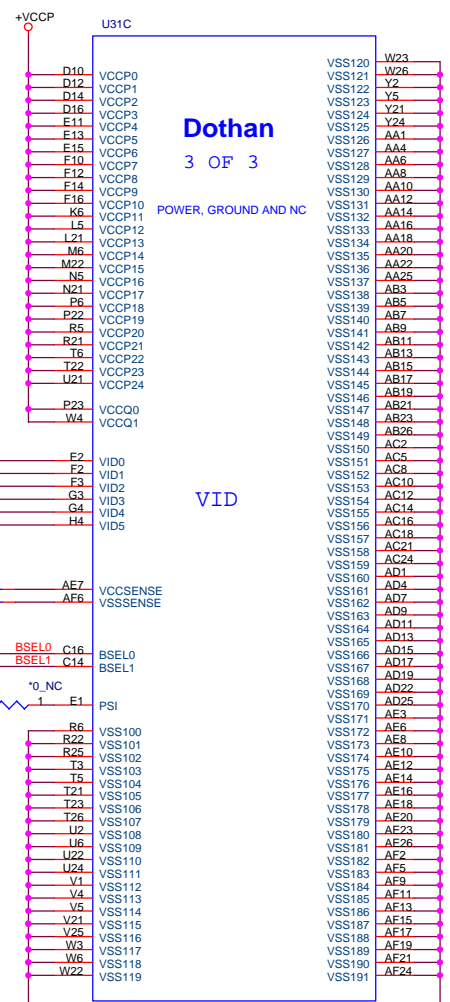


Dothan
2 OF 3

POWER, GROUND, RESERVED SIGNALS

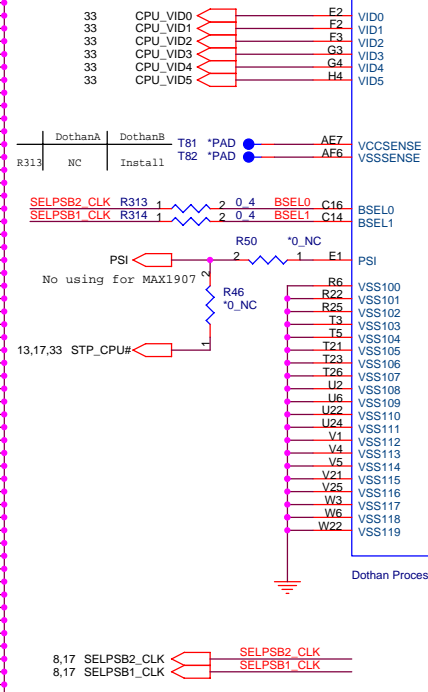
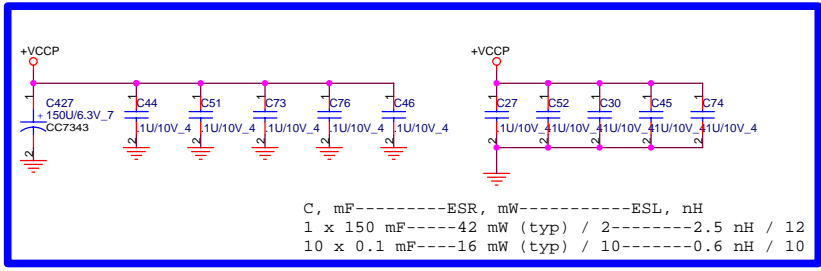
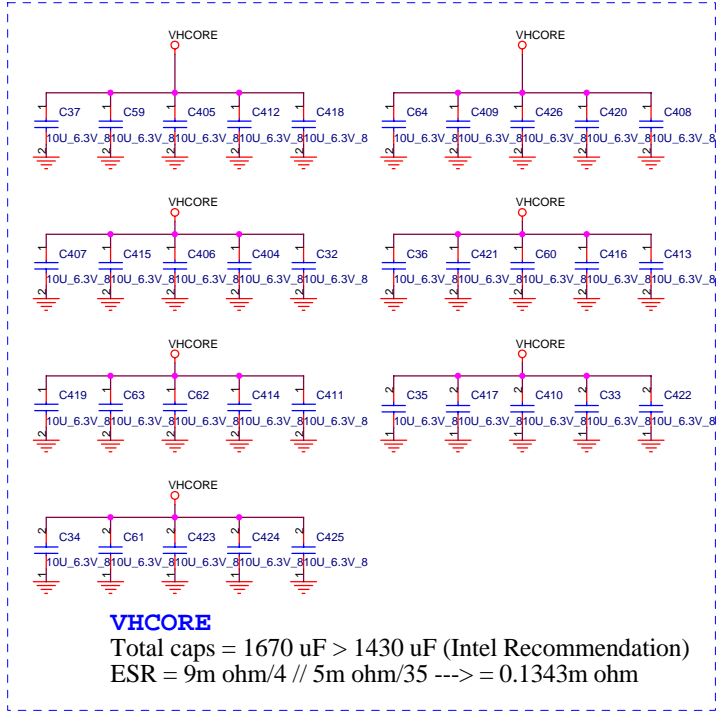


Dothan Processor

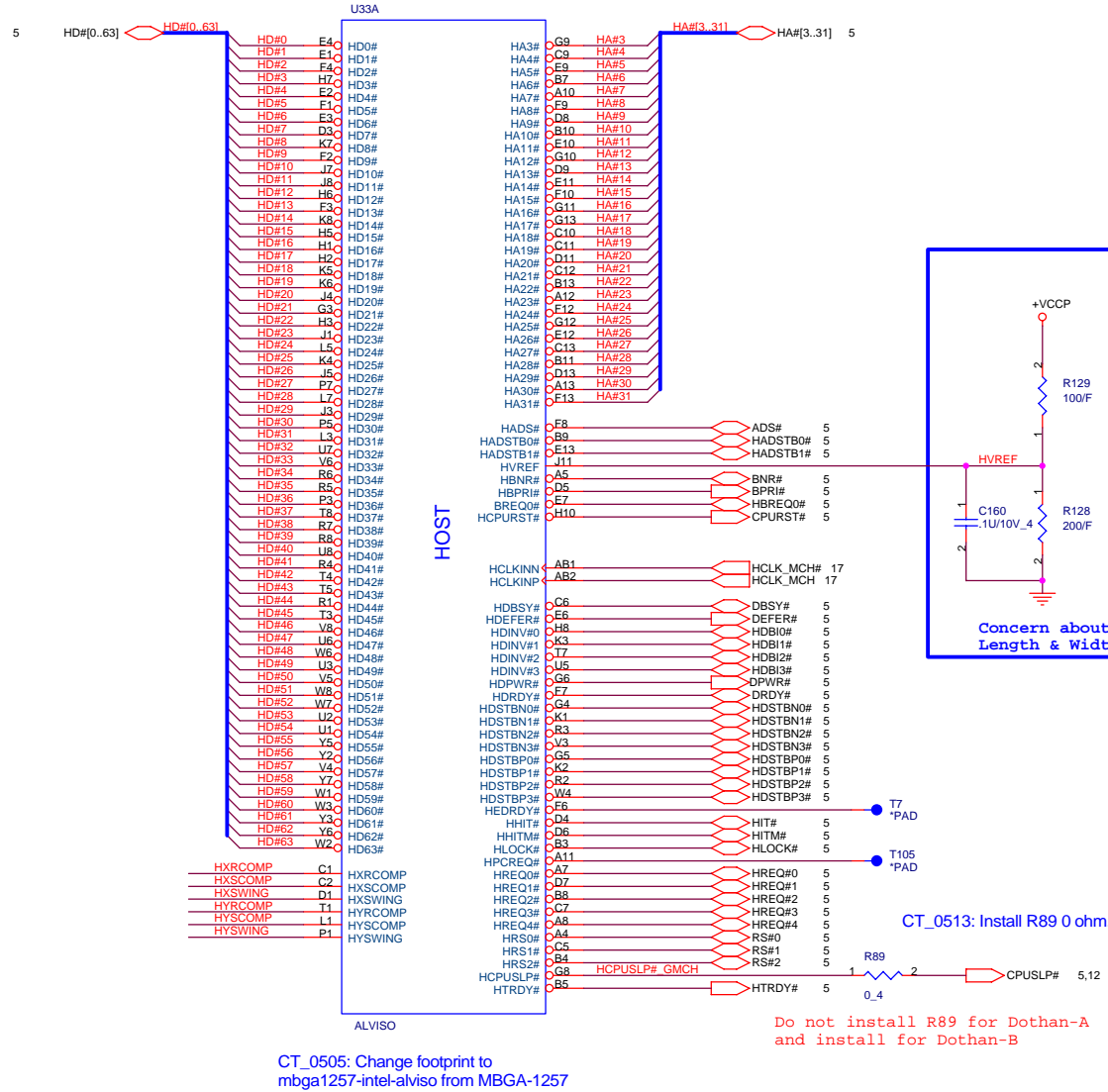
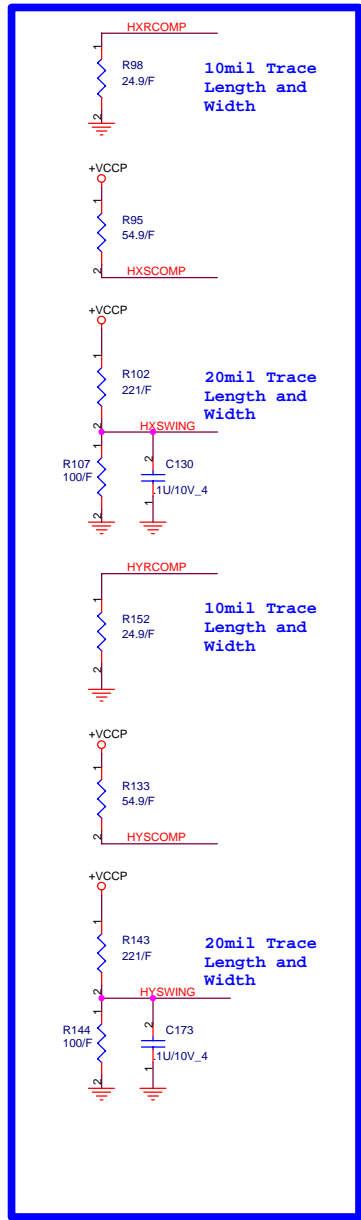


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	Dothan (Power)	E3A
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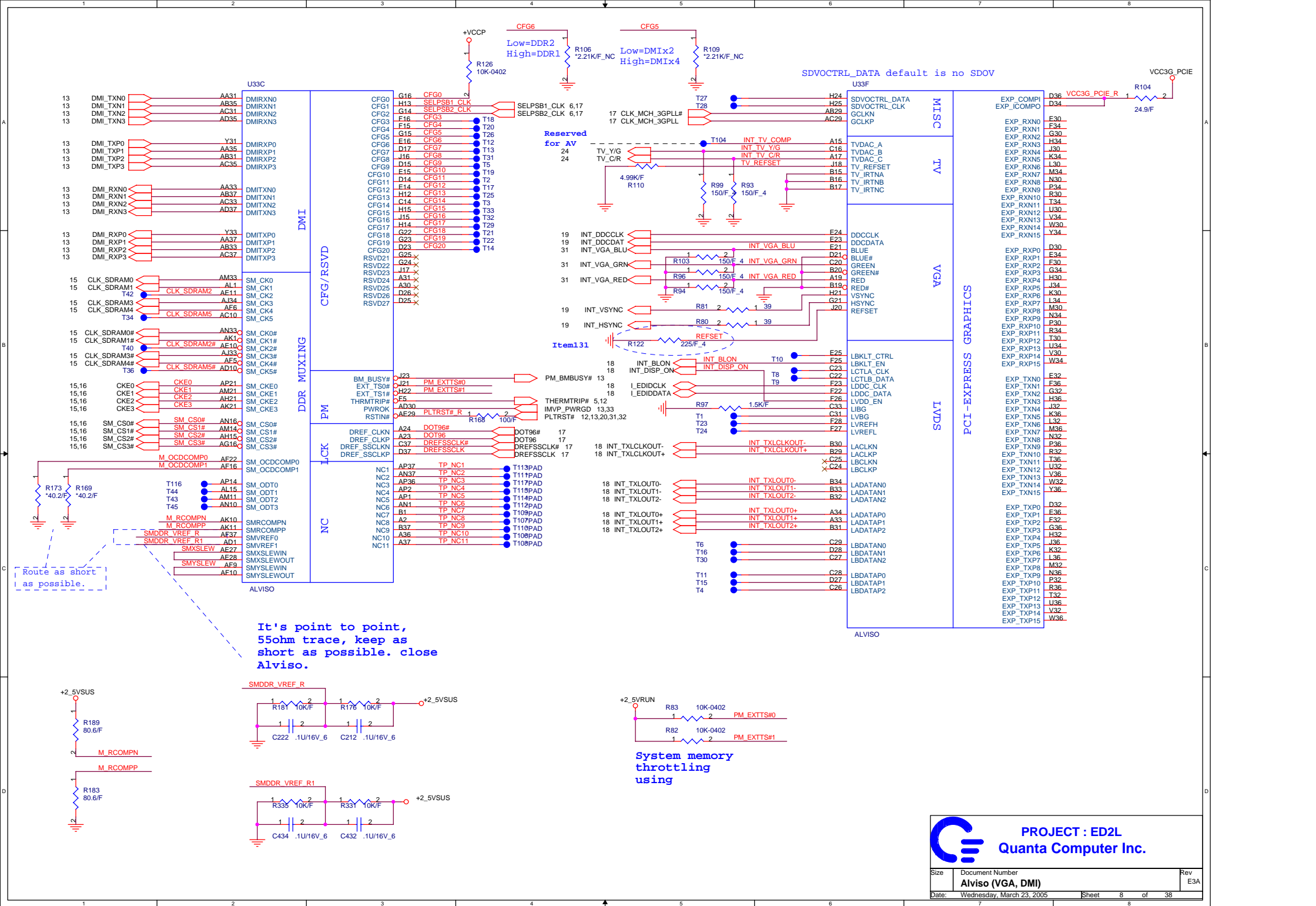


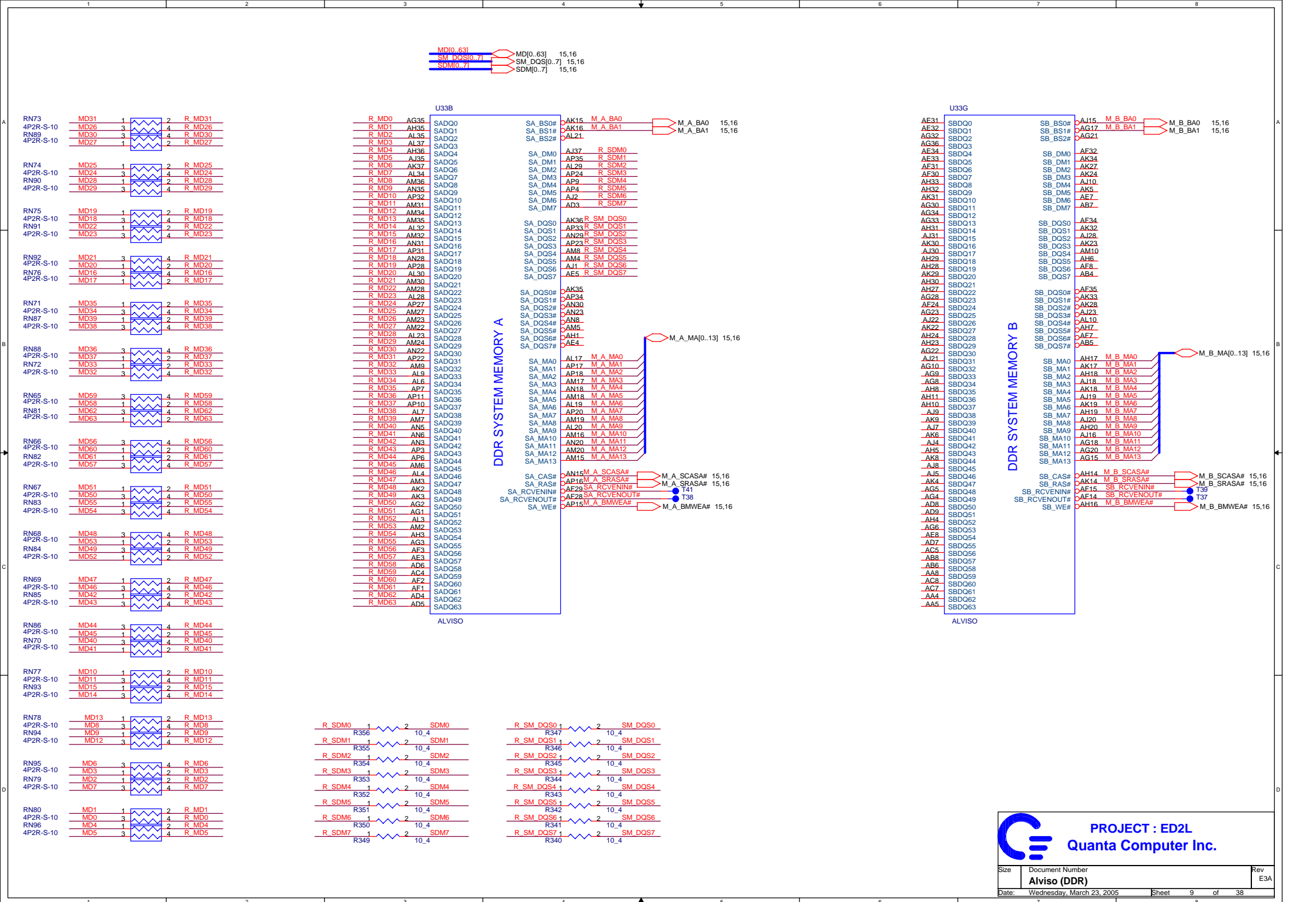
Dothan Processor



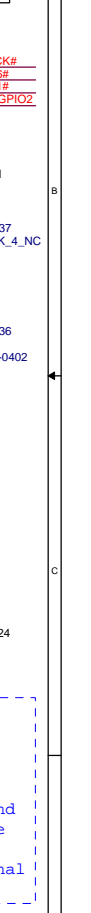
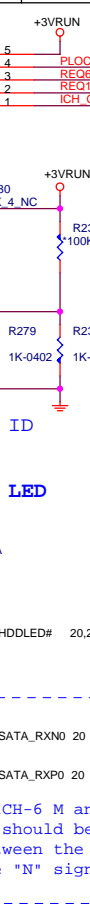
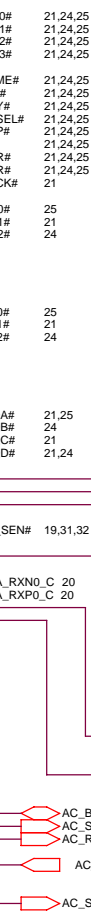
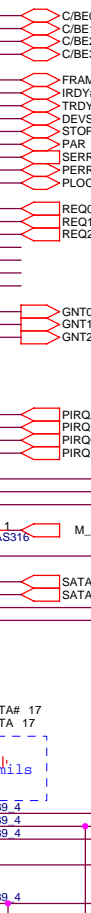
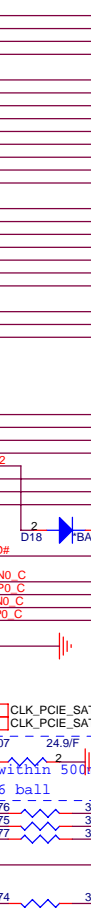
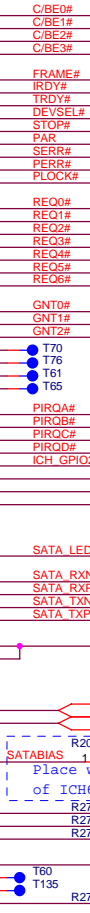
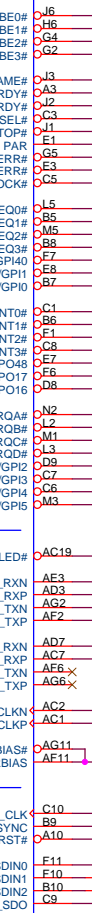
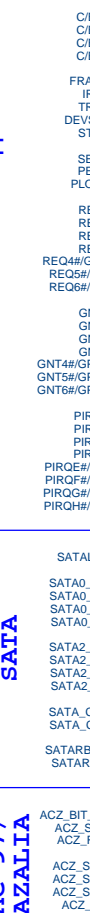
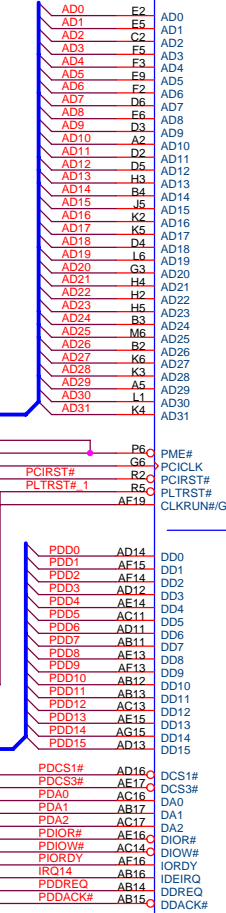
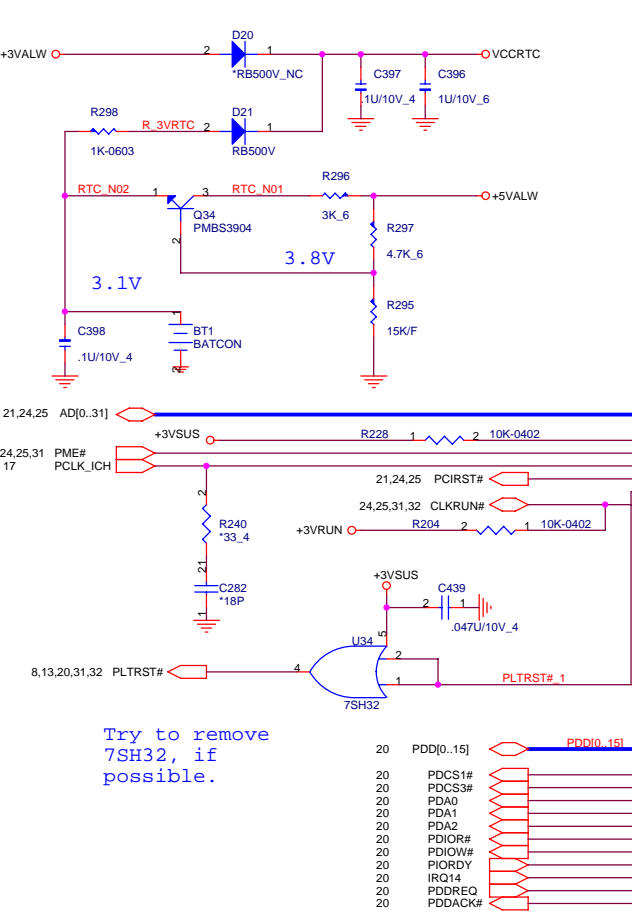
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Quanta Computer Inc.

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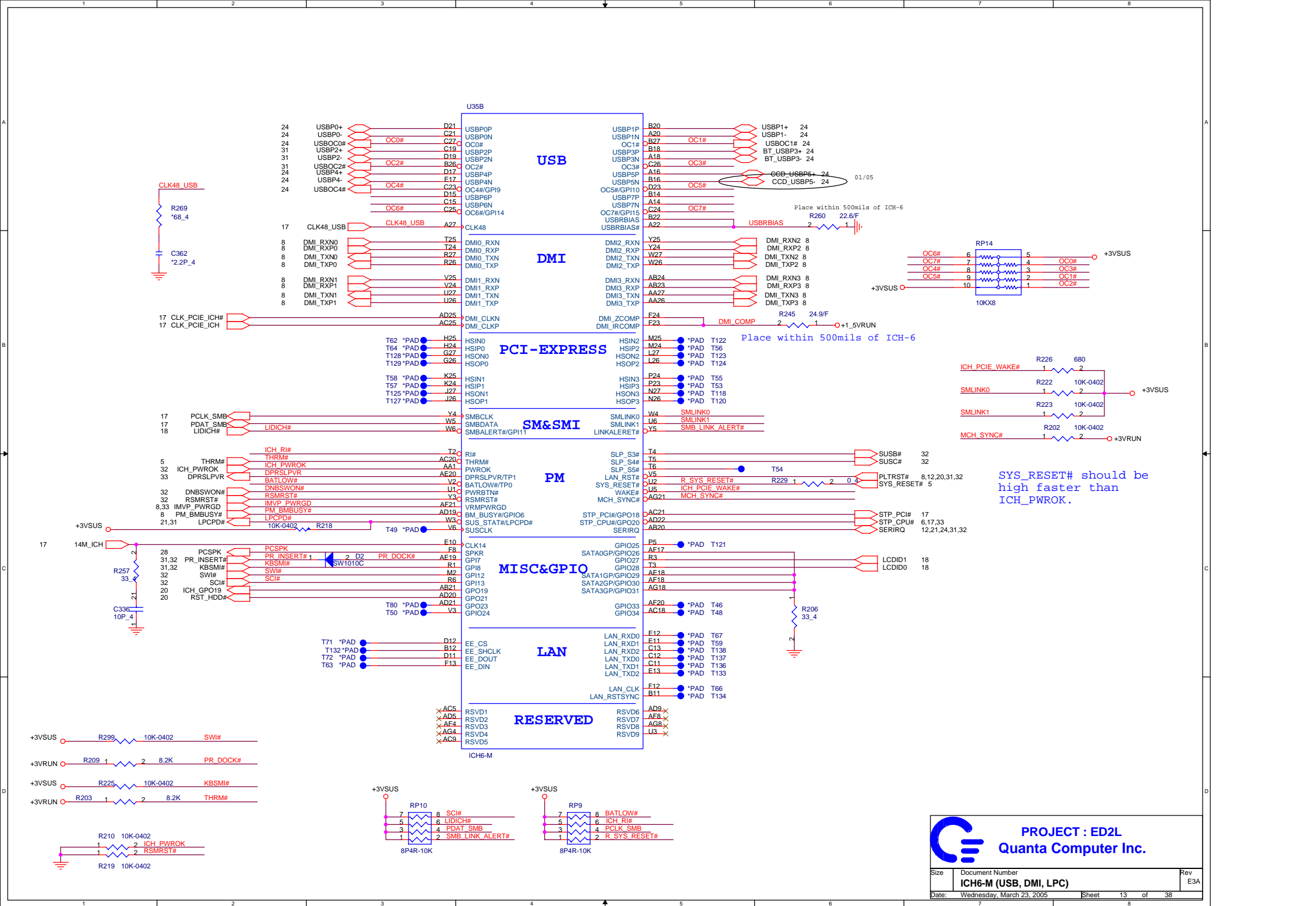


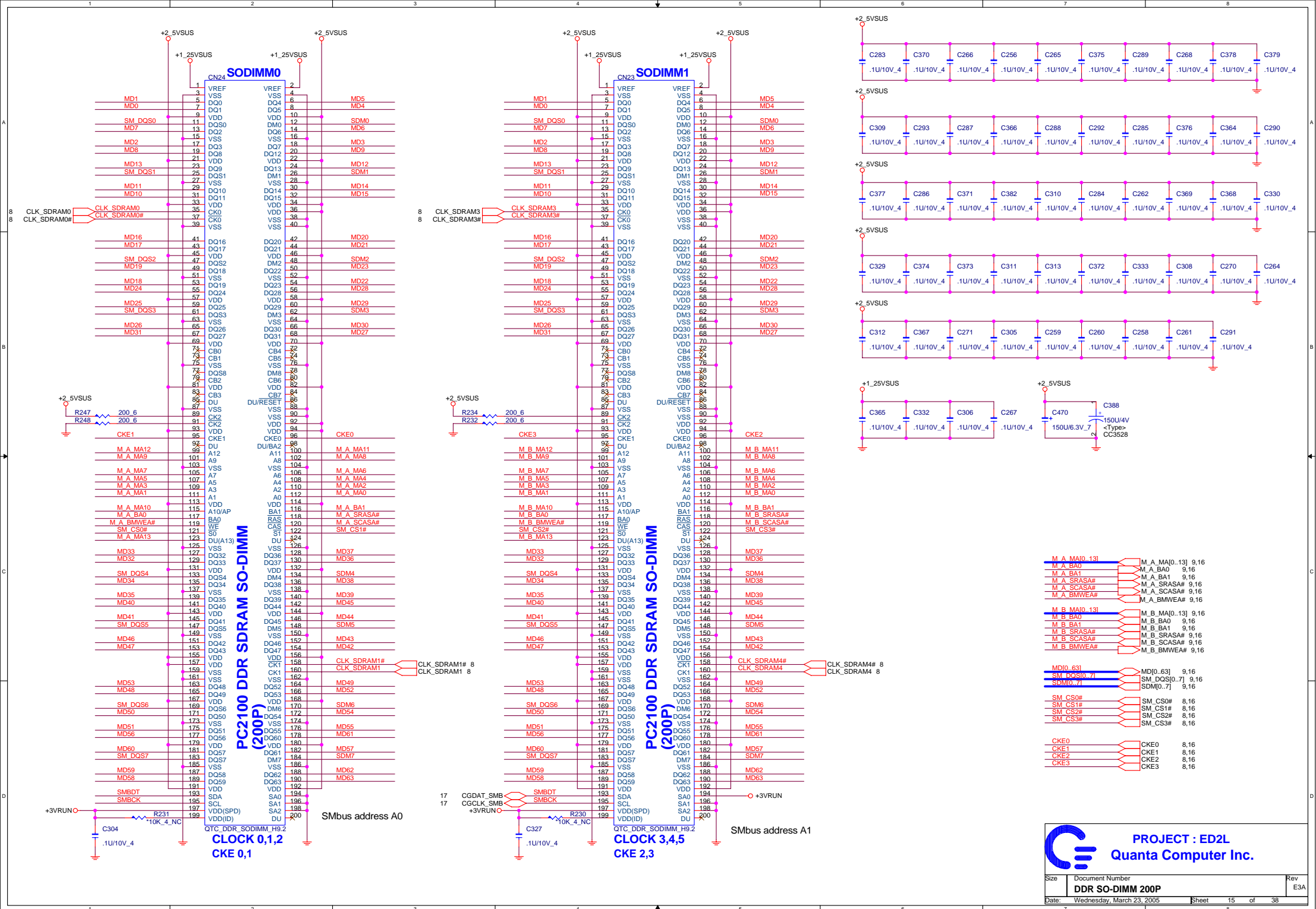
RTC



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Quanta Computer Inc.

Size	Document Number	Rev
	ICH6-M (CPU, PCI, IDE, SATA, AC97)	E3A
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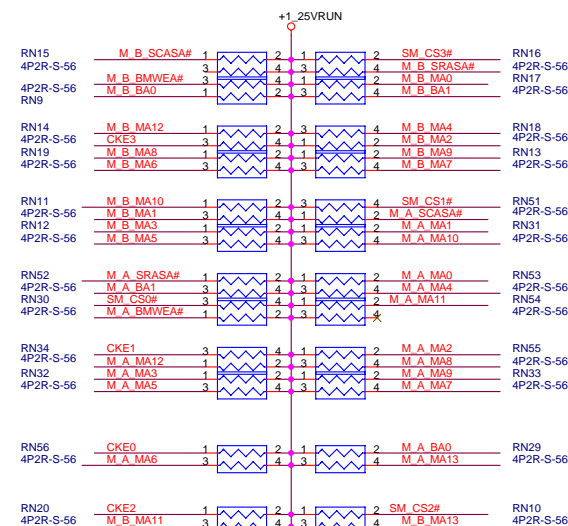
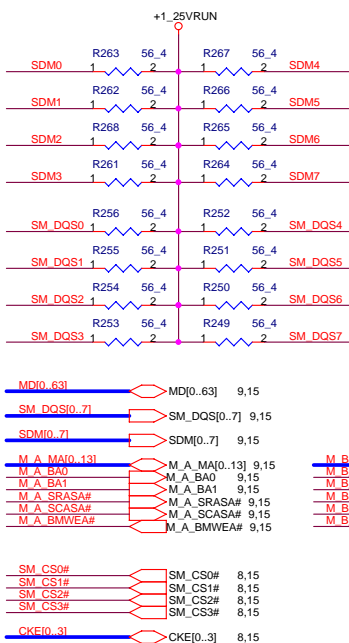
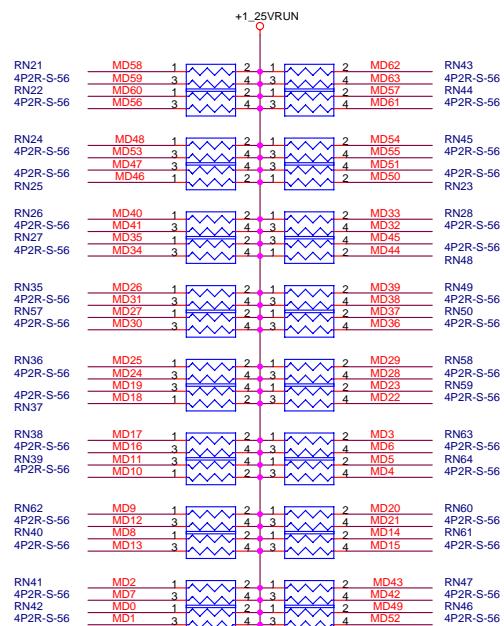
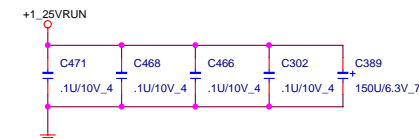




For terminal R-pack.

19 PCS

19 PCS



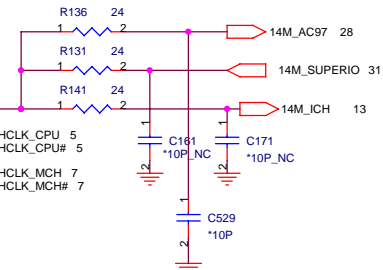
PROJECT : ED2L
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Size	Document Number DDR TERMINATION	Rev E3A
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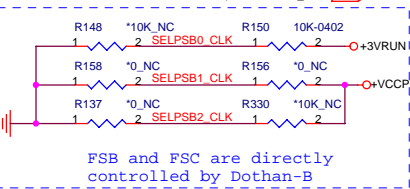
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

	DothanA	DothanB
R137	Install	NC
R330	NC	NC

Place these termination to close CK410M. Cause those Pin-out is for Current-Mode.



SMBus address D2



CK-410M

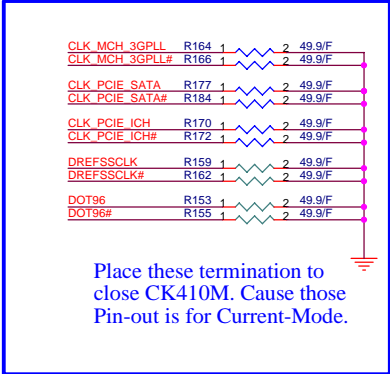
CT_0505: Change footprint to TSSOP56-8_1-5 from TSSOP56-240

Tie to VCC (Logic 1) is for ITP using.
Tie to GND (Logic 0) is for PCIE using.

CK-410M PIN 35,36 Strap Pin. for ITP or PCIE using.

These are for backdrive issue

CT_0229: Change MOS to RHU002N06 due to layout concern.

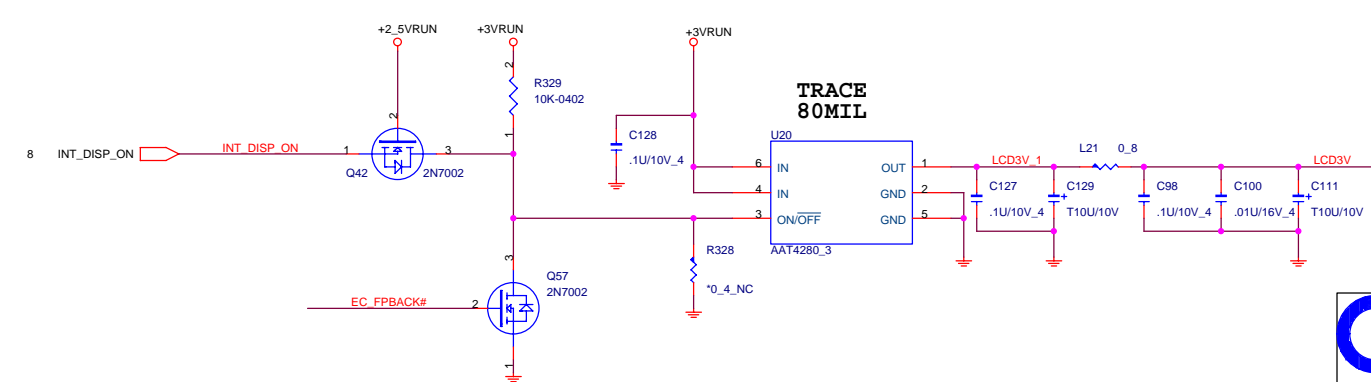
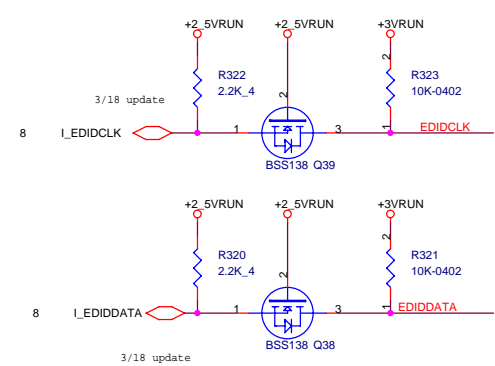
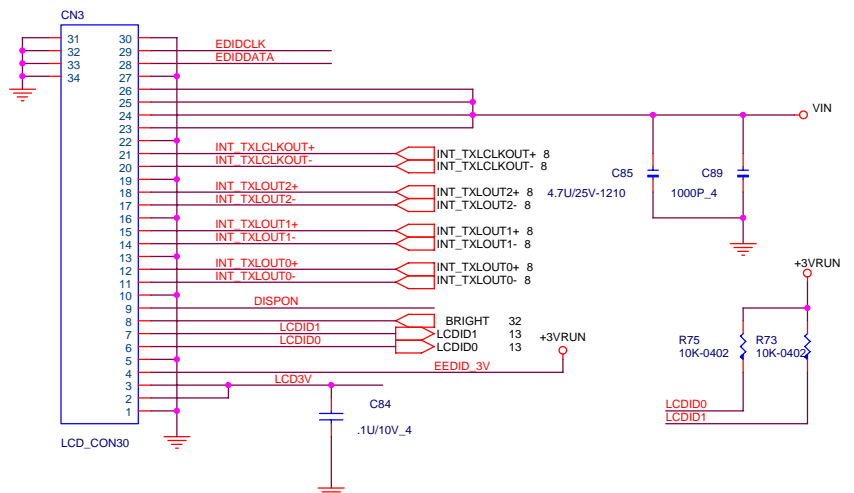
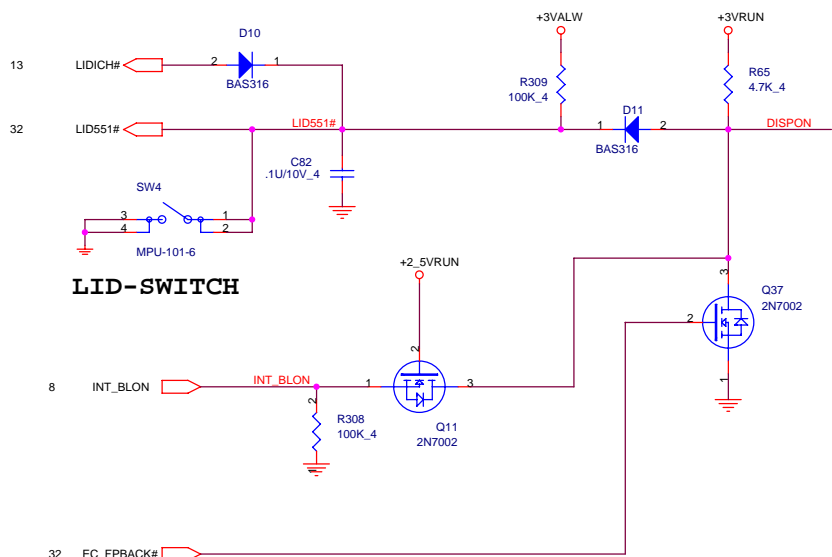


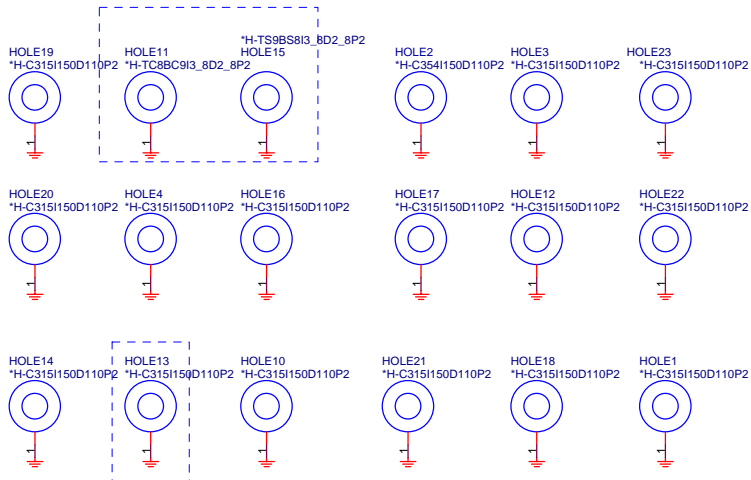
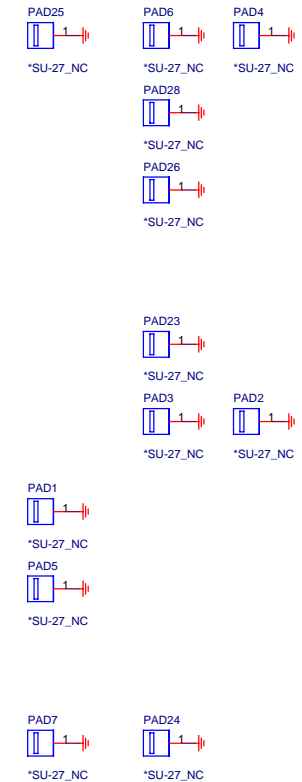
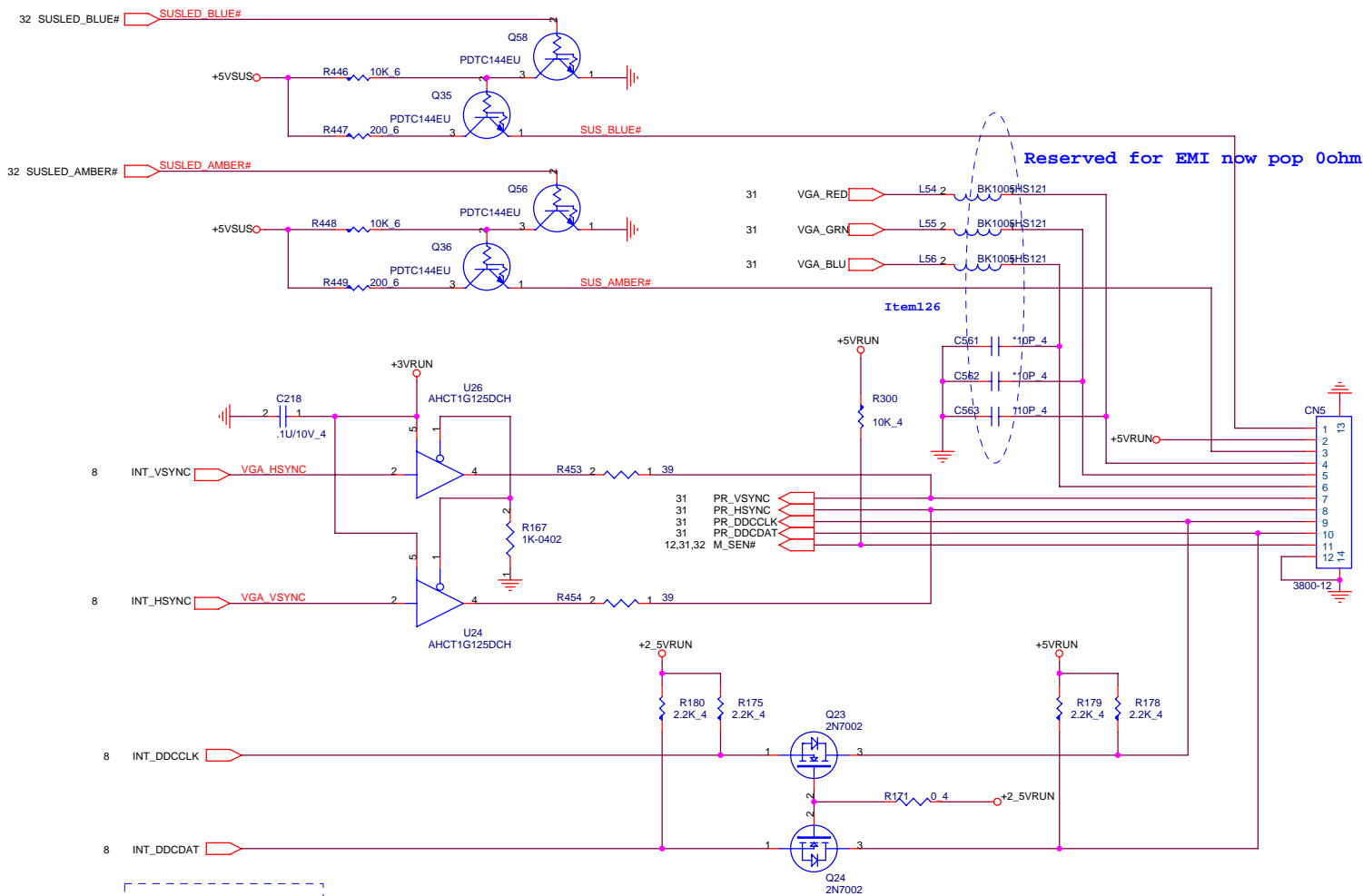
Place these termination to close CK410M. Cause those Pin-out is for Current-Mode.

Bypass CAPs need to follow Bypass CAP. Routing Rule, no vias between CAP to CHIPSET VCC Pin or GND.

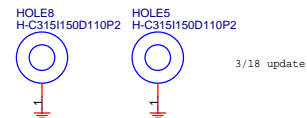


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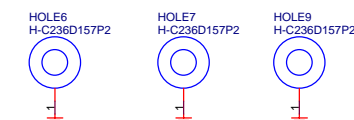




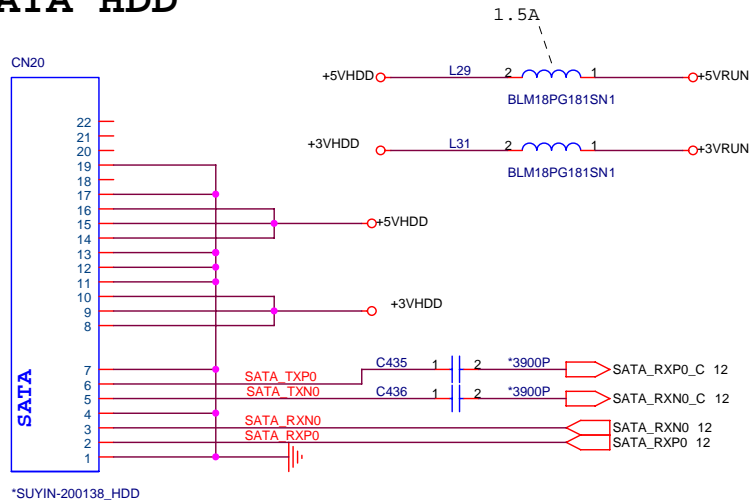
TV-Board fixing Nut



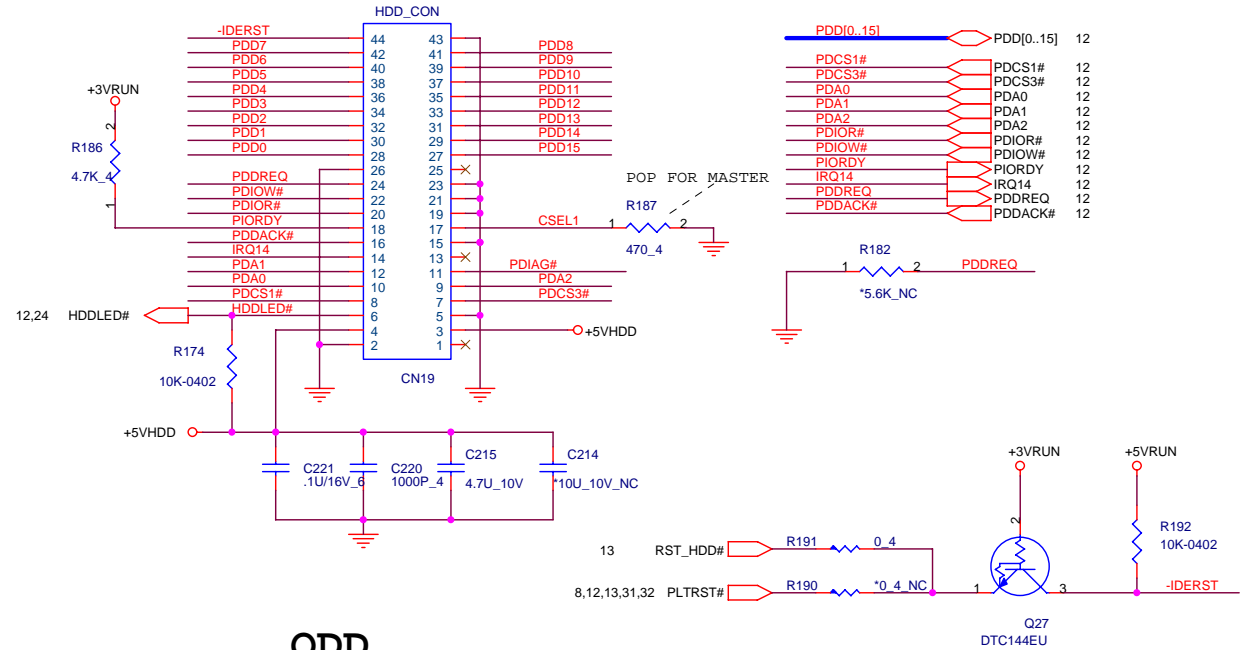
CPU SOCKET



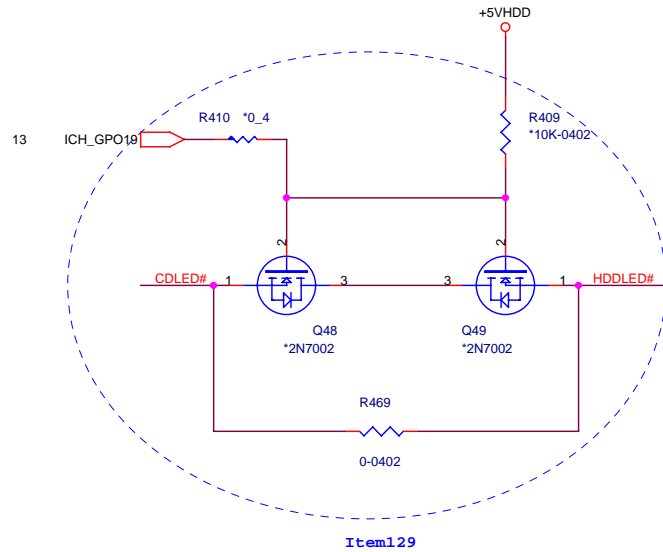
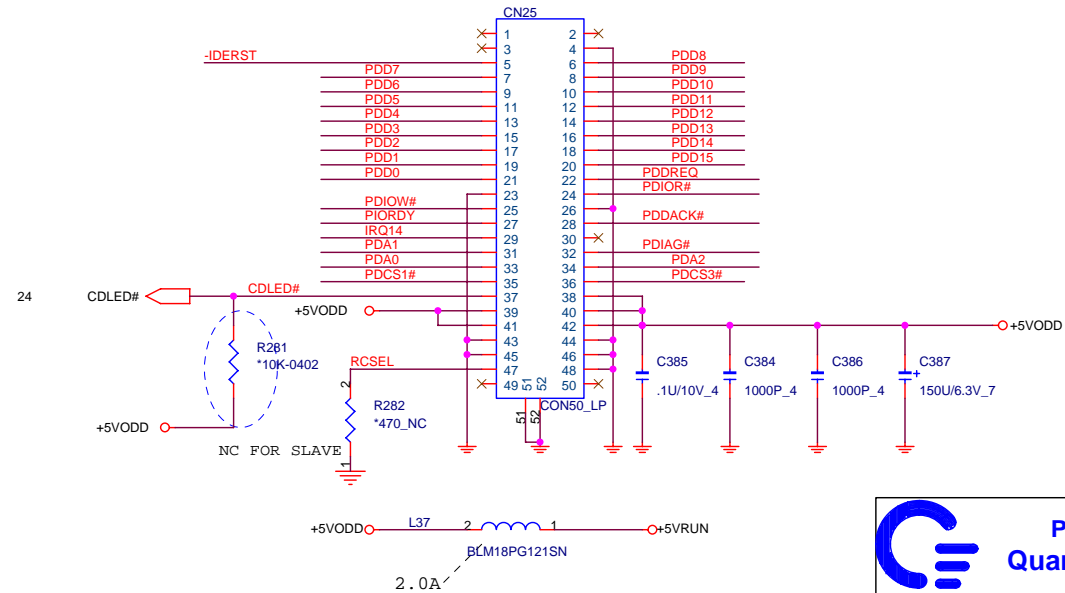
SATA HDD

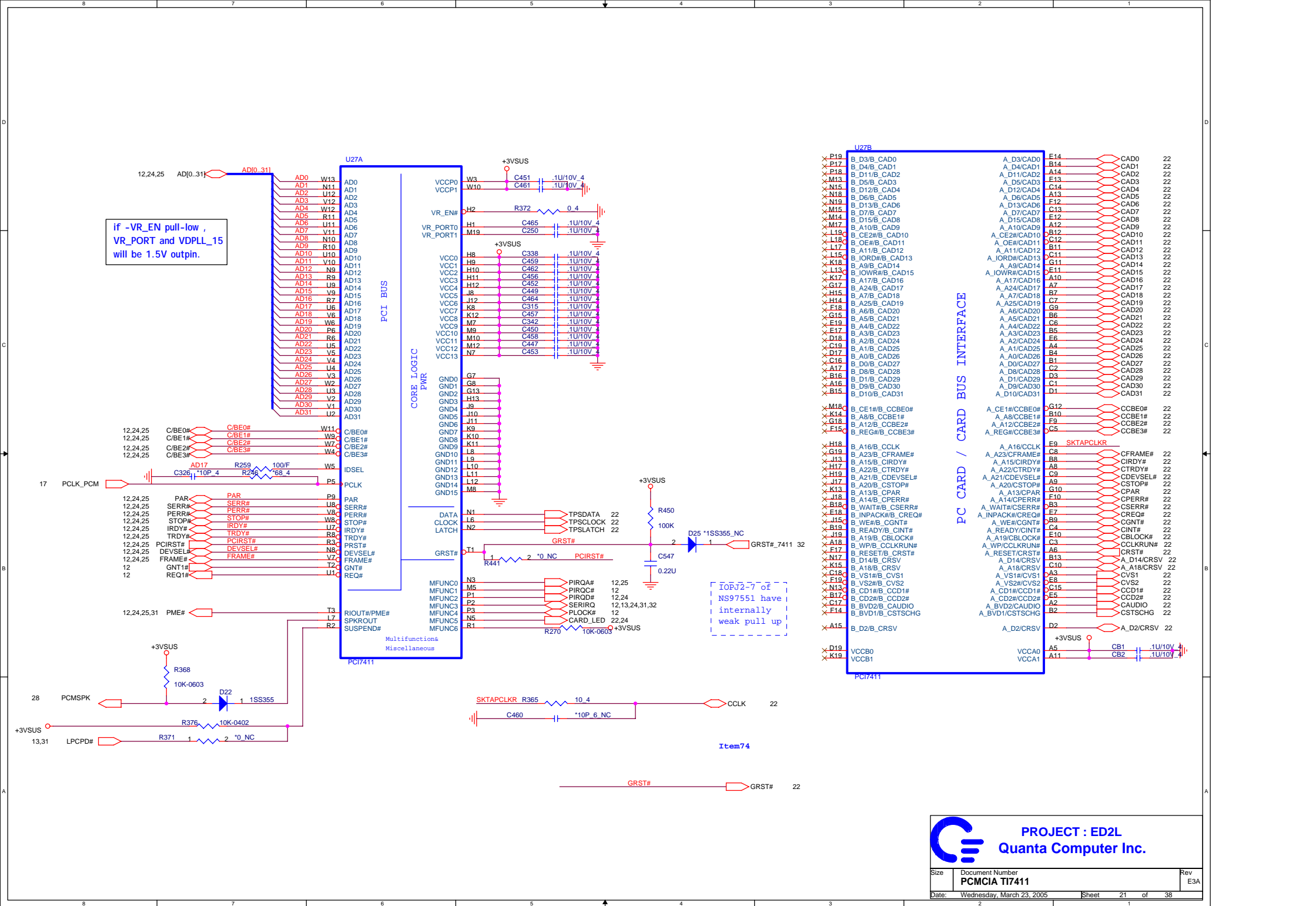


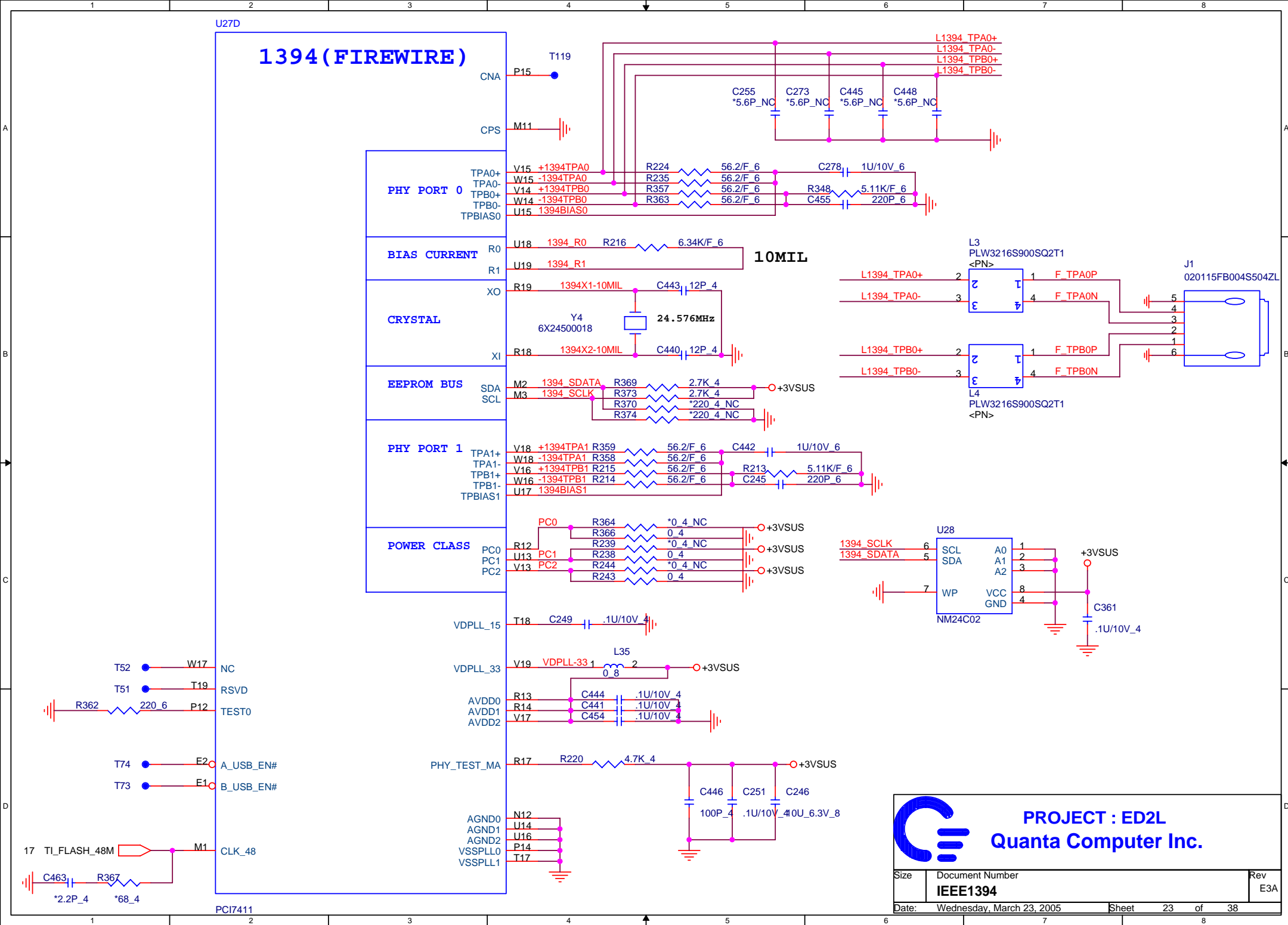
PATA HDD

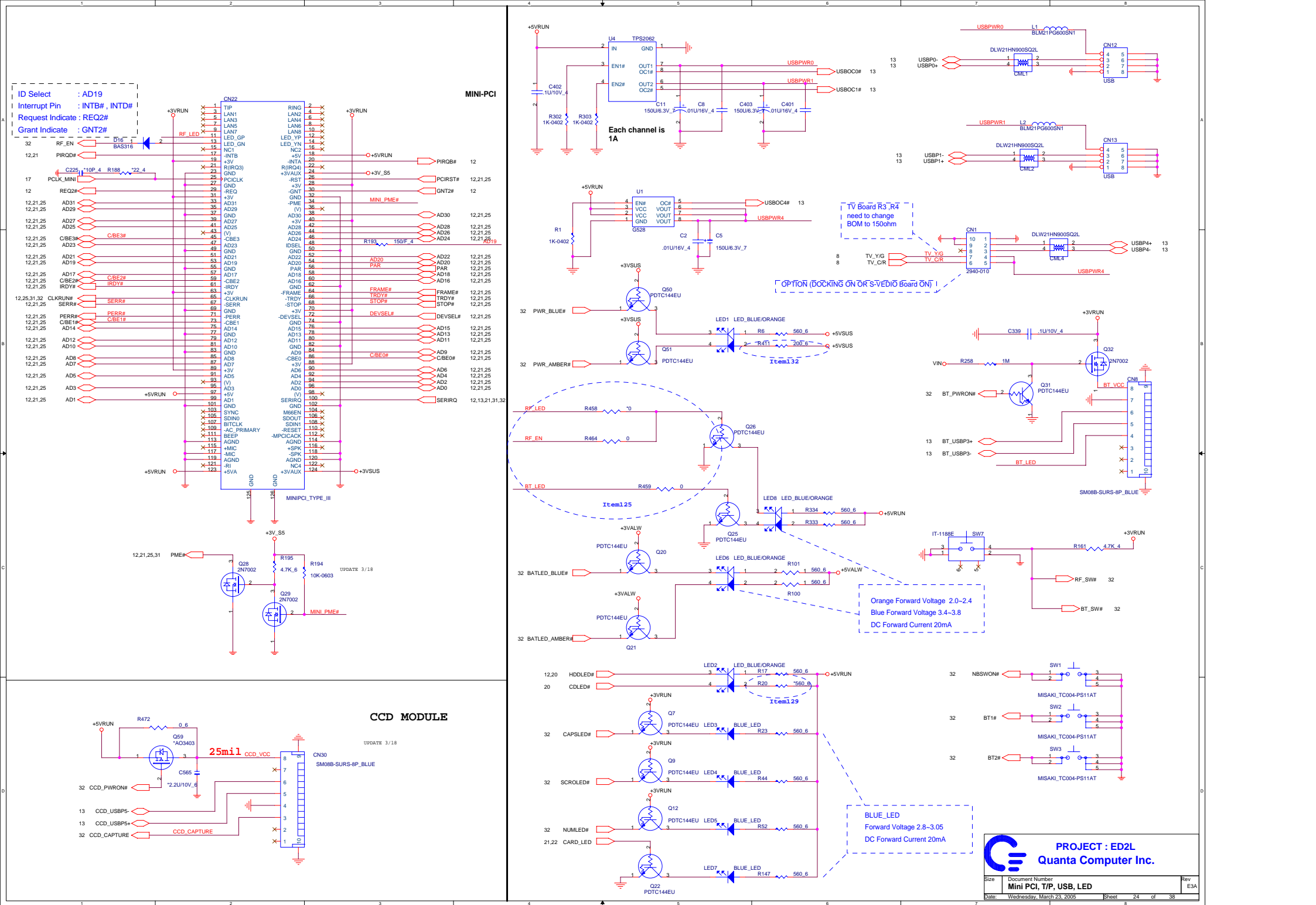


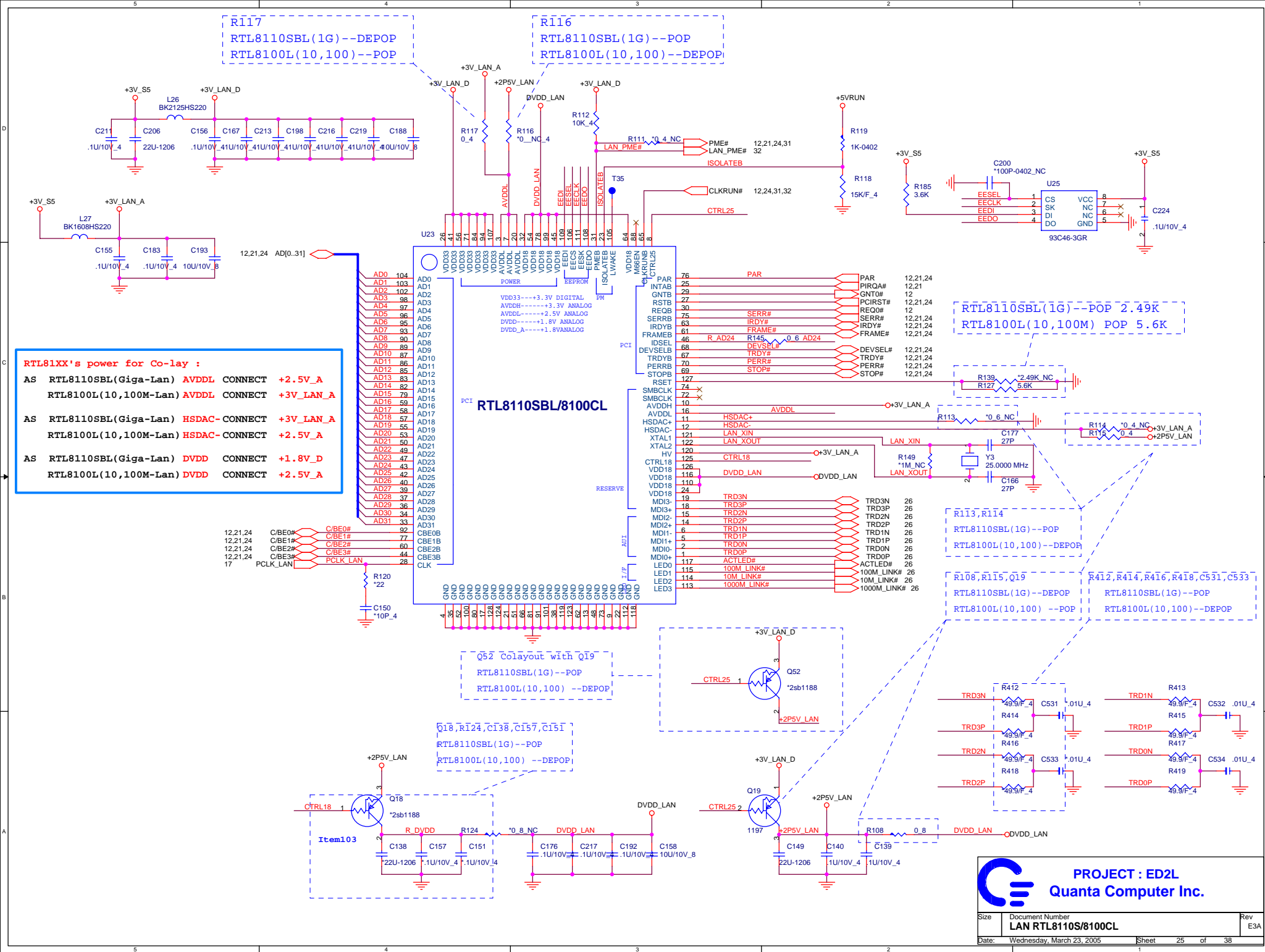
ODD

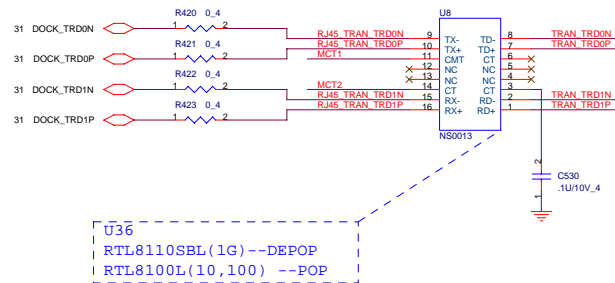
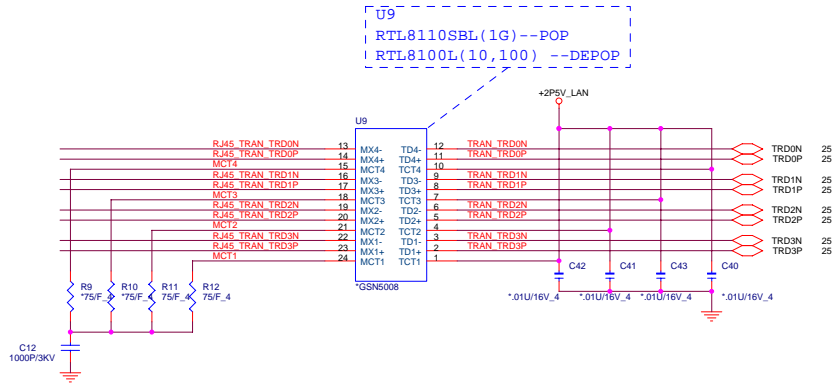
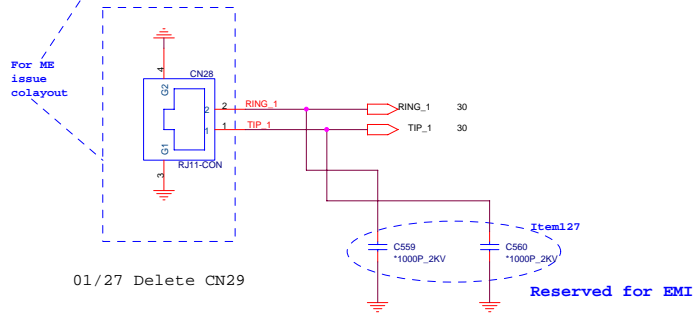
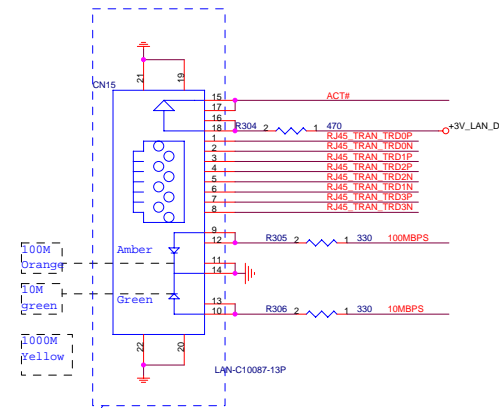




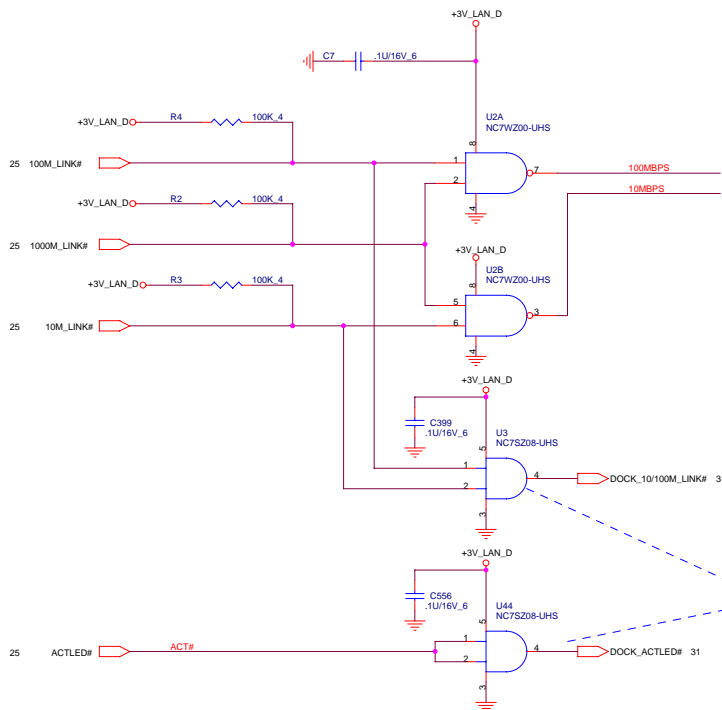






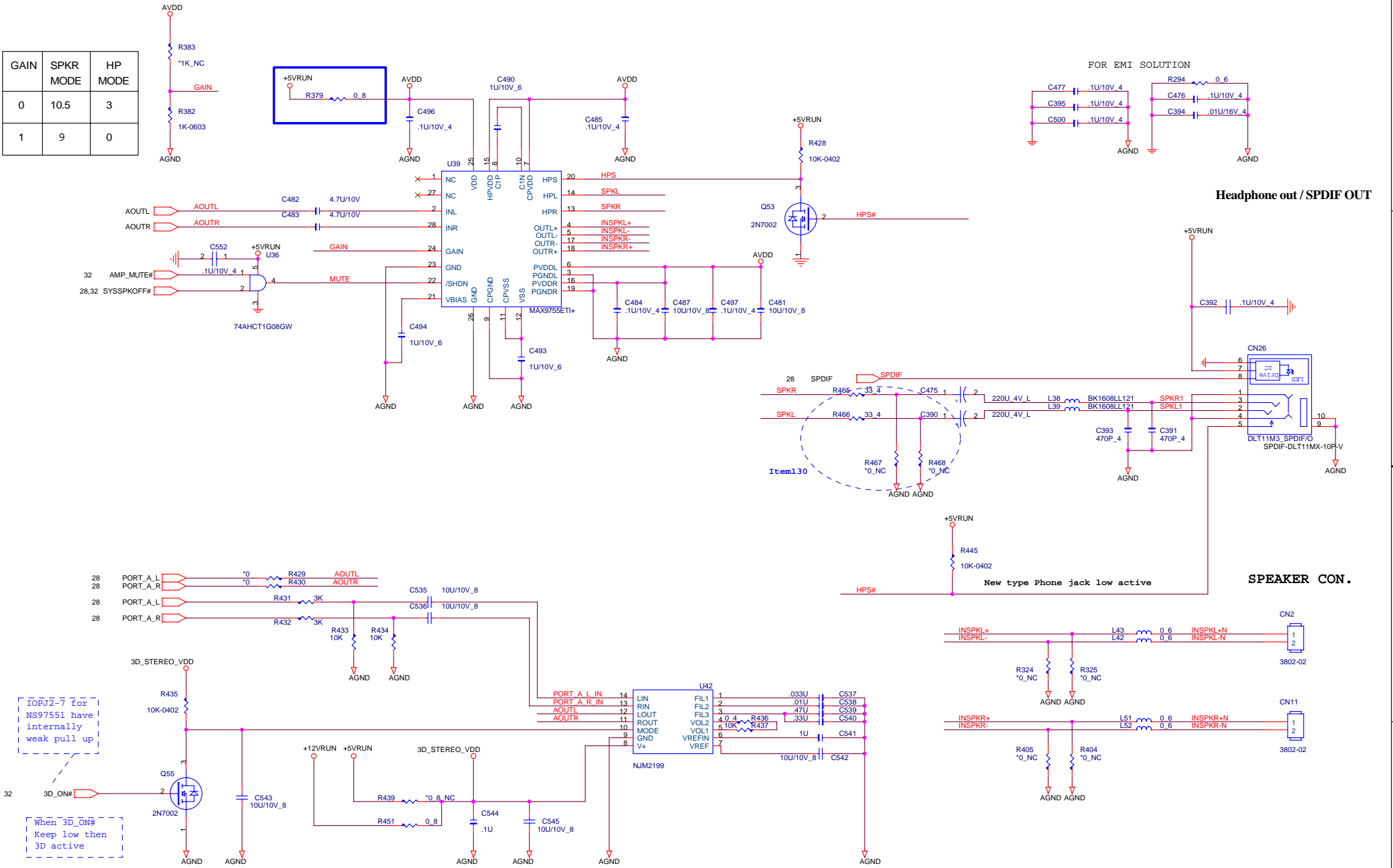


	For RTL8100C	For RTL810SB
R116	UN-POP	POP
R117	POP	UN-POP
R139	UN-POP	POP
R127	POP	UN-POP
R113	UN-POP	POP
R114	UN-POP	POP
R115	POP	UN-POP
R108	POP	UN-POP
R124	UN-POP	POP
C151	UN-POP	POP
C138	UN-POP	POP
C157	UN-POP	POP
Q52	UN-POP	POP
Q18	UN-POP	POP
Q19	POP	UN-POP
R412	UN-POP	POP
R414	UN-POP	POP
R416	UN-POP	POP
R418	UN-POP	POP
C531	UN-POP	POP
C533	UN-POP	POP
C40	UN-POP	POP
C41	UN-POP	POP
C42	UN-POP	POP
C43	UN-POP	POP
R9	UN-POP	POP
R10	UN-POP	POP
R420	POP	UN-POP
R421	POP	UN-POP
R422	POP	UN-POP
R423	POP	UN-POP
C530	POP	UN-POP
U8	POP	UN-POP
U9	UN-POP	POP
U44	POP	UN-POP
C556	POP	UN-POP
U3	POP	UN-POP
C399	POP	UN-POP



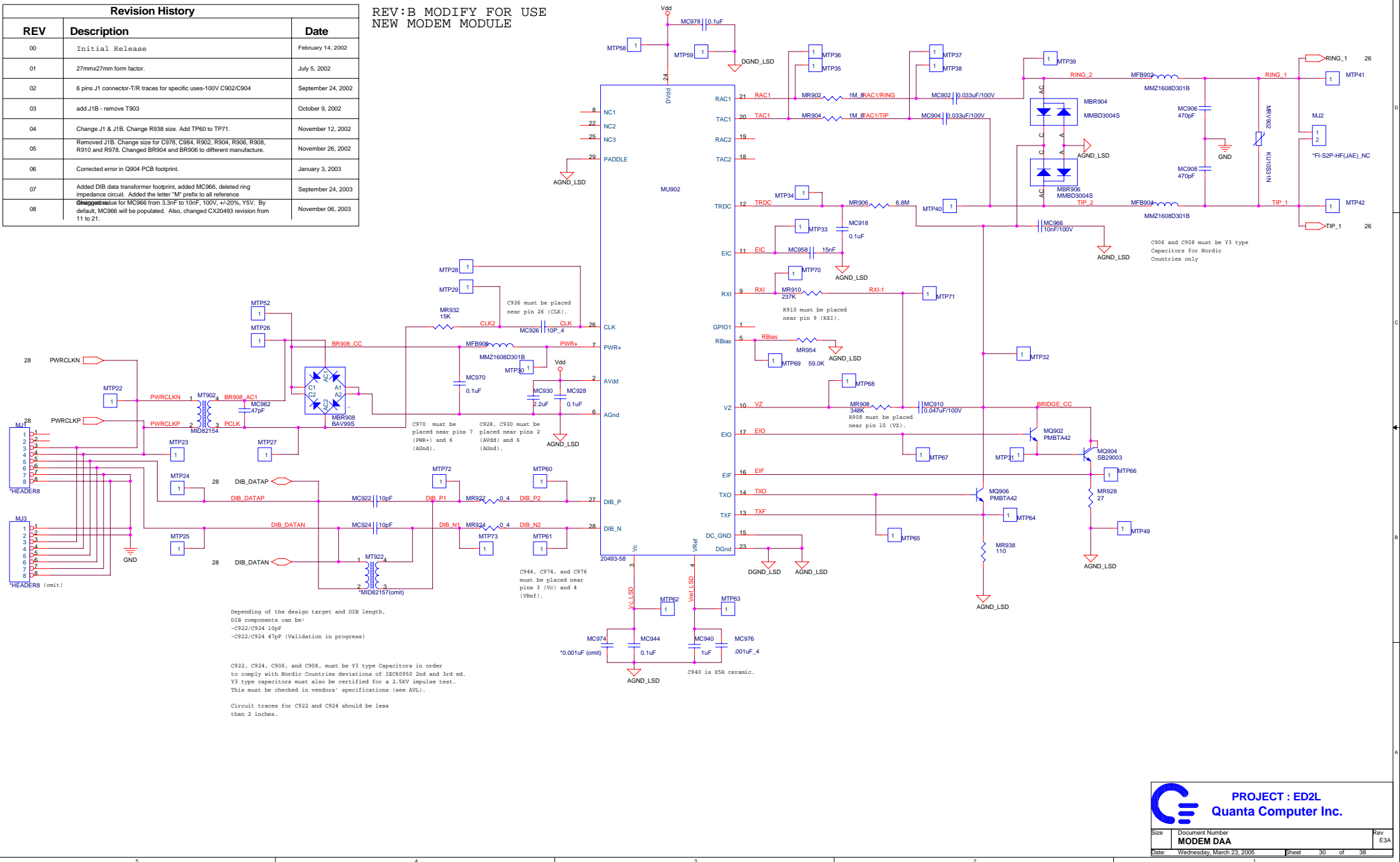
U44,U3,C399,C556
RTL8110SBL(1G)--DEPOP
RTL8100L(10,100)--POP

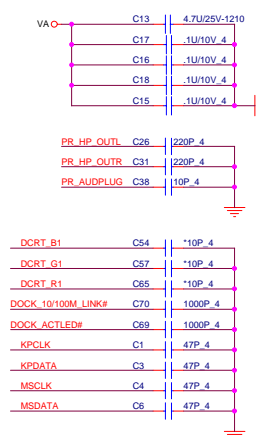
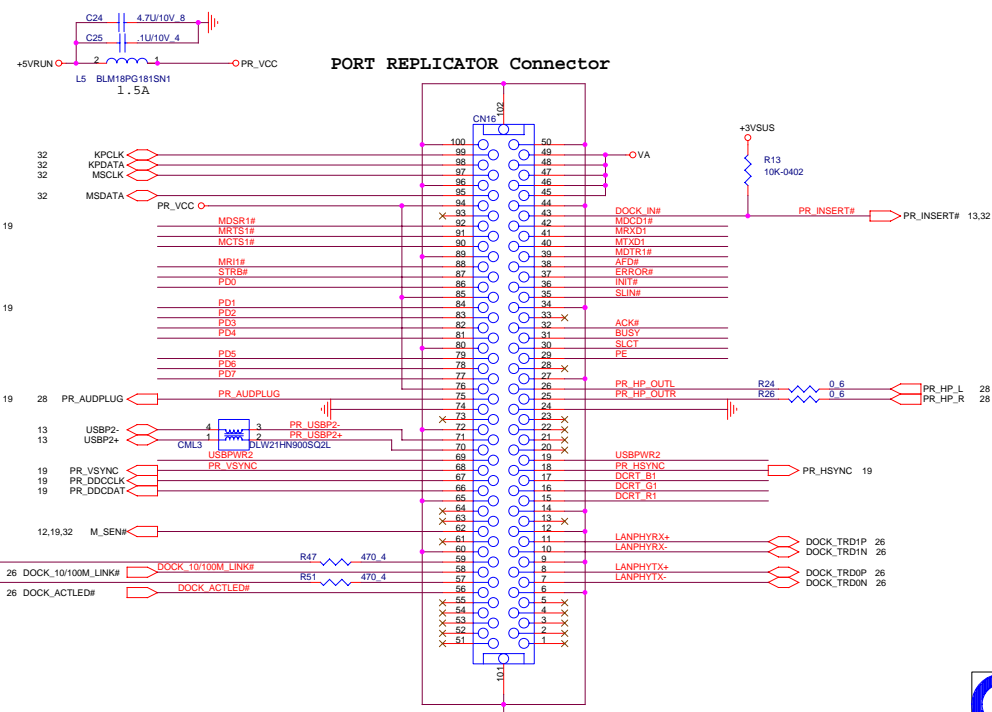
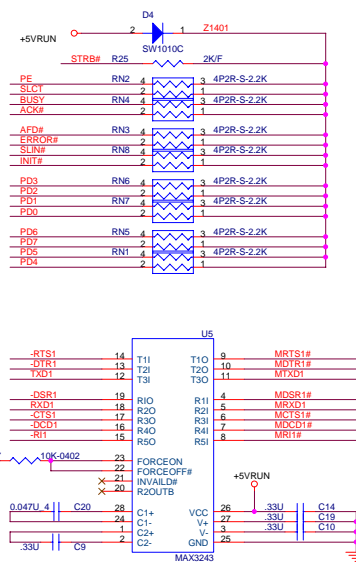
GAIN	SPKR MODE	HP MODE
0	10.5	3
1	9	0

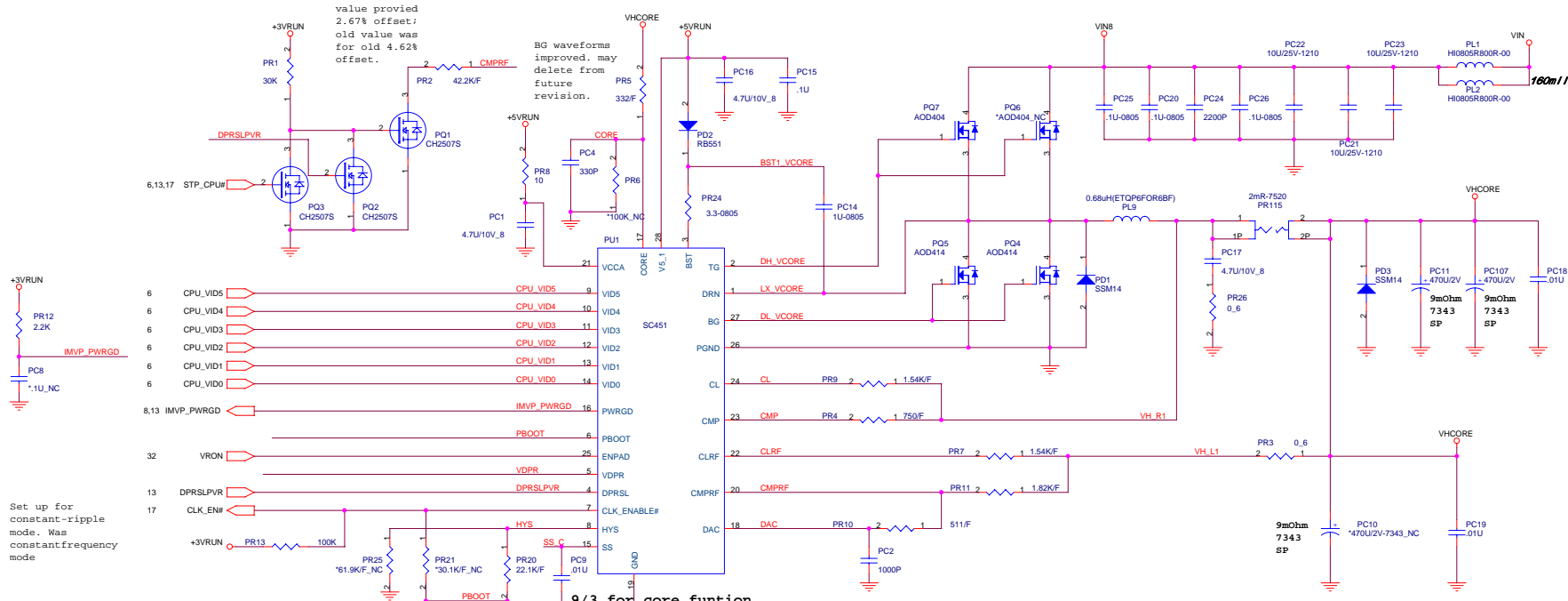


Revision History		
REV	Description	Date
00	Initial Release	February 14, 2002
01	27mmx27mm form factor.	July 5, 2002
02	6 pins J1 connector-T/R traces for specific uses-100V C902/C904	September 24, 2002
03	add J1B - remove T903	October 9, 2002
04	Change J1 & J1B. Change R938 size. Add TP60 to TP71.	November 12, 2002
05	Removed J1B. Change size for C978, C984, R902, R904, R906, R908, R910 and R978. Changed BR904 and BR906 to different manufacture.	November 26, 2002
06	Corrected error in Q904 PCB footprint.	January 3, 2003
07	Added DIB data transformer footprint, added MC966, deleted ring impedance circuit. Added the letter "M" prefix to all reference designators for MC966 from 3.3k to 10k, 100V, +20%, Y5V. By default, MC966 will be populated. Also, changed CX20493 revision from 11 to 21.	September 24, 2003
08		November 06, 2003

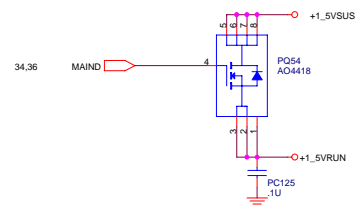
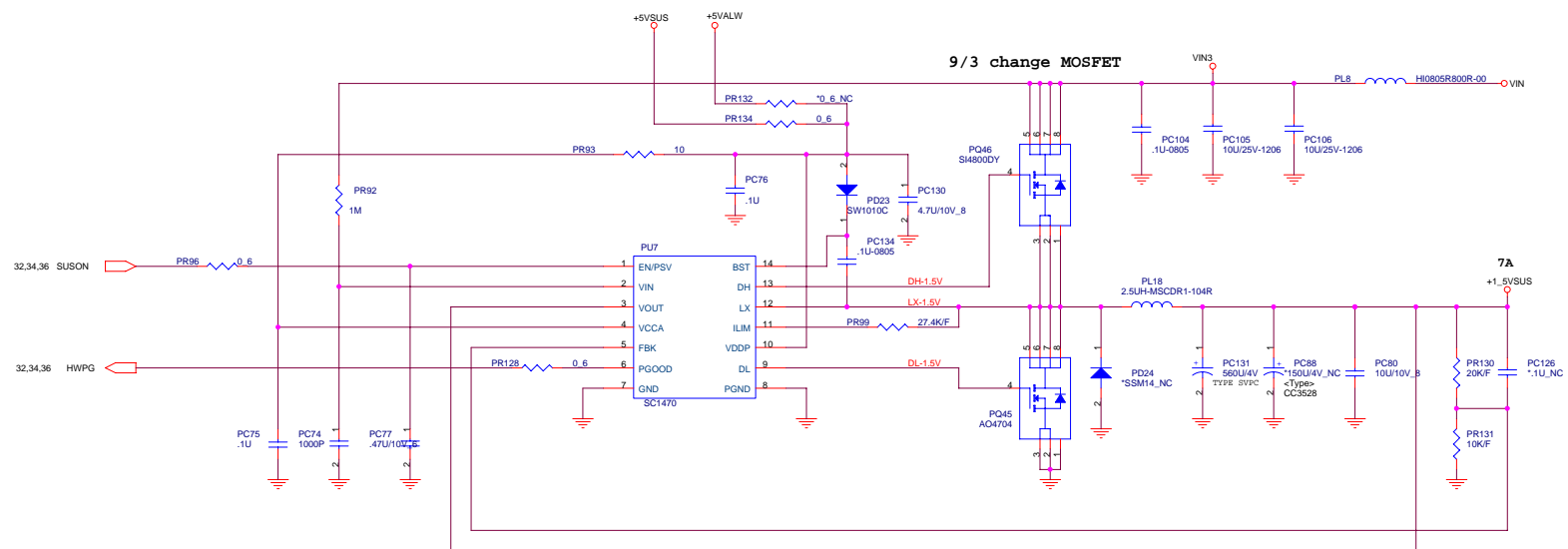
REV:B MODIFY FOR USE
NEW MODEM MODULE





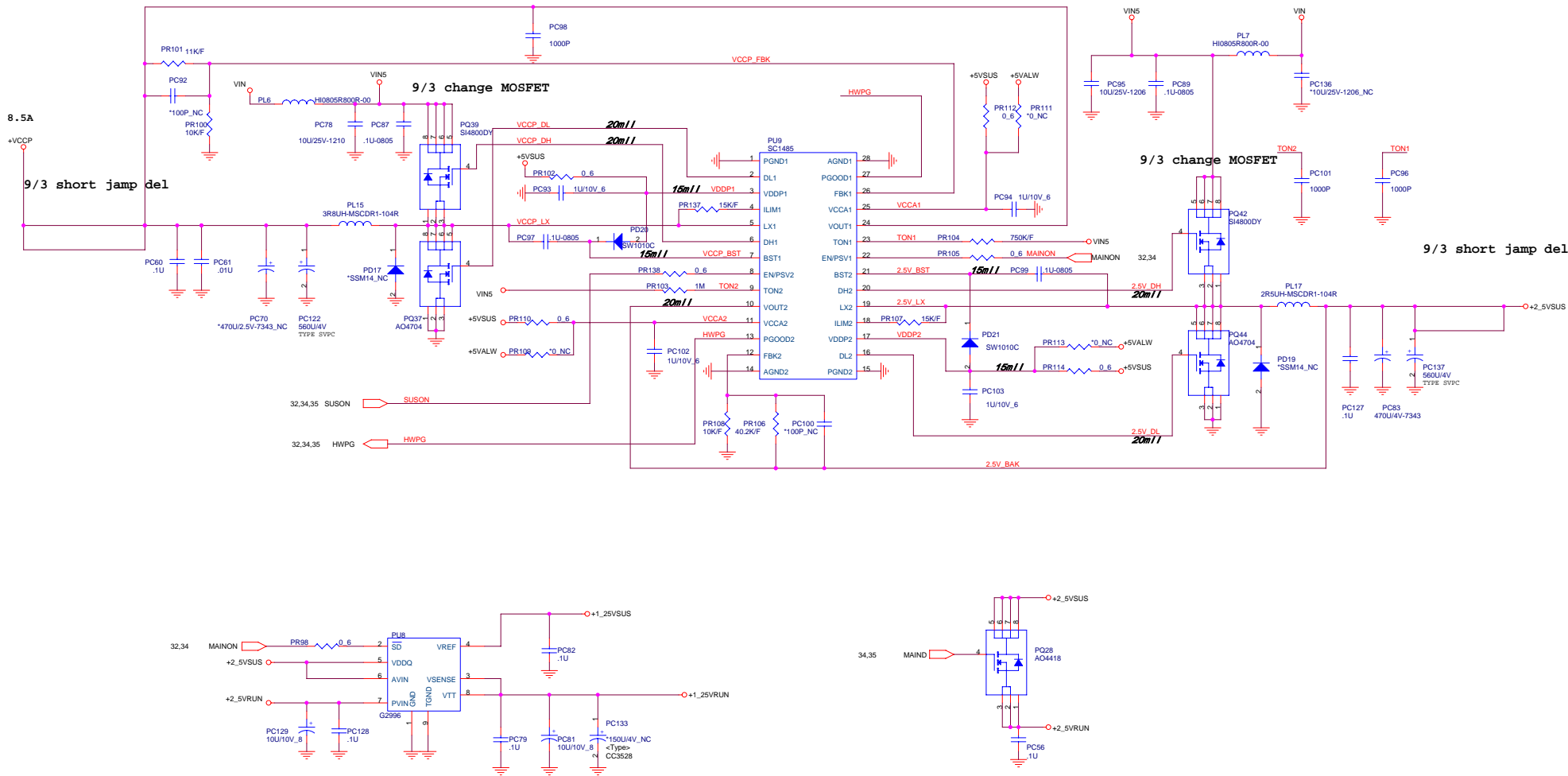


V I D							Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0	V	
0	1	0	1	1	1	1.340	
0	1	1	0	0	0	1.324	
0	1	1	0	1	0	1.292	
0	1	1	1	0	0	1.260	
0	1	1	1	0	1	1.244	
0	1	1	1	1	1	1.212	
1	0	0	0	0	1	1.180	
1	0	0	0	1	1	1.148	
1	0	0	1	1	0	1.100	
1	0	1	0	0	1	1.052	
1	0	1	0	1	1	1.020	
1	0	1	1	1	0	0.972	
1	1	0	0	0	0	0.940	




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	2.5VSUS / +1.25TERM	E3A
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Size	Document Number	Rev
	+VCCP/+1.25V/+2.5V	E3A
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MODEL	REV	CHANGE LIST				Model	JM2	MB
						Page	FM	TO
	1A	Preliminary Release	1					
			2	1A				
			3					
			4					
			5					
			6					
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			49					
<div>QUANTA COMPUTER</div>		PROJECT : DM3B	DOC. NO. ???	REV: 1A	ASSY:			
		APPROVED BY :	CHECKED BY:	DRAWN BY :	DATE :	SHEET 1 OF 1		