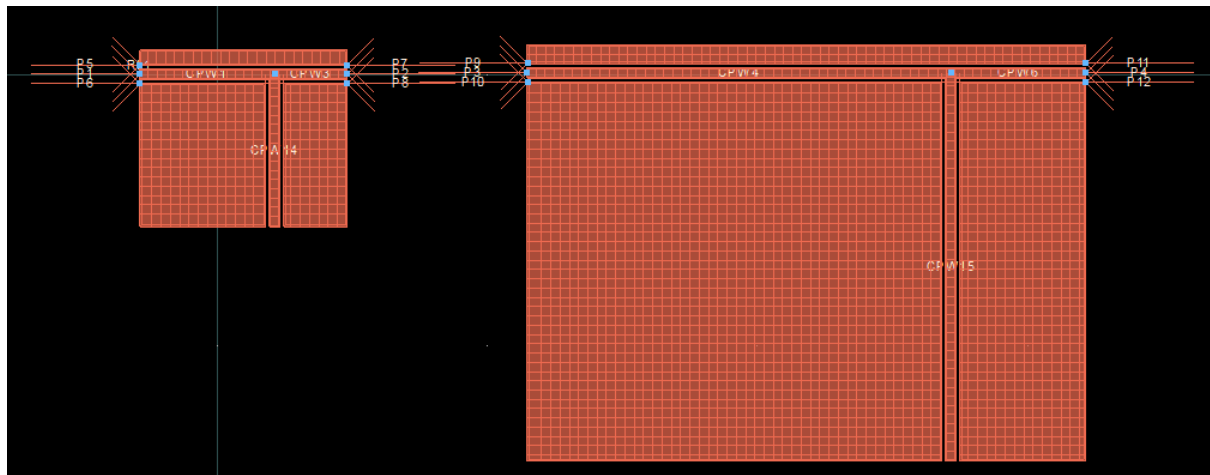
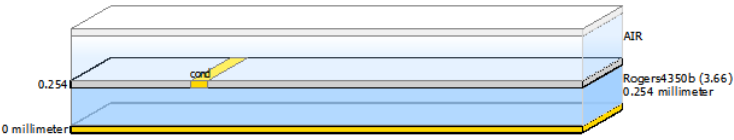


Layout :







Substrate Name: substrate1



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Substrate Layer Stackup 

	Type	Name	Material	Thickness
	Dielectric		AIR	
	Conductor La...	cond (1)	Copper	18 um
	Dielectric		Rogers4350b	0.254 mm
	Cover		PERFECT_C...	0 um

Ports

Defined by input layout

[Open Layout Port Editor...](#)



S-parameter Ports (view only)

Number	Gnd Layer	Name	Feed Type	Ref Impedance [Ohm]	Ref Offset [mm]	Term Type
1	N/A	P1	TML	50 + 0i	0	inputOutput
		P5				
		P6				
2	N/A	P2	TML	50 + 0i	0	inputOutput
		P7				
		P8				
3	N/A	P3	TML	50 + 0i	0	inputOutput
		P9				
		P10				
4	N/A	P4	TML	50 + 0i	0	inputOutput
		P11				
		P12				

Layout Pins (view only)

Name	Layer	Net	Connected to	Purpose	X [mm]	Y [mm]	Number	Layer Num	Purpose Num	Pin Type
P1	cond	P1	1(+)	drawing	-2.8644	0.0224	1	1	-1	Point
P2	cond	P2	2(+)	drawing	4.8271	0.0224	2	1	-1	Point
P3	cond	P3	3(+)	drawing	11.4878	0.0777	3	1	-1	Point
P4	cond	P4	4(+)	drawing	32.189	0.0777	4	1	-1	Point
P5	cond	P7	1(-)	drawing	-2.8612	0.3726	5	1	-1	Point
P6	cond	P6	1(-)	drawing	-2.8584	-0.3277	6	1	-1	Point
P7	cond	P7	2(-)	drawing	4.8079	0.3734	7	1	-1	Point

☐ Hide connected layout pins

☒ Auto-select

☒ Auto-center

☐ Auto-zoom