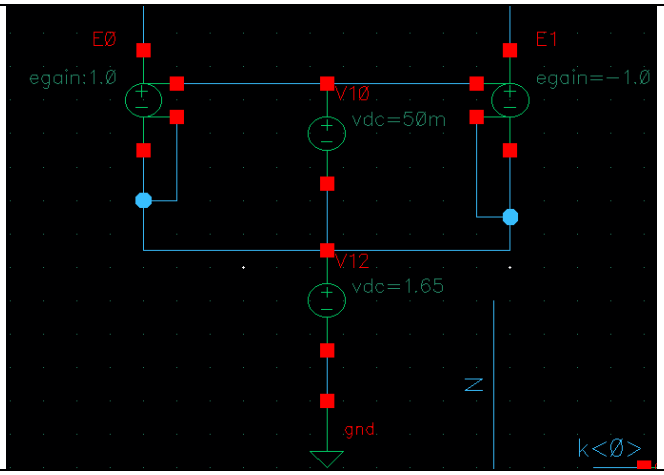
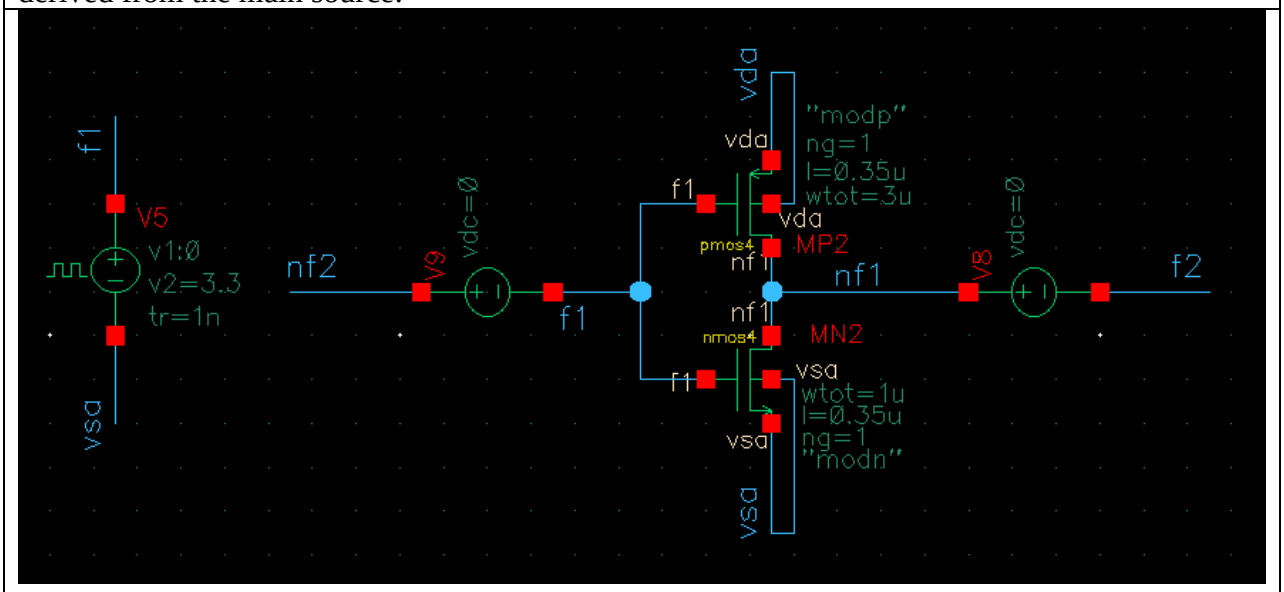
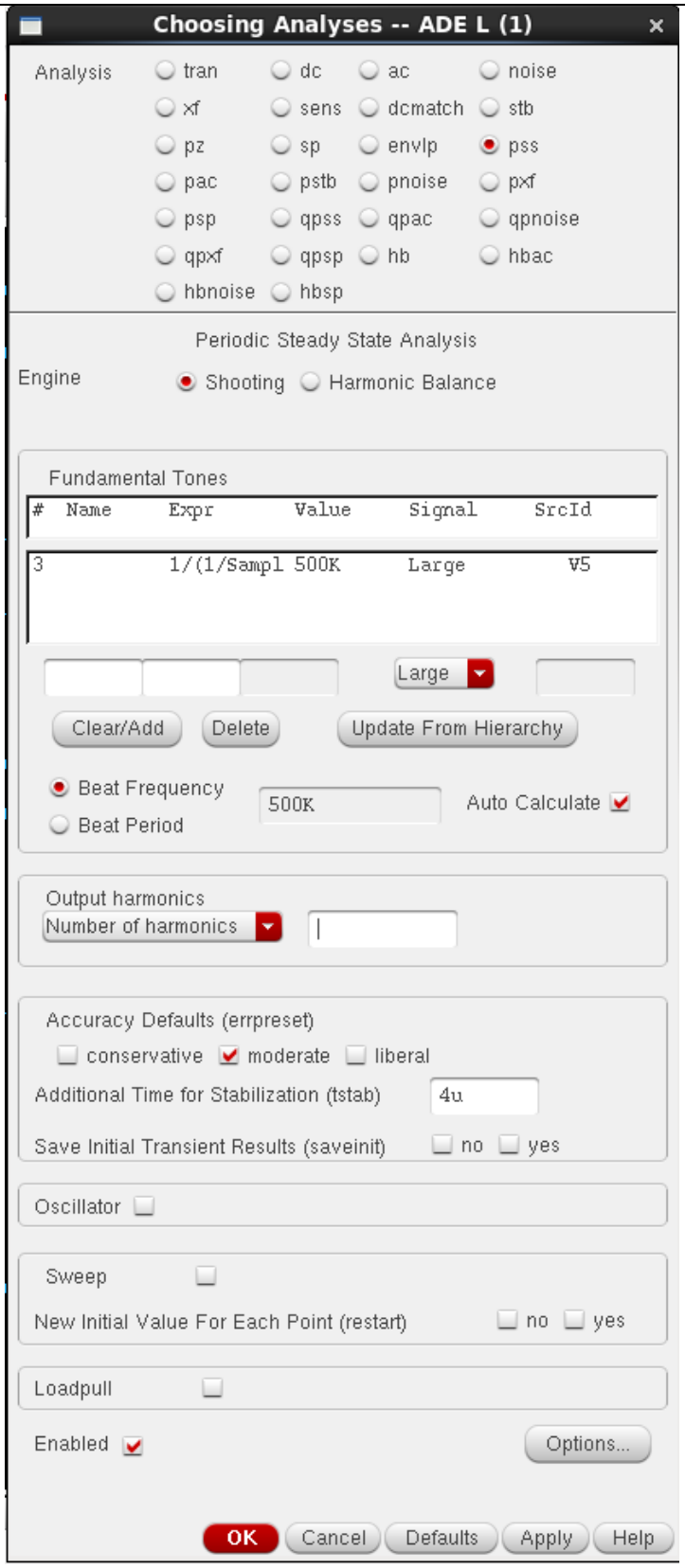
1 Input signal is set to be DC.



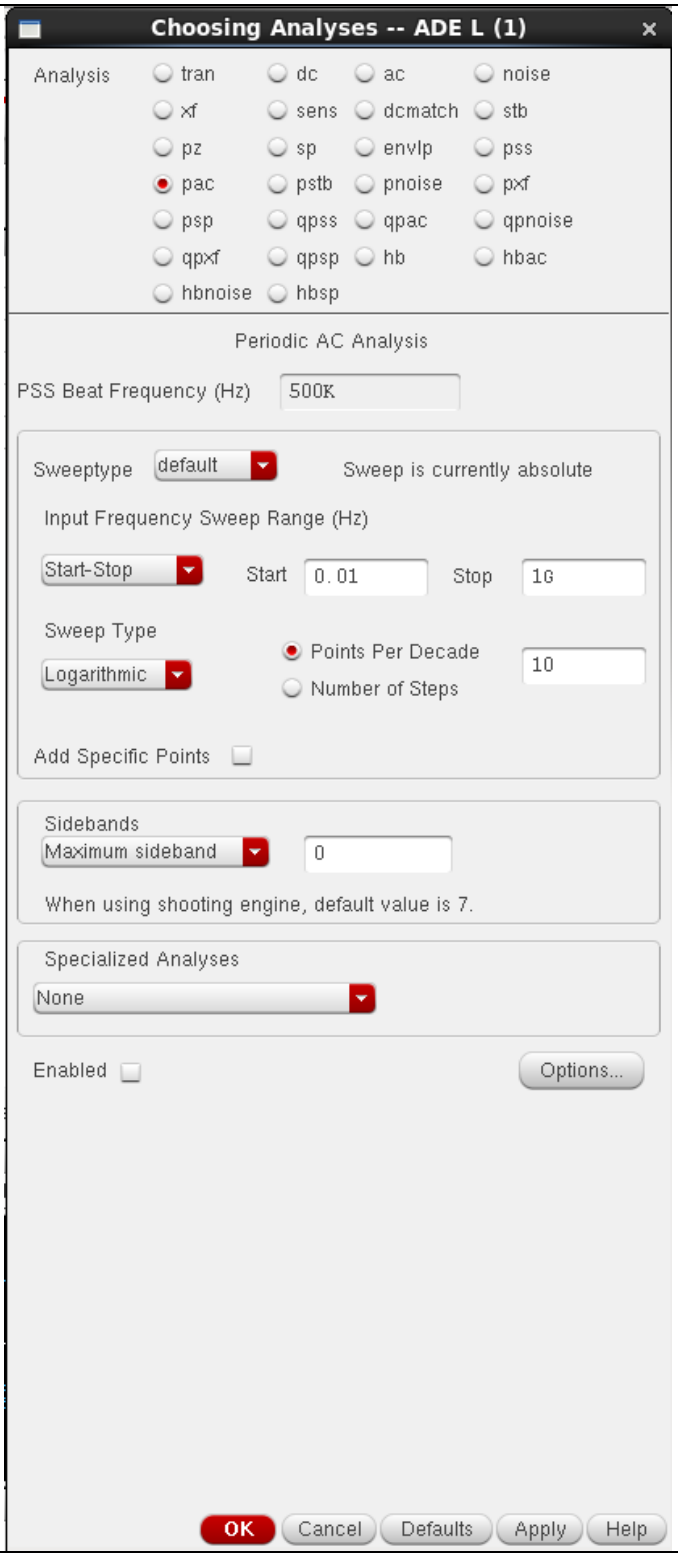
2 Ideal clock generator should be only one. If we need more tan one clock signal, they should be derived from the main source:



3 PSS analysis setup.
Fundamental Tones should have only one frequency.
Beat Frequency should equal to the sampling frequency.
Additional Time for Stabilization – time required for the circuit to achieve normal region of operation.

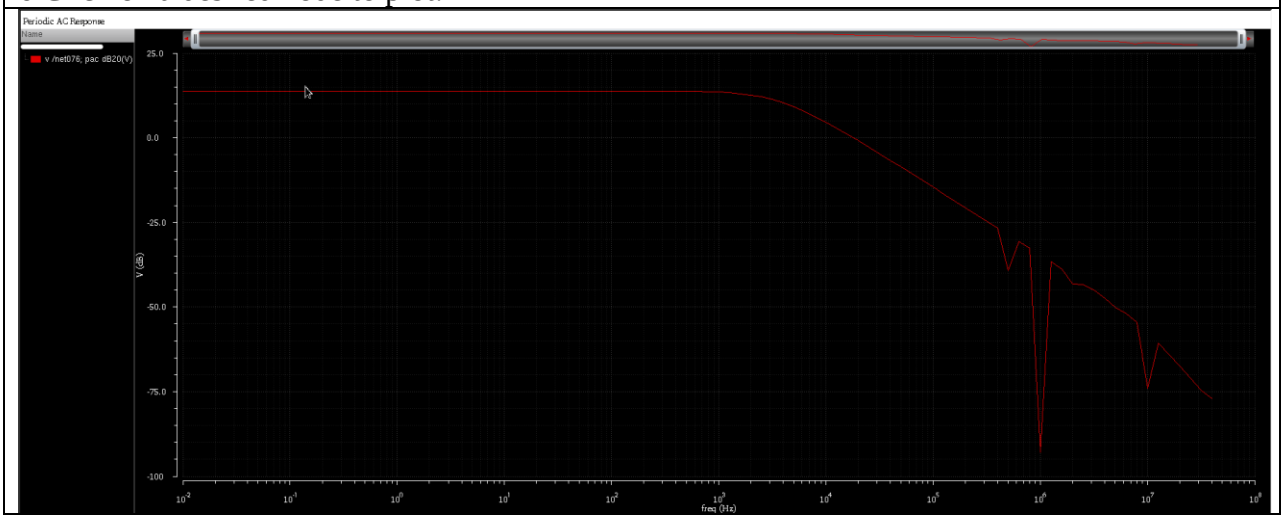


4 PAC analysis setup.
Fill in frequency range and the required frequency step.
Sidebands should be 0.



5 Printing out the results.
Select Results->Direct Plot->Main Form. Then select PAC tab and select required parameters.

6 Click on a desired node to plot.



P.S. We will see some artefacts near the sampling frequency, which is normal and makes complete sense.