

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE
DESIGN				
CHECK				
RESPONSIBLE				
SIZE = 3		VER :		
FILE NAME : XXXX-XXXXXX-XX				
P/N	XXXXXXXXXXXX			

INVENTEC

TITLE GALILEO

SIZE	CODE	DOC. NUMBER	REV
A3	CS		000

SHEET 1 OF 45

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F

TABLE OF CONTENTS

PAGE

1. COVER PAGE	
2. INDEX	
3. BLOCK DIAGRAM	
4. POWER SEQUENCE BLOCK	
5. CHARGER & OCP	
6. SELECT & BATTERY CONN	
7. SYSTEM POWER (3V/5V)	
8. SYSTEM POWER (1.5V/1.8V)	
9. CPU CORE POWER	
10.VCCP / 0.9V	
11-12. POWER SLEEP	
13.CLOCK GENERATOR & ICS9P956	
14.CPU VIA C7-M ULV -1	
15.CPU VIA C7-M ULV -2	
16.FAN & THERMAL CONTROLLER	
17.N/B VN896CD 1	
18.N/B VN896CD 2	
19.N/B VN896CD 3	
20.N/B VN896CD 4	
21.N/B VN896CD 5	
22.DDR2 DIMM0	

PAGE

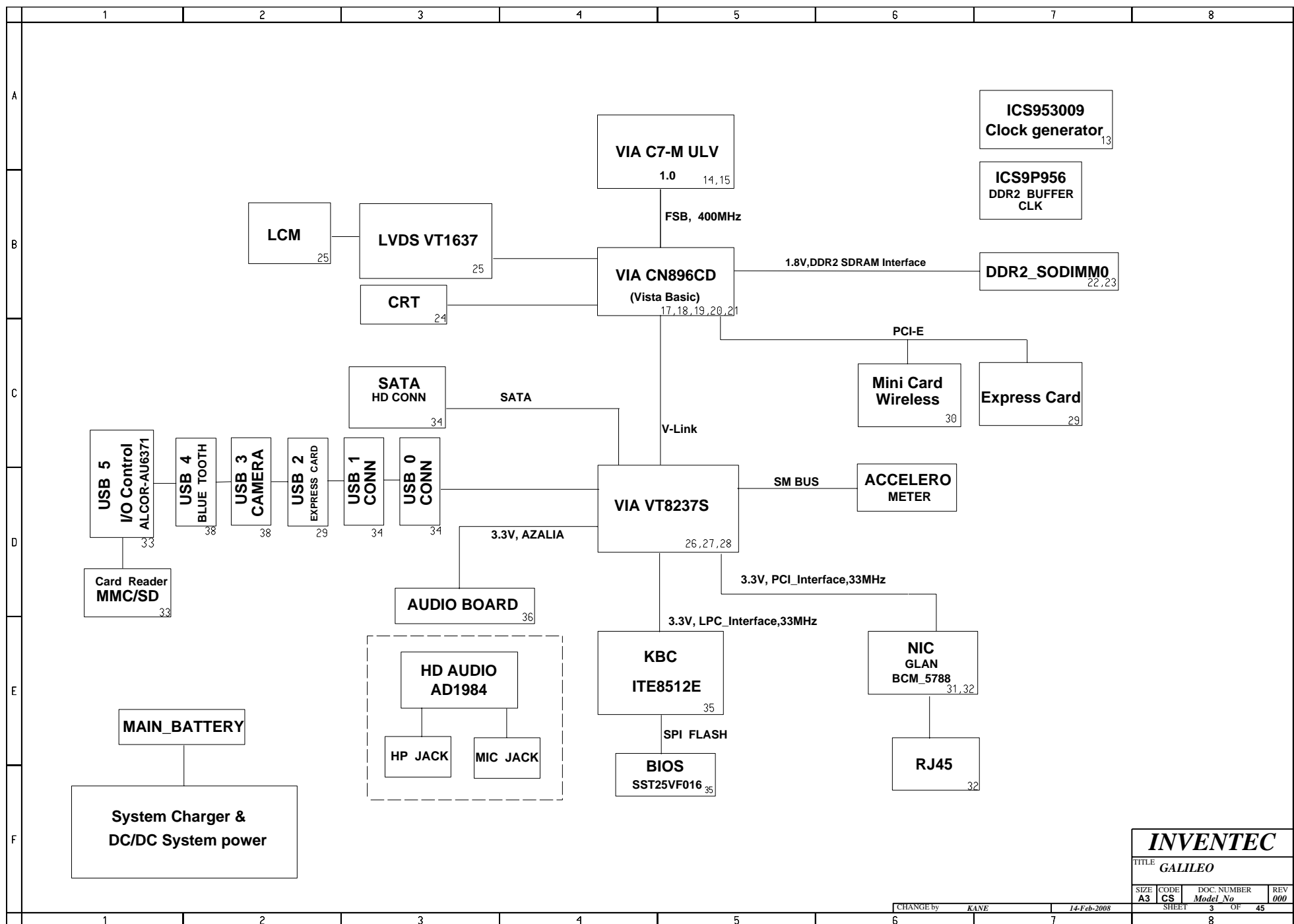
23.DDR2 PULL UP TERMINATOR	
24.CRT	
25.LCM CONN & LVDS TRANSMITTER	
26.S/B VT8237S 1	
27.S/B VT8237S 2	
28.S/B VT8237S 3	
29.EXPRESS CARD	
30.MINICARD CONN (WIRELESS LAN)	
31.LAN CONTROLLER 1	
32.LAN CONTROLLER 2 & RJ45	
33.CARD READER	
34.USB & SATA CONN & ACCELEROMETER	
35.KBC	
36.KEYBOARD CONN	
37.SWITCH & LED (MB)	
38.BLUETOOTH CONN & CAMERA	
39.EMI CAP	

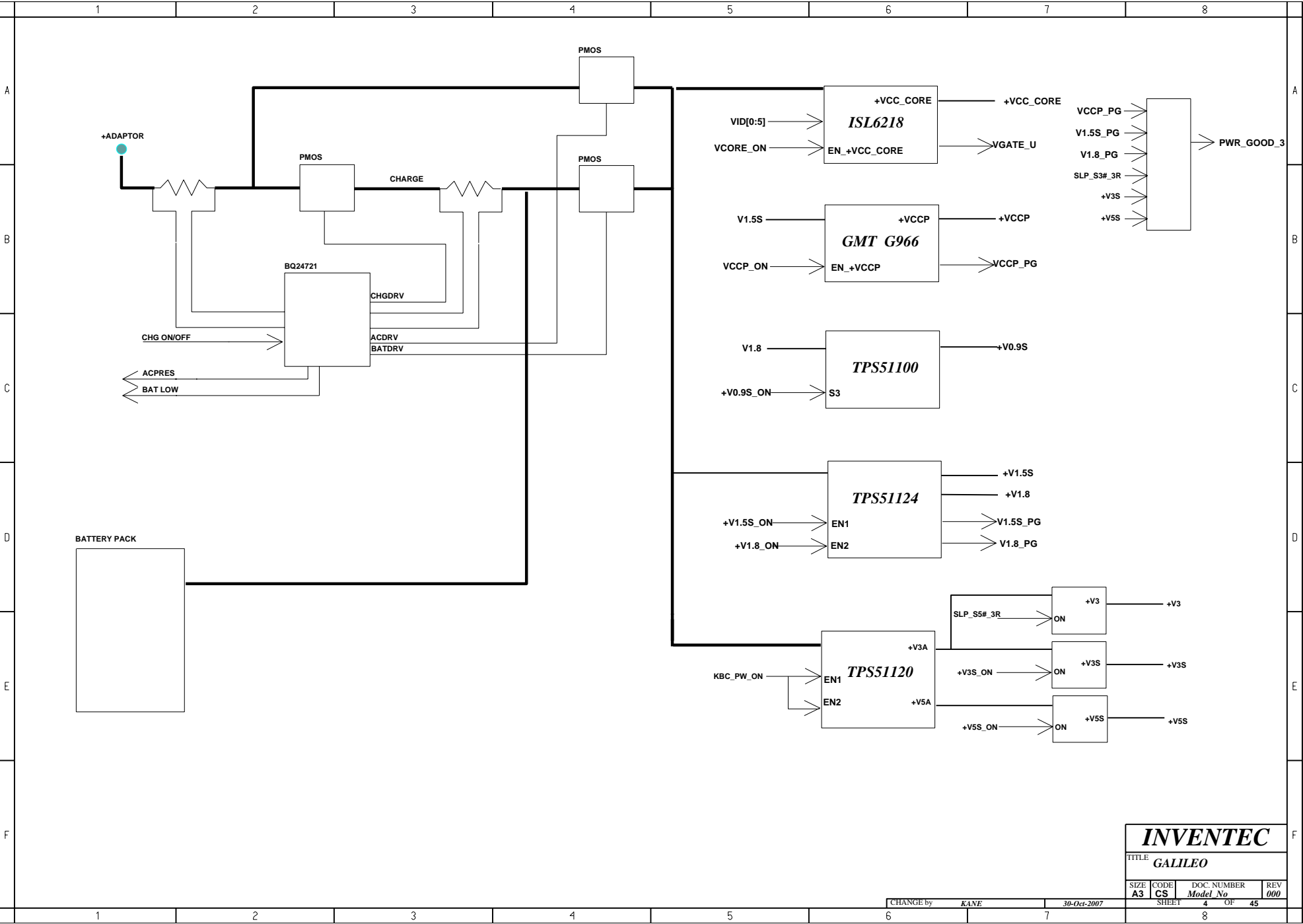
INVENTEC

TITLE **GALILEO**

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV 000
SHEET 2		OF 45	

CHANGE by **KANE** 30-Oct-2007





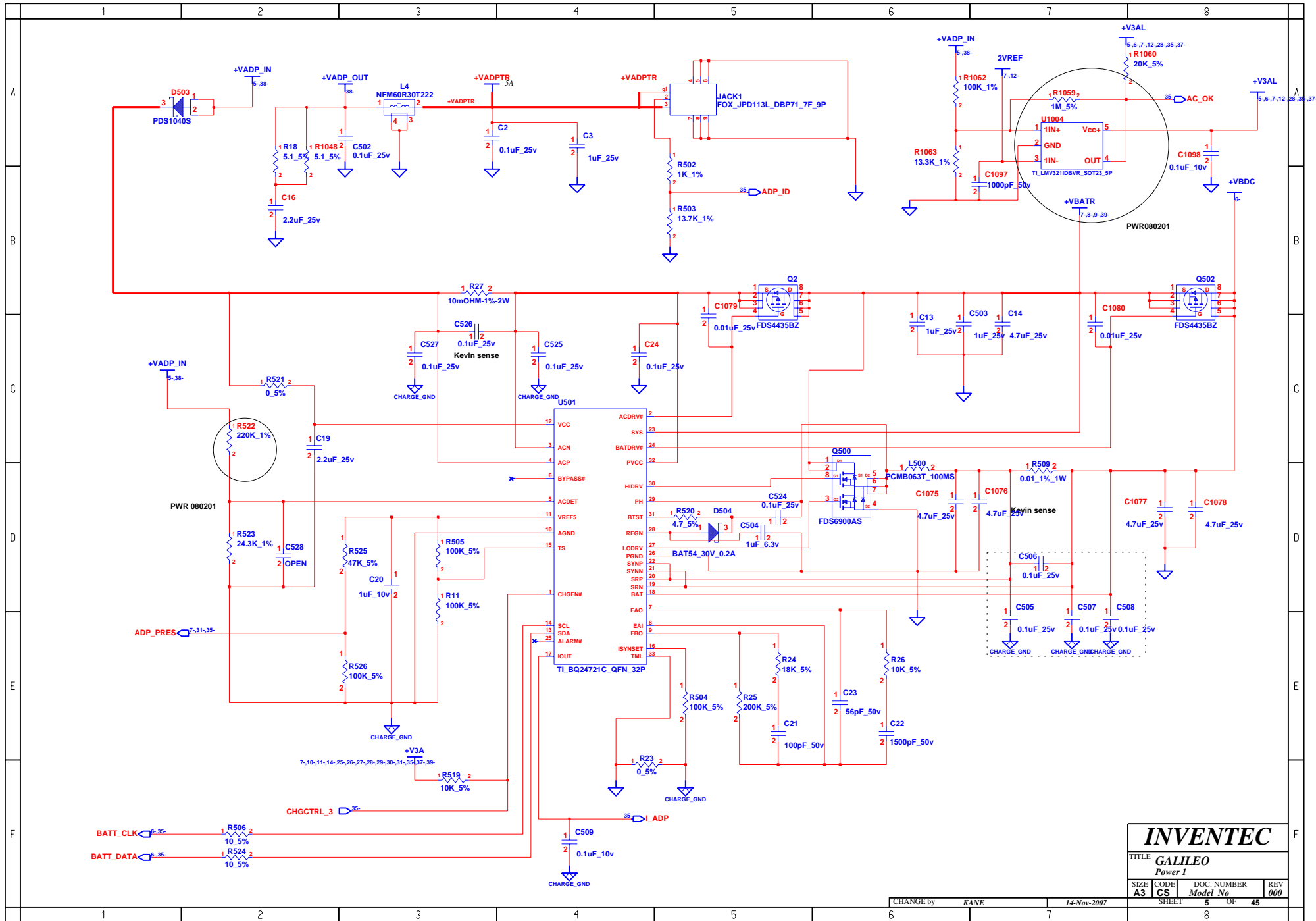
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TITLE **GALILEO**

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV 000
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CHANGE by **KANE** 30-Oct-2007

SHEET **4** OF **45**



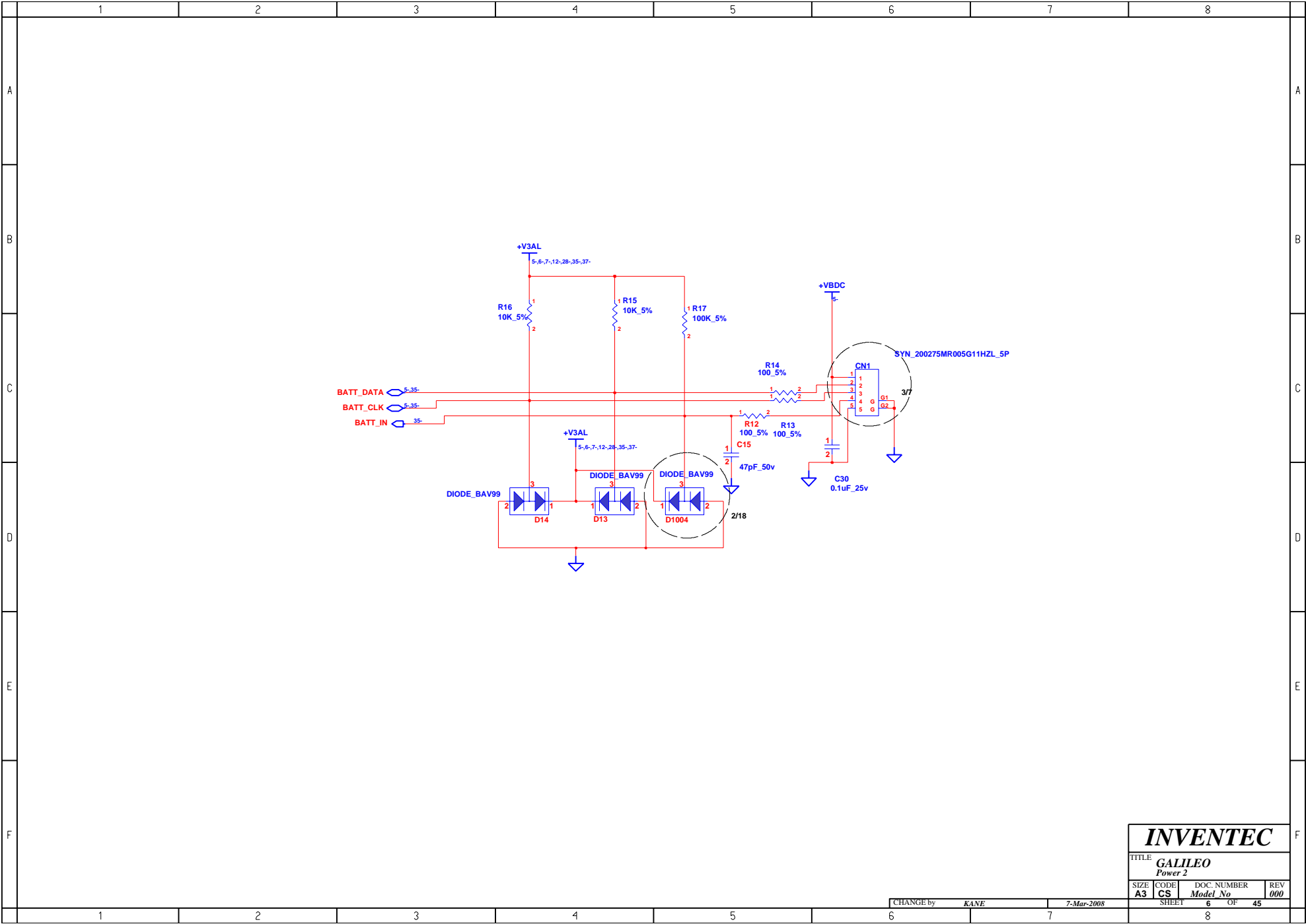
INVENTEC

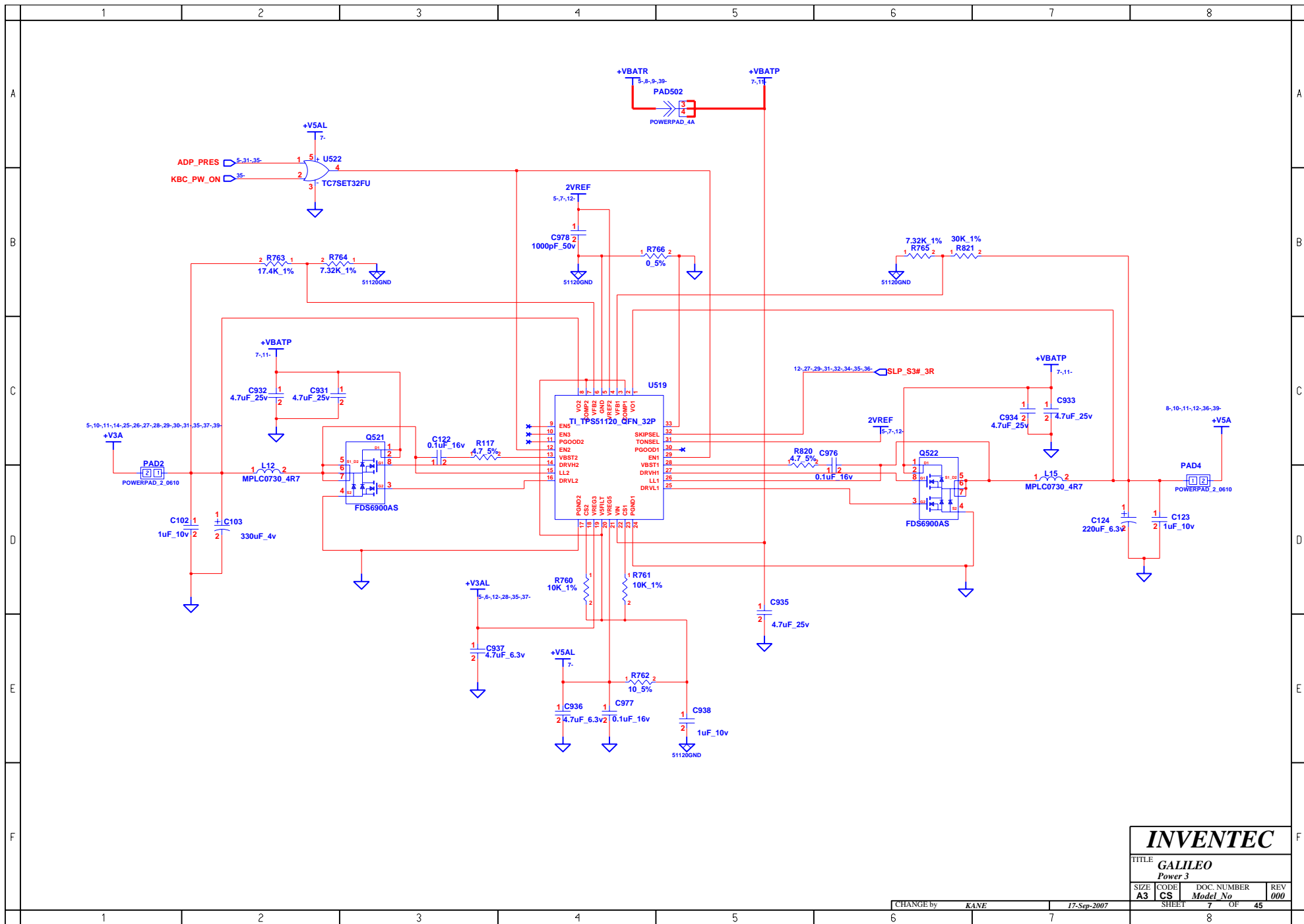
TITLE
GALILEO
Power 1

SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
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CHANGE by **KANE** 14-Nov-2007

SHEET **5** OF **45**



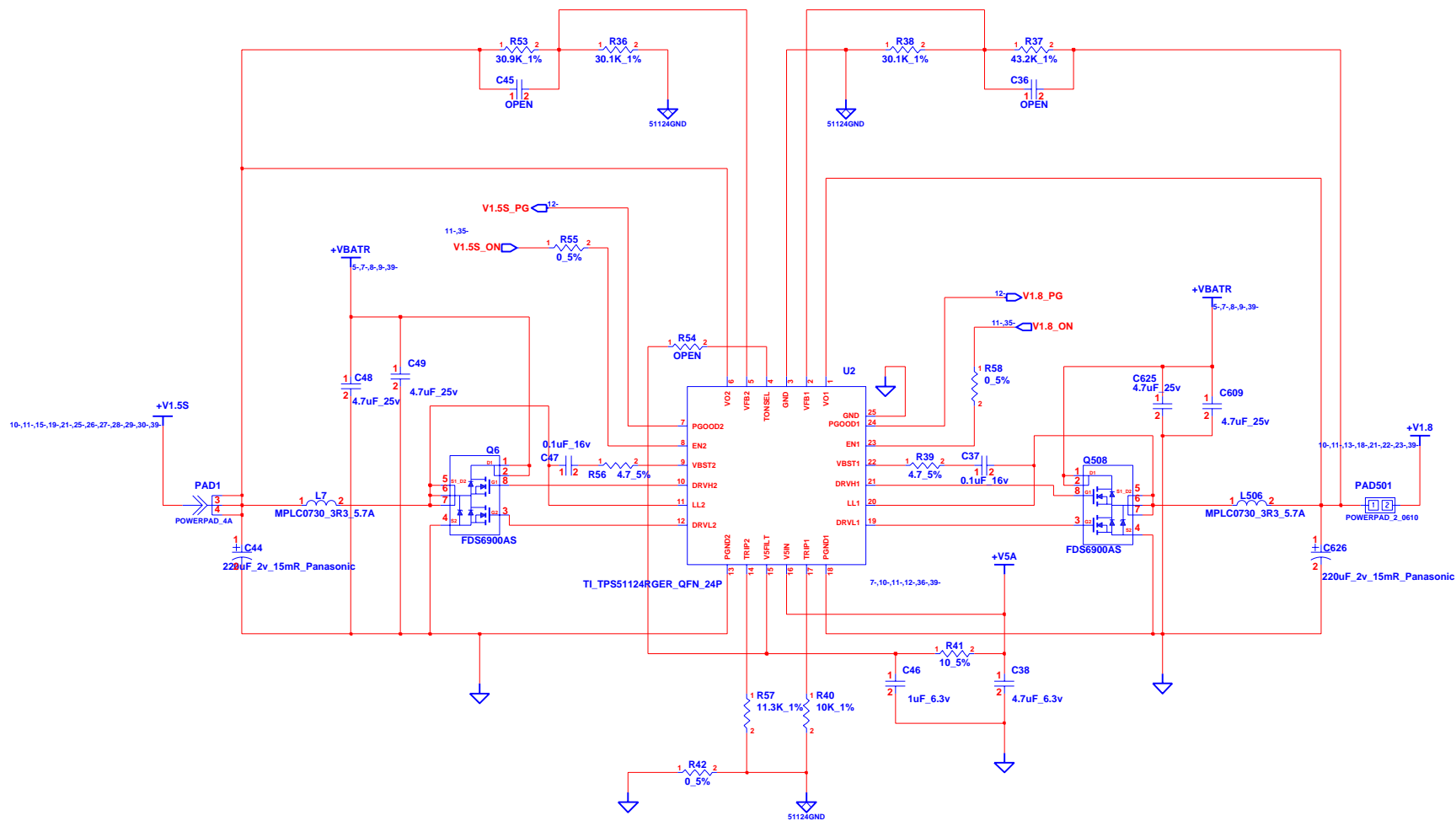


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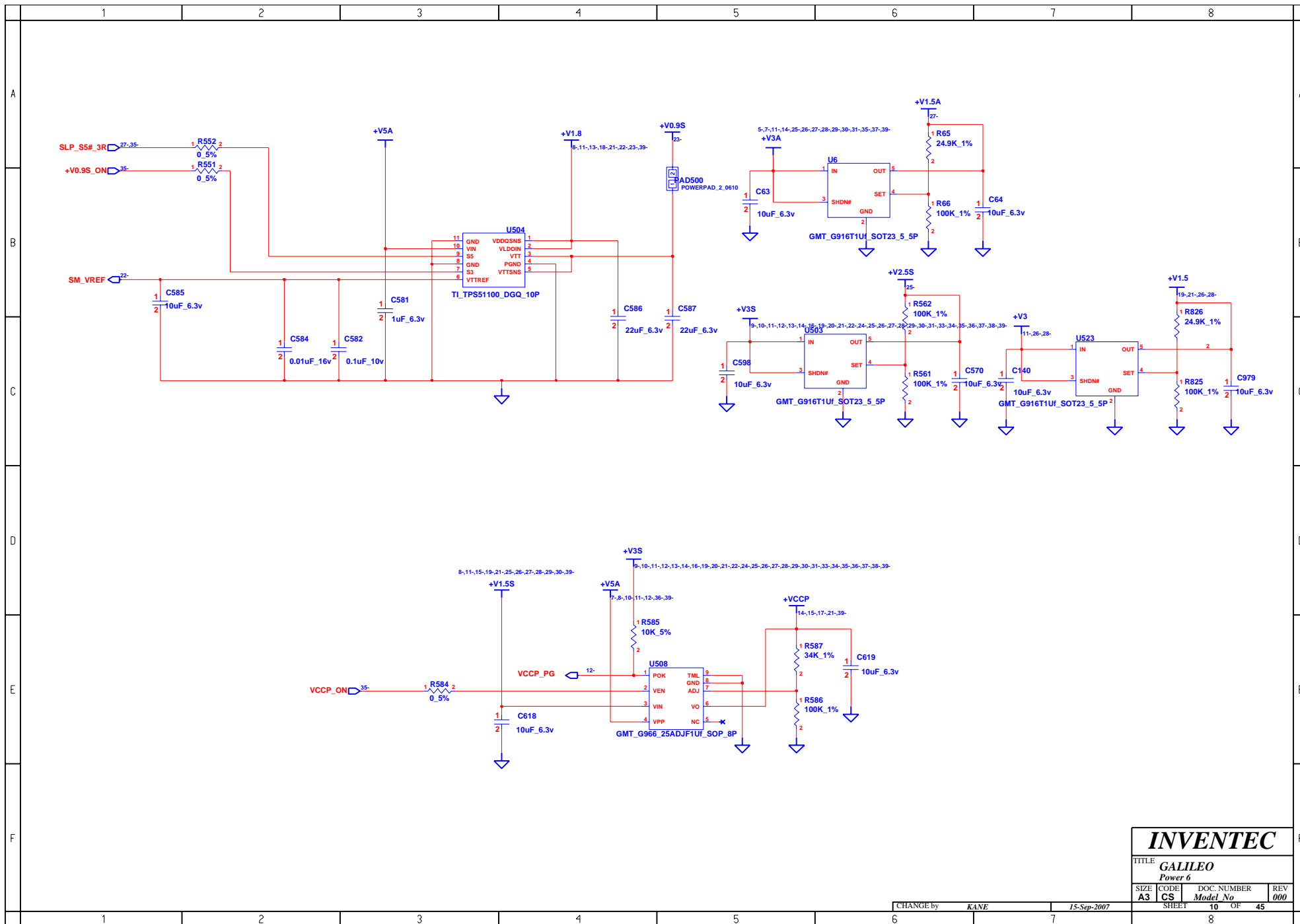
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Power 3			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

CHANGE by KANE 17-Sep-2007

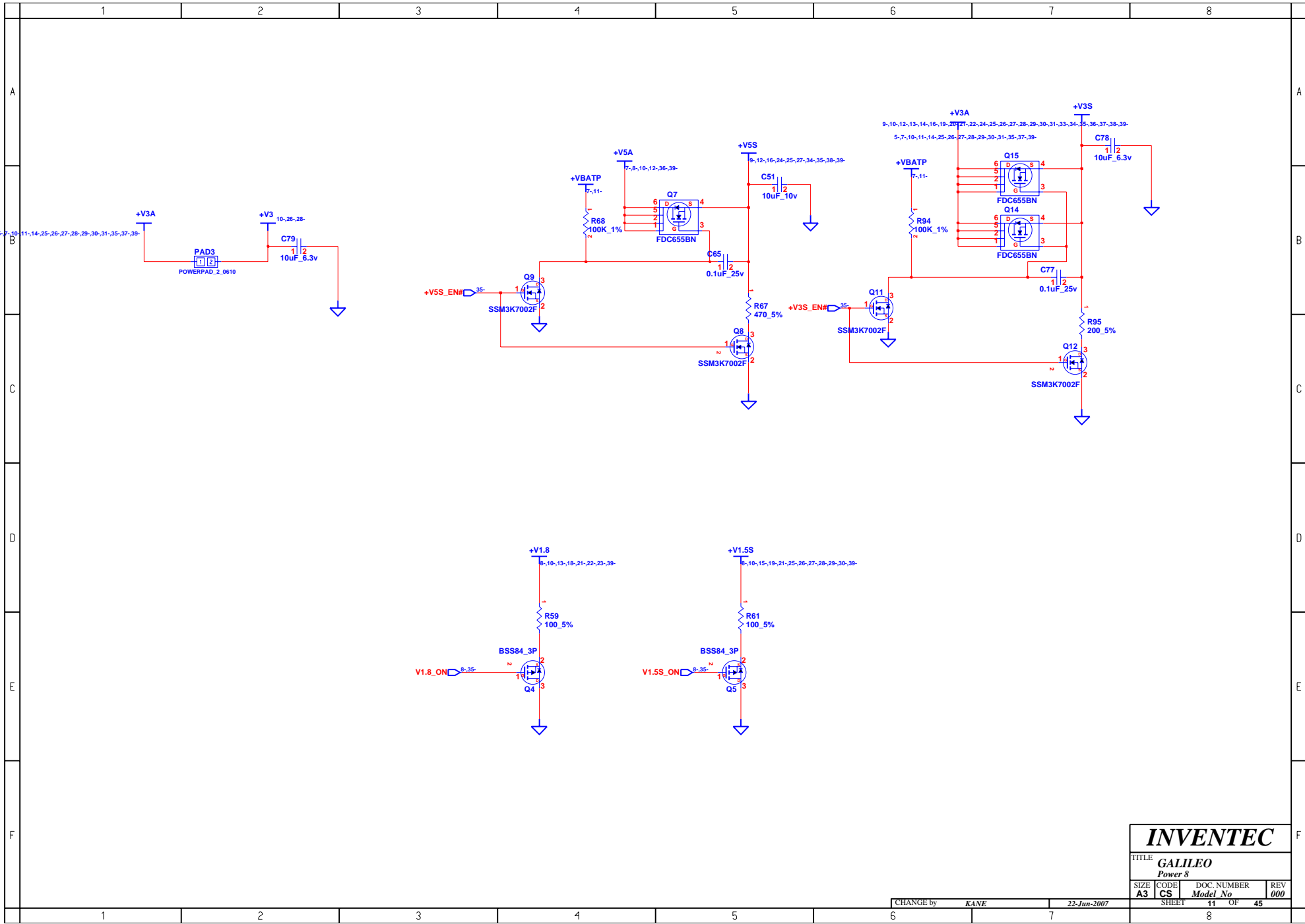
SHEET 7 OF 45



INVENTEC			
TITLE GALILEO Power 4			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		26-Dec-2007	
SHEET 8		OF 45	

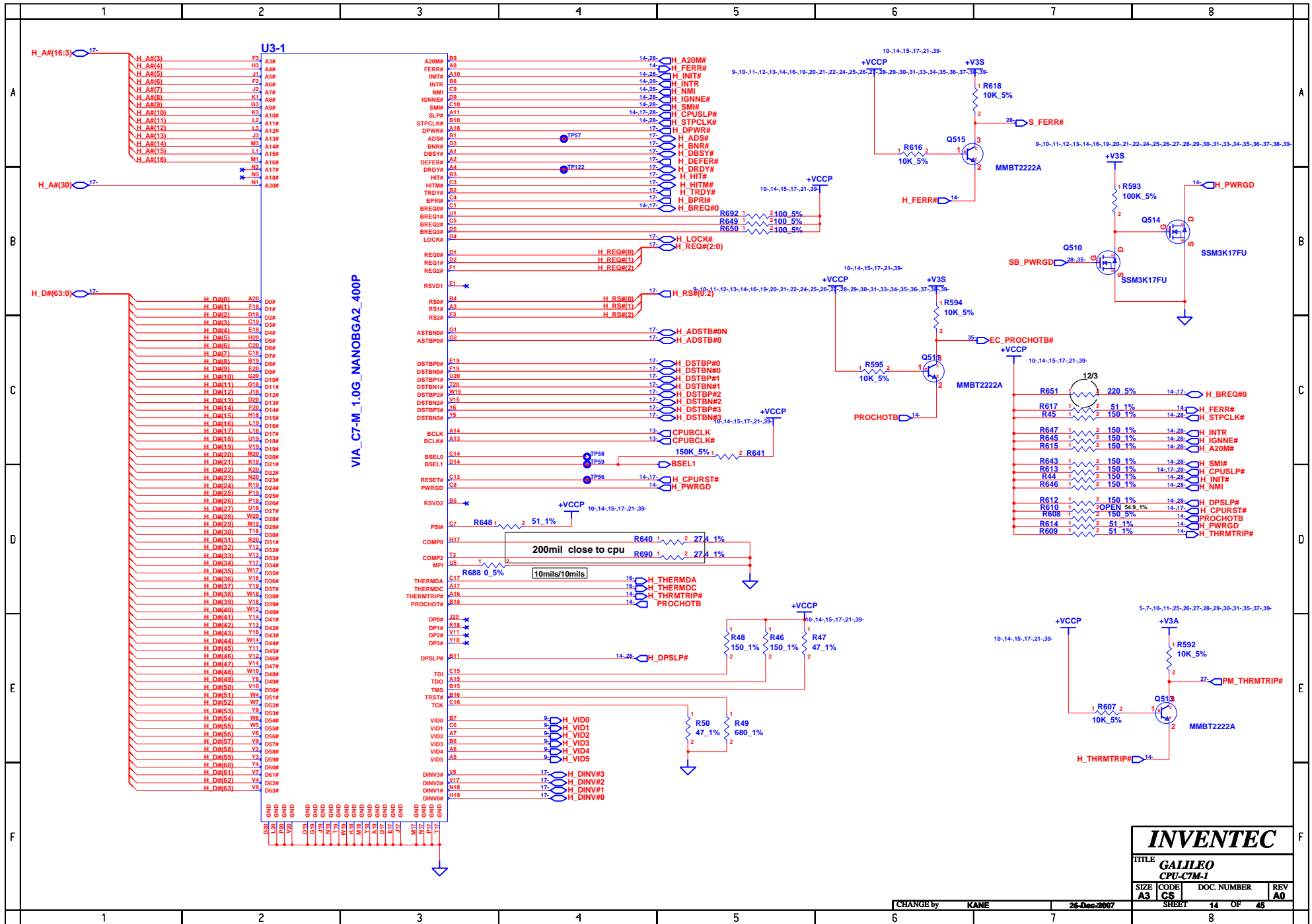


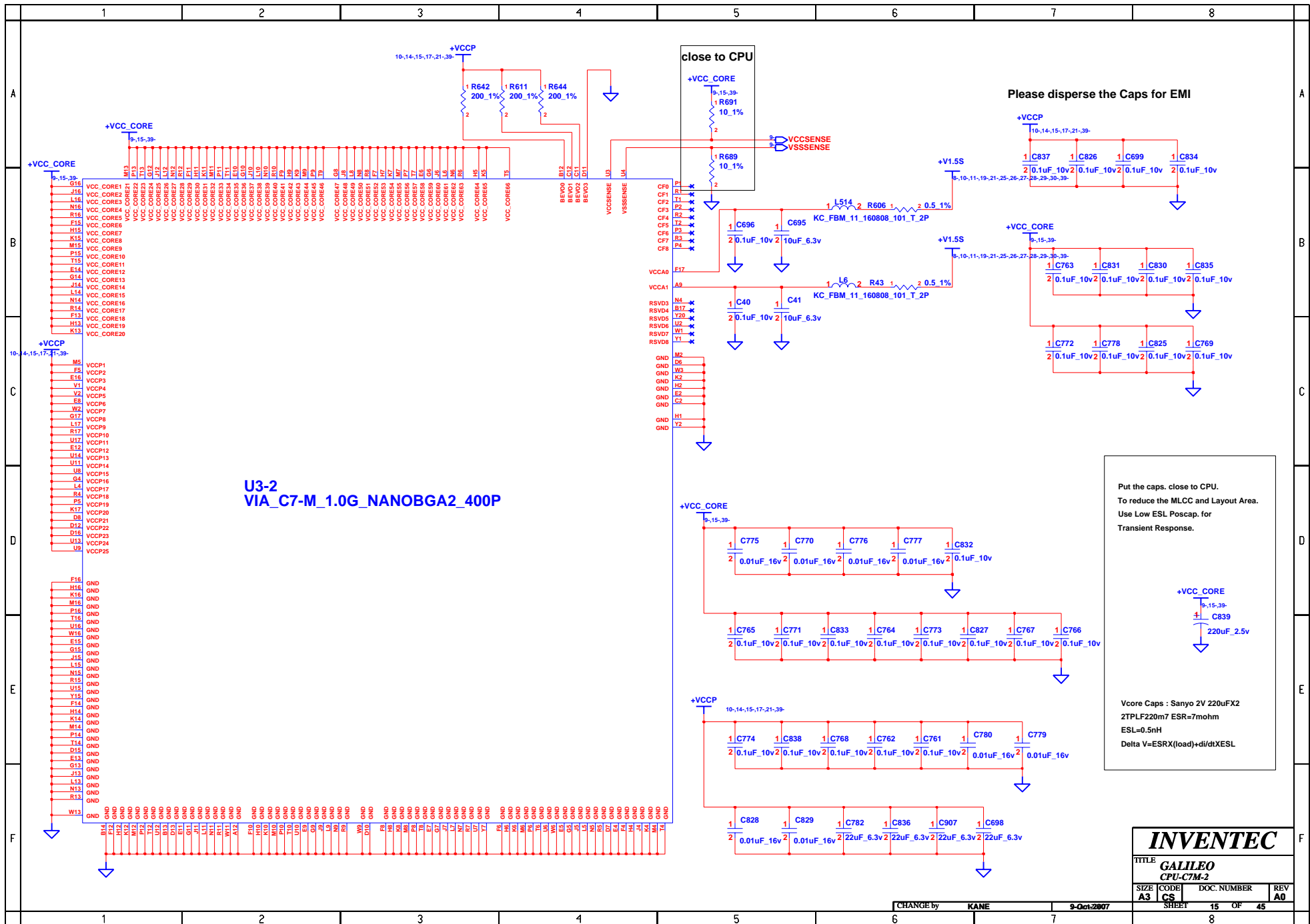
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TITLE GALILEO Power 6			
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SHEET 10		OF 45	

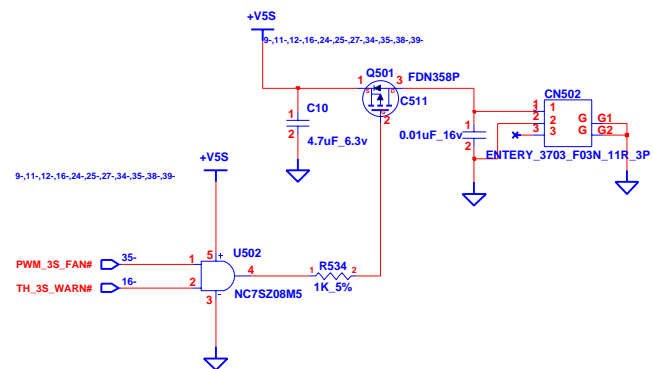
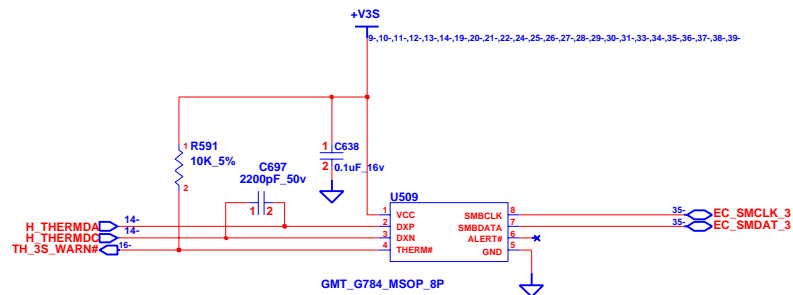


INVENTEC			
TITLE GALILEO Power 8			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
SHEET 11 OF 45			

CHANGE by KANE 22-Jun-2007





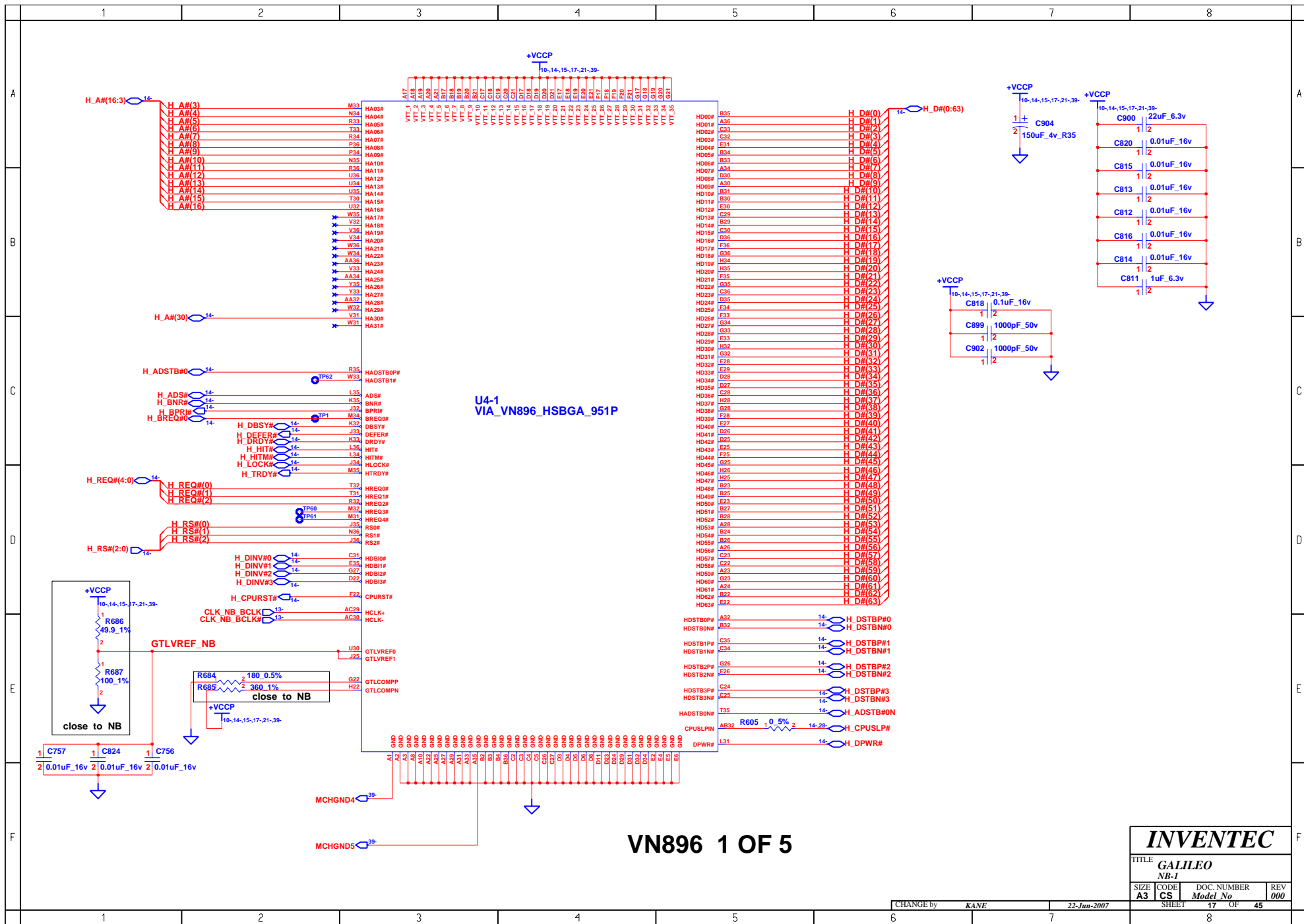
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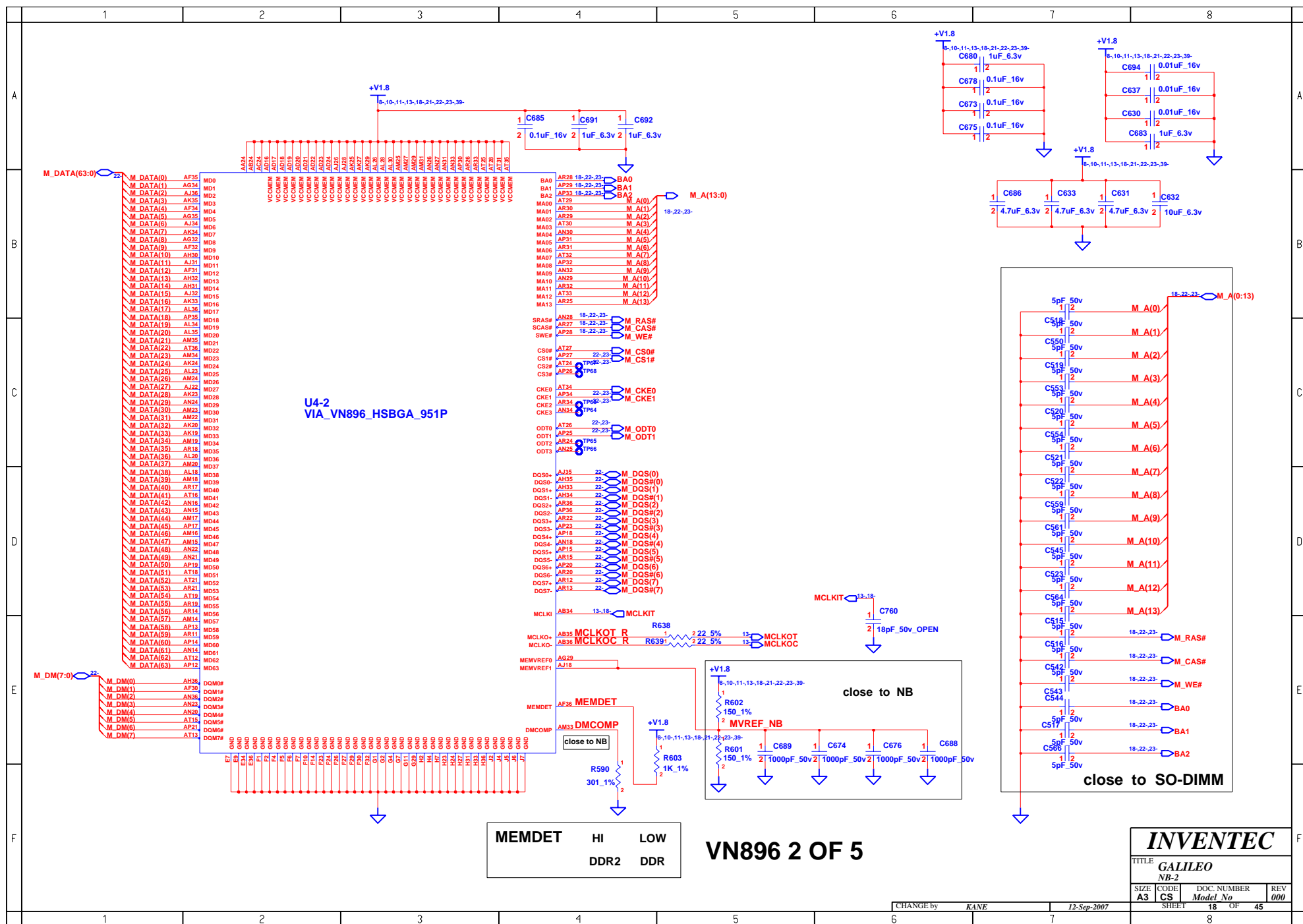
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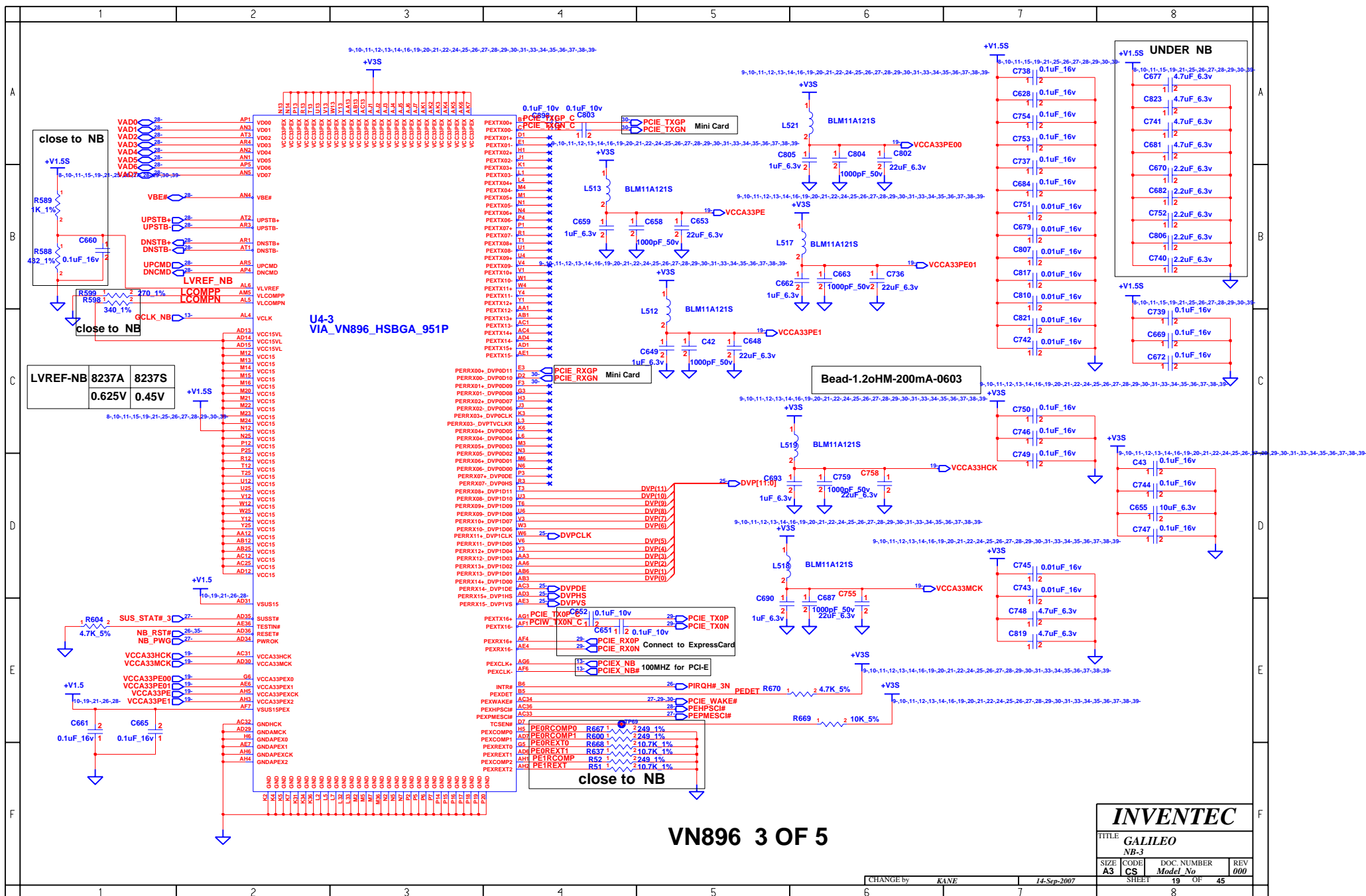
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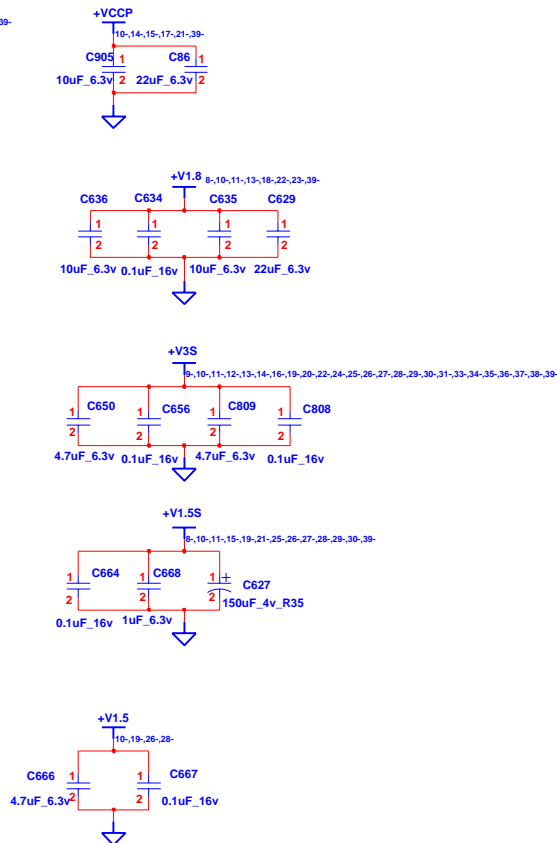
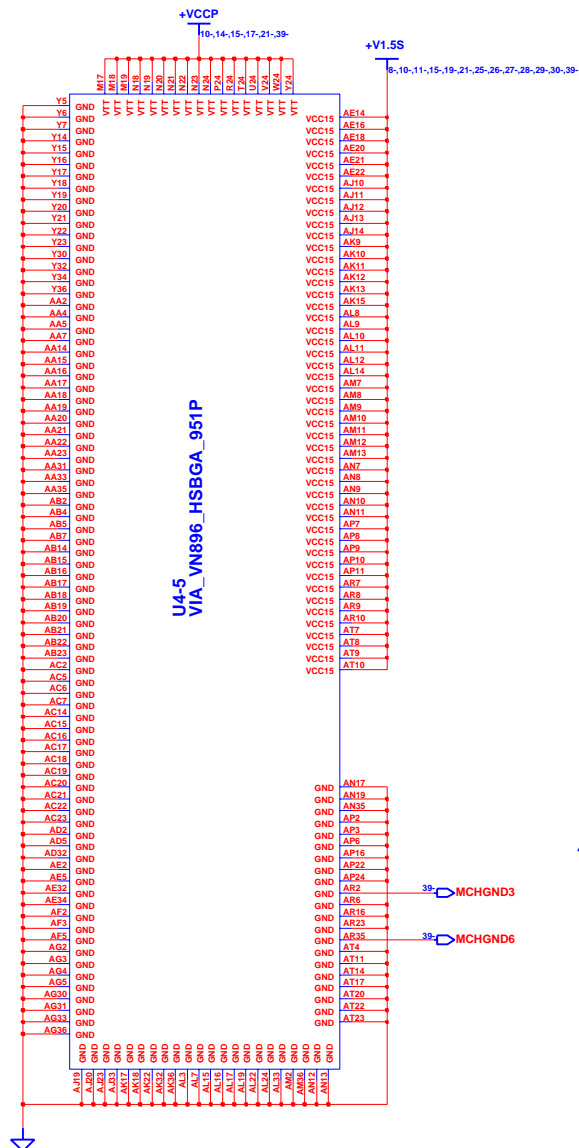
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8



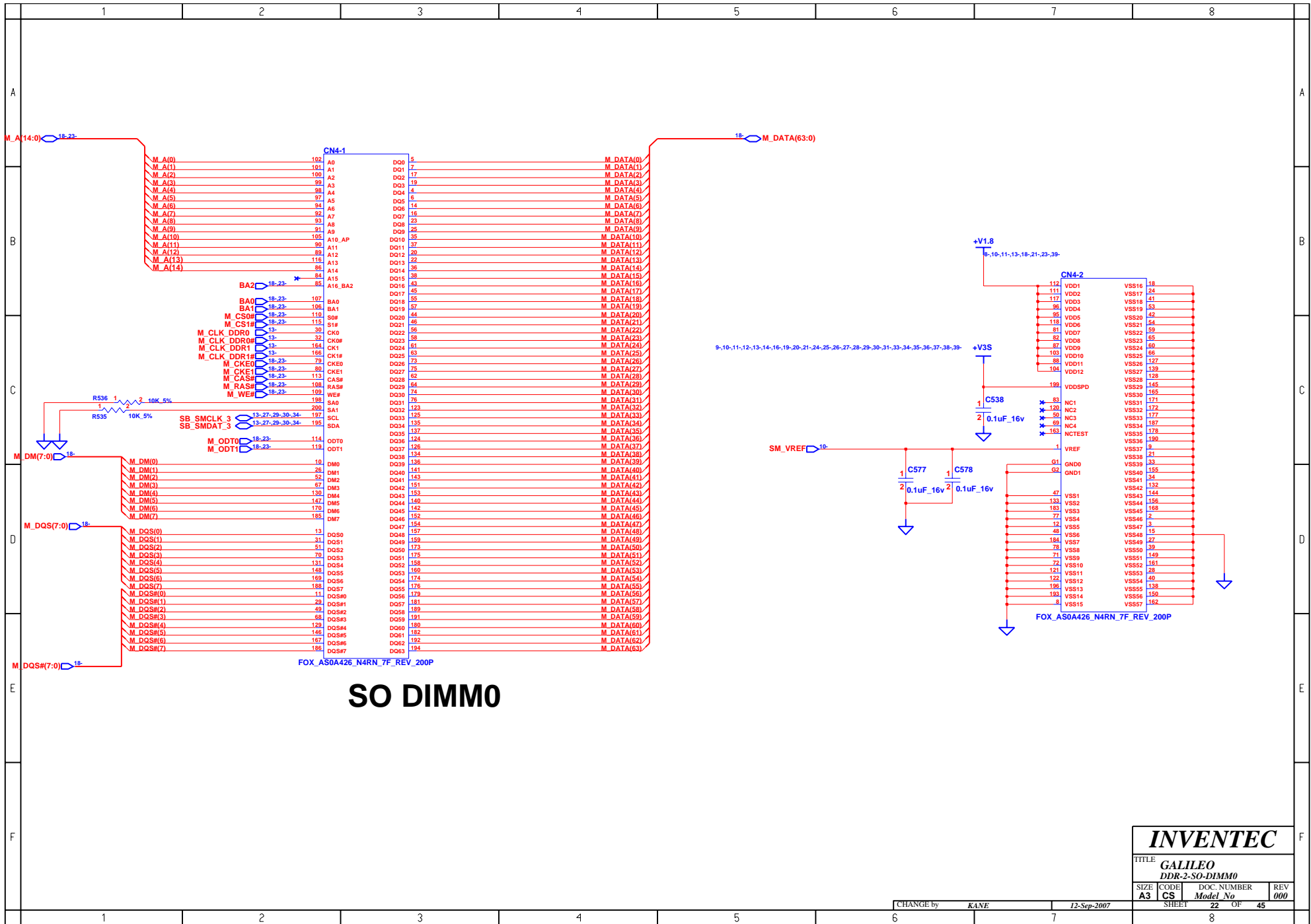


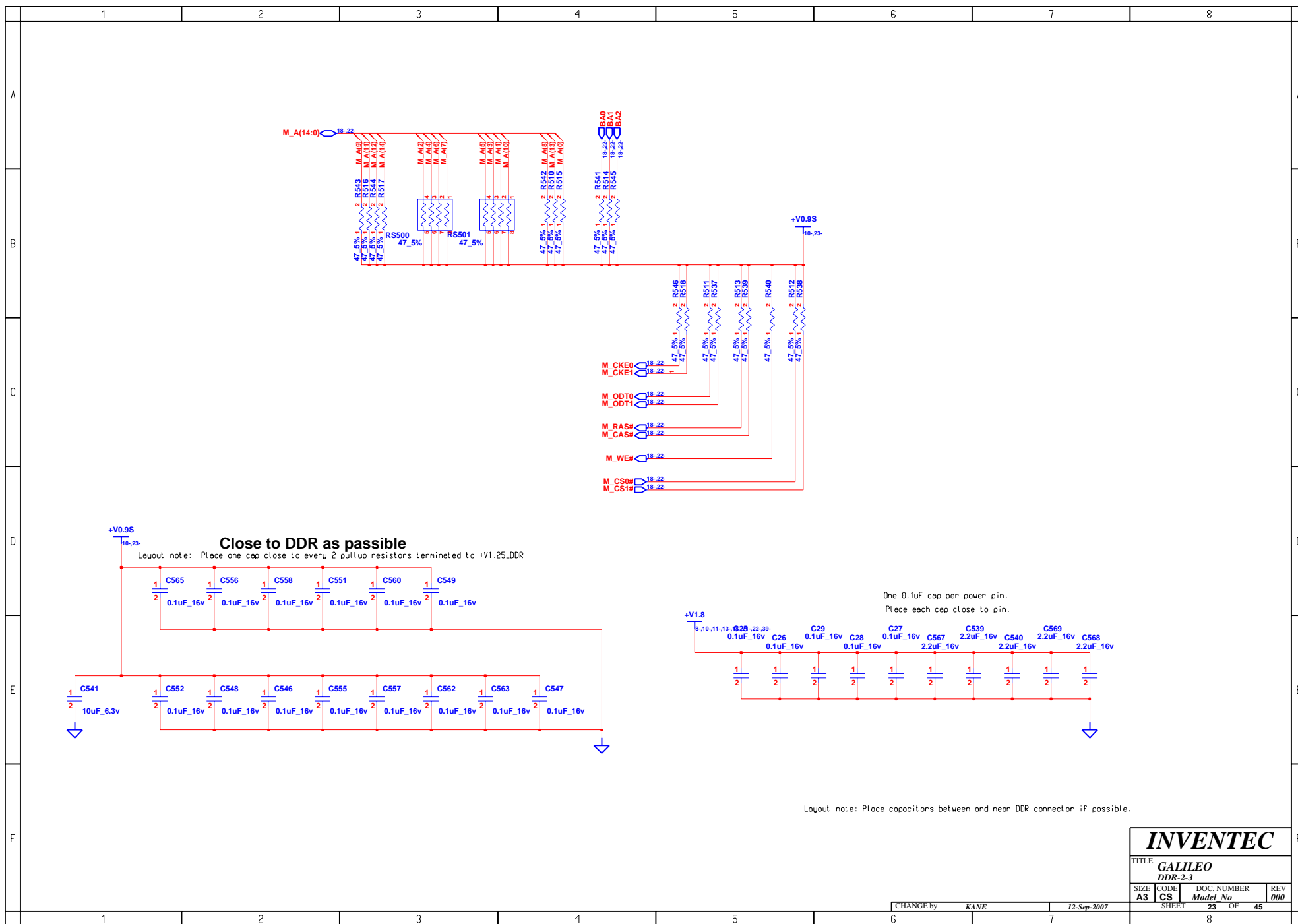


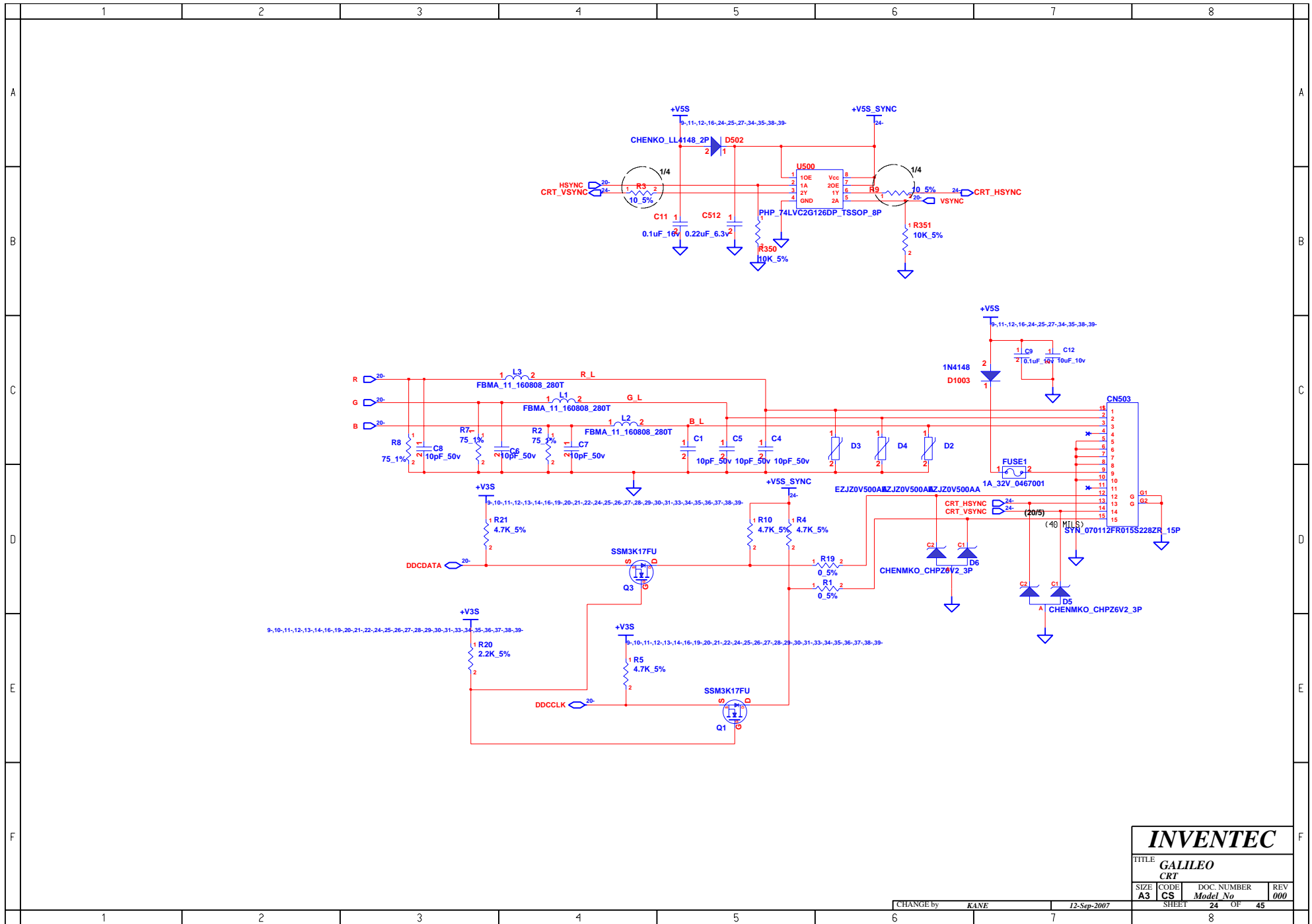


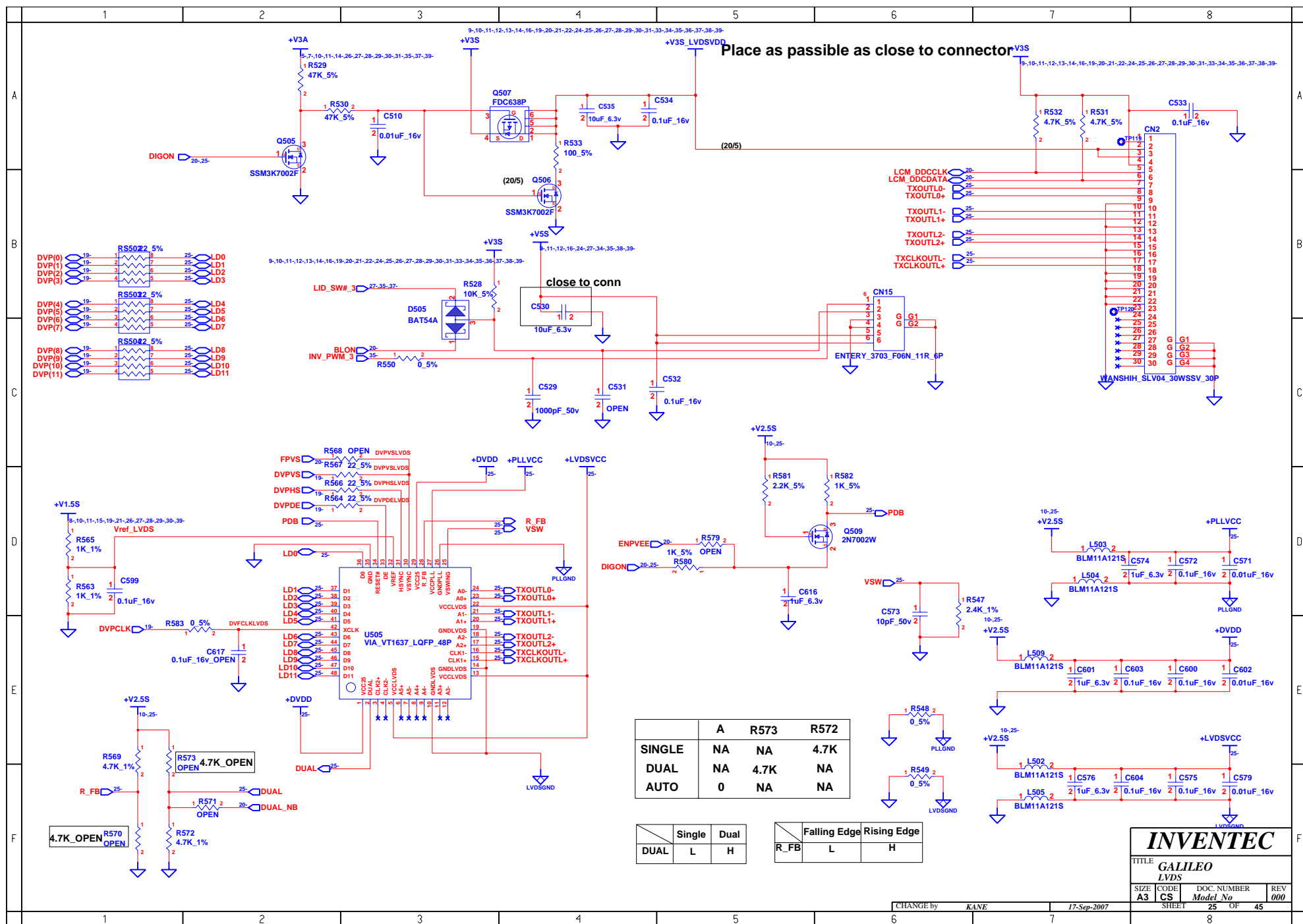
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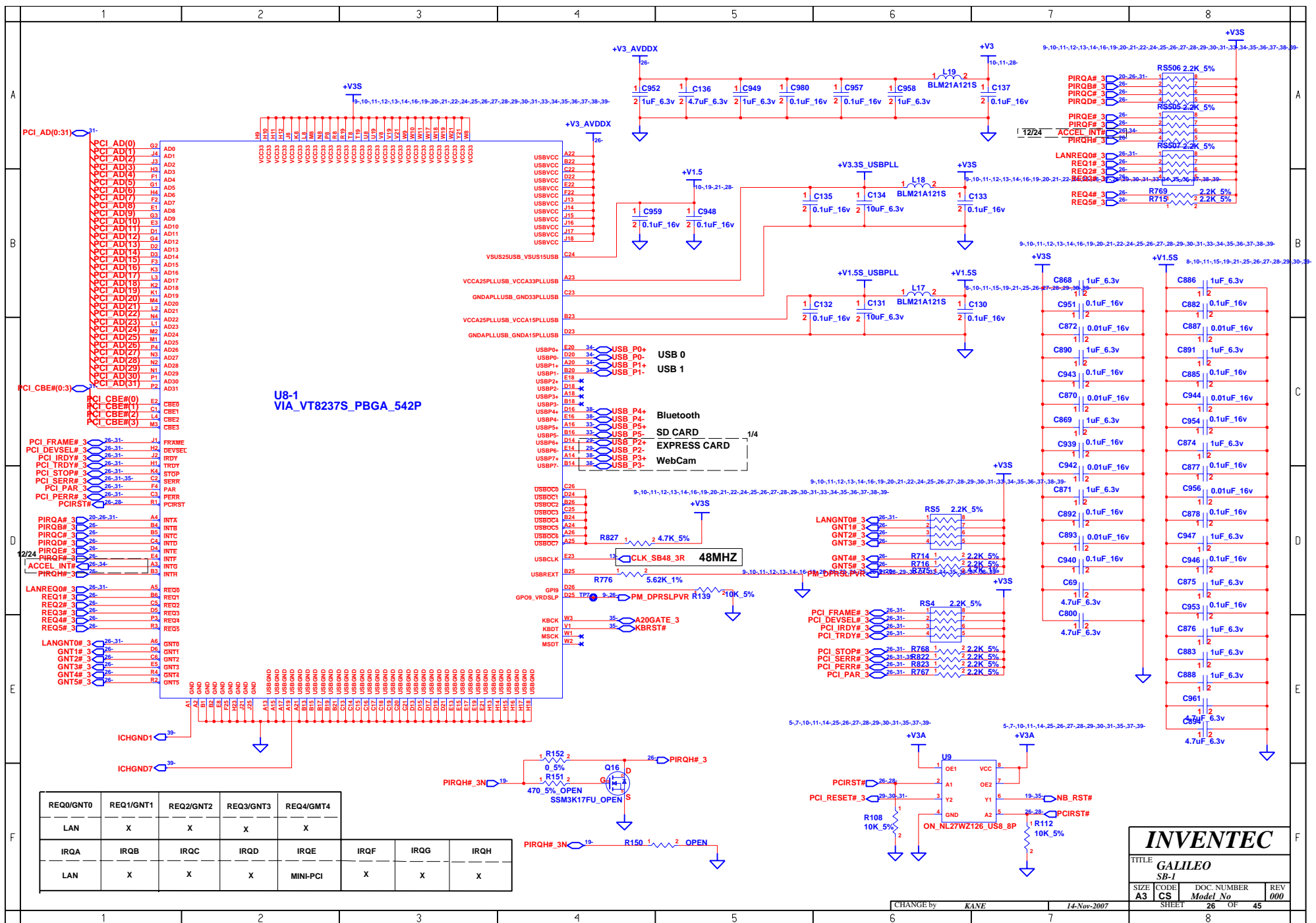
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TITLE GALILEO				
NB-5				
SIZE	CODE	DOC. NUMBER	REV	
A3	CS	Model No	000	
CHANGE by		18-Jul-2007	SHEET 21 OF 45	

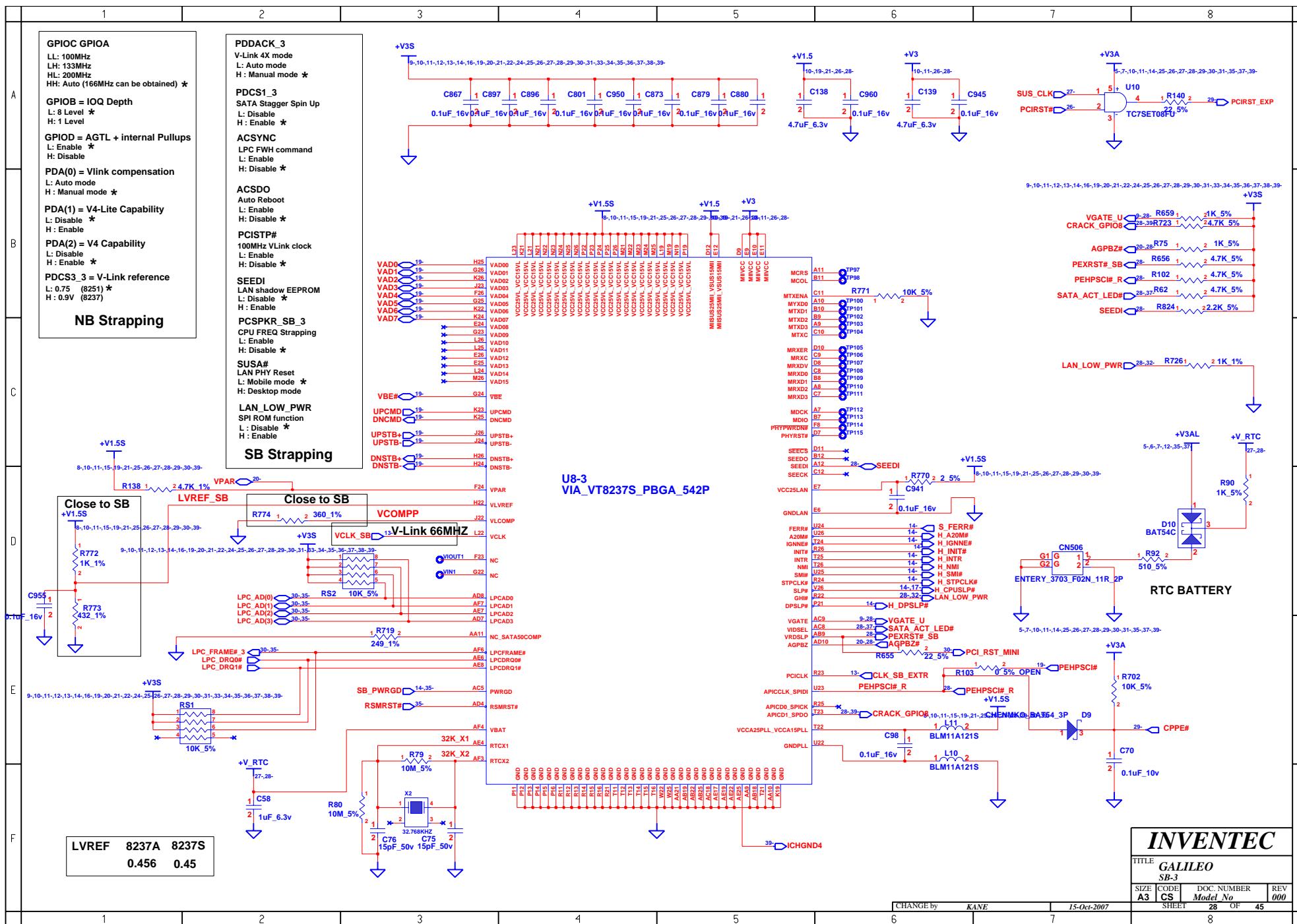


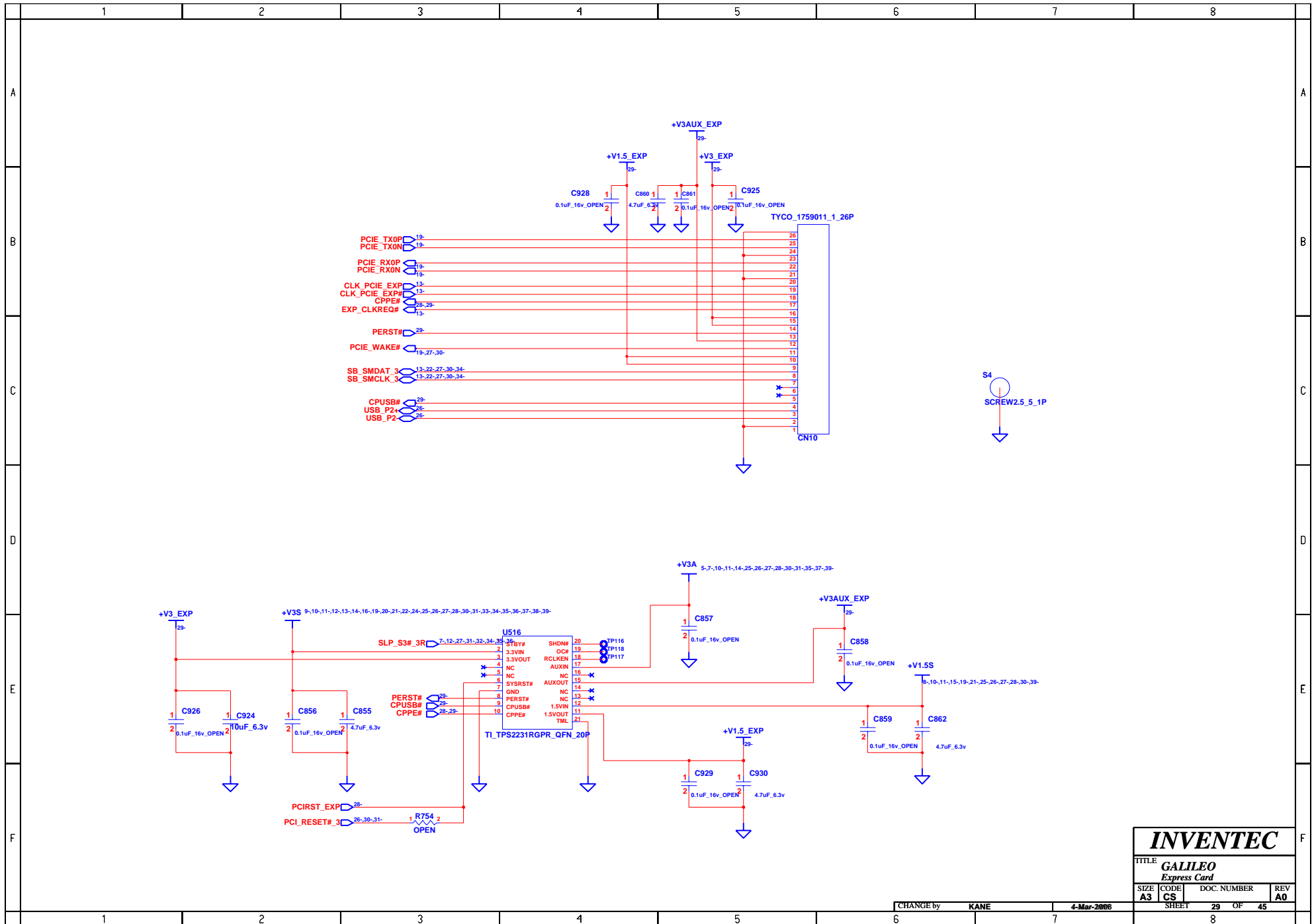






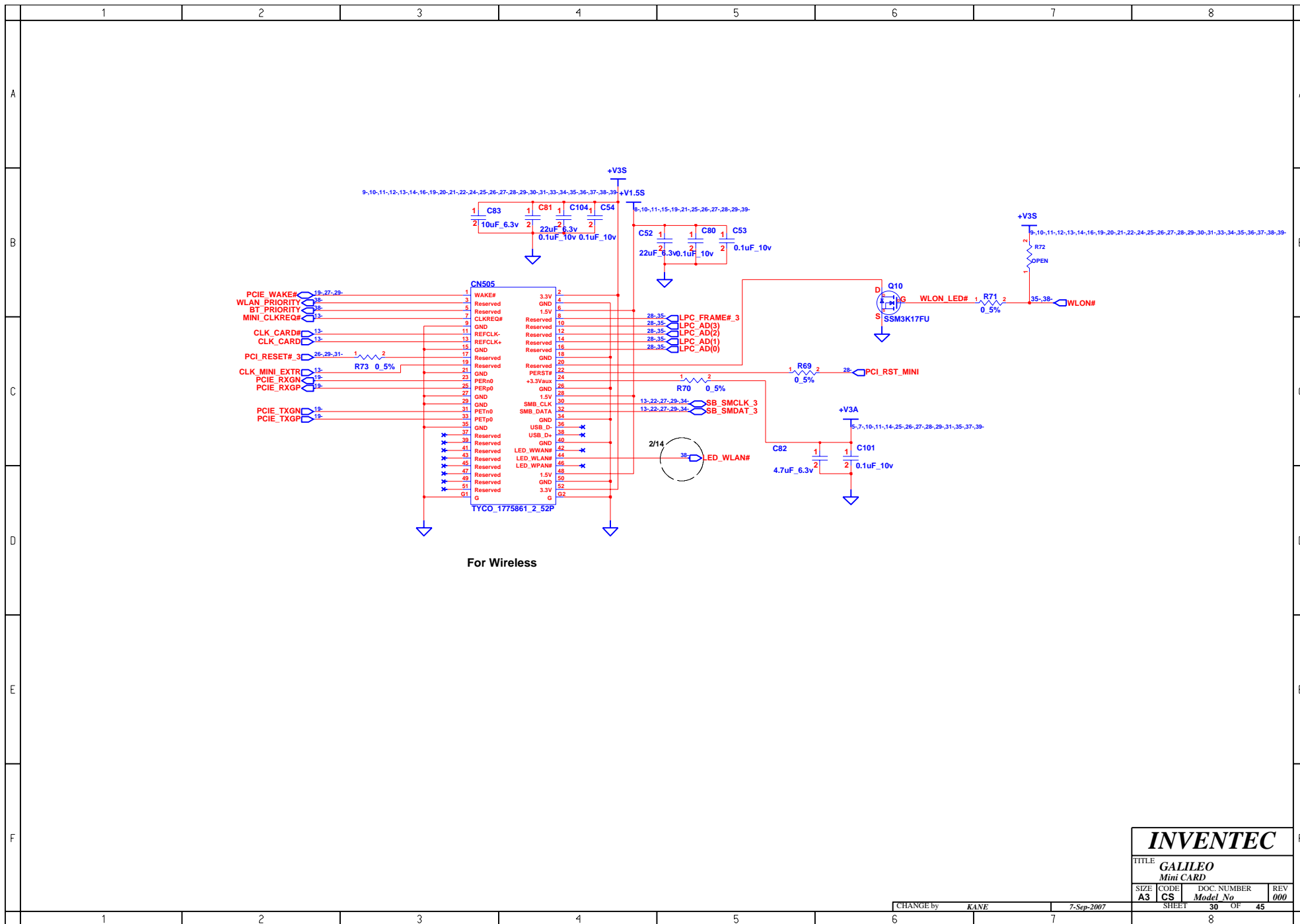






INVENTEC			
TITLE GALILEO Express Card			
SIZE A3	CODE CS	DOC. NUMBER 29 OF 45	REV A0

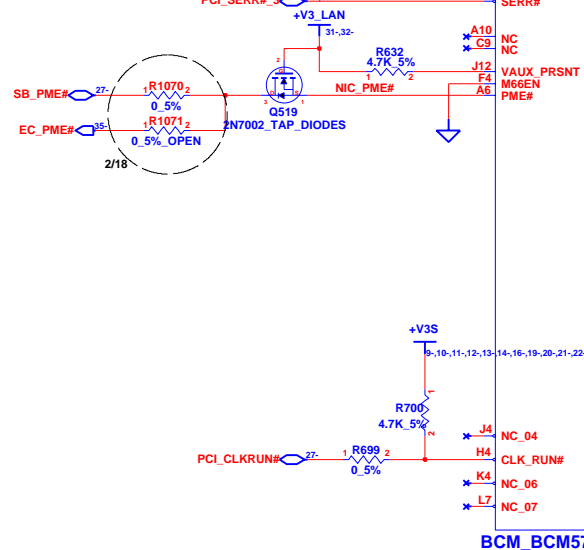
CHANGE by KANE 4-Mar-2008



INVENTEC			
TITLE GALILEO Mini CARD			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		7-Sep-2007	
SHEET 30		OF 45	

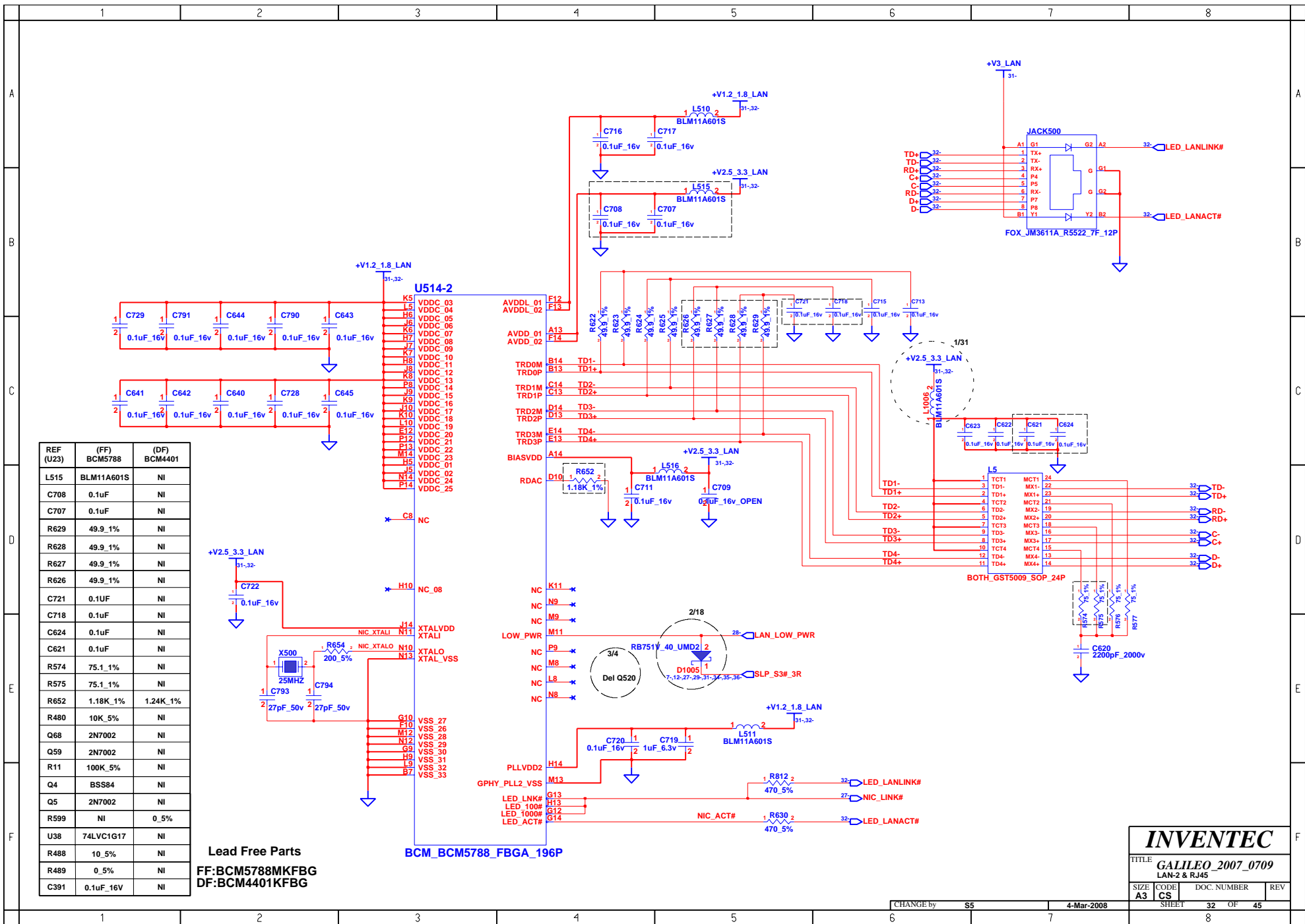
REF (U23)	(FF) BCM5788	(DF) BCM4401
R698	NI	0.5%
R701	0.5%	NI
Q517	BCP69	NI
Q516	BCP69	NI
R634	1K 5%	NI
R635	1K 5%	NI
R636	1K 5%	NI
U512	AT24C64A	NI
U30	NI	AT93C46A
R631	NI	0.5%
C724	0.01uF_16V	NI
C723	0.01uF_16V	NI
C726	0.01uF_16V	NI

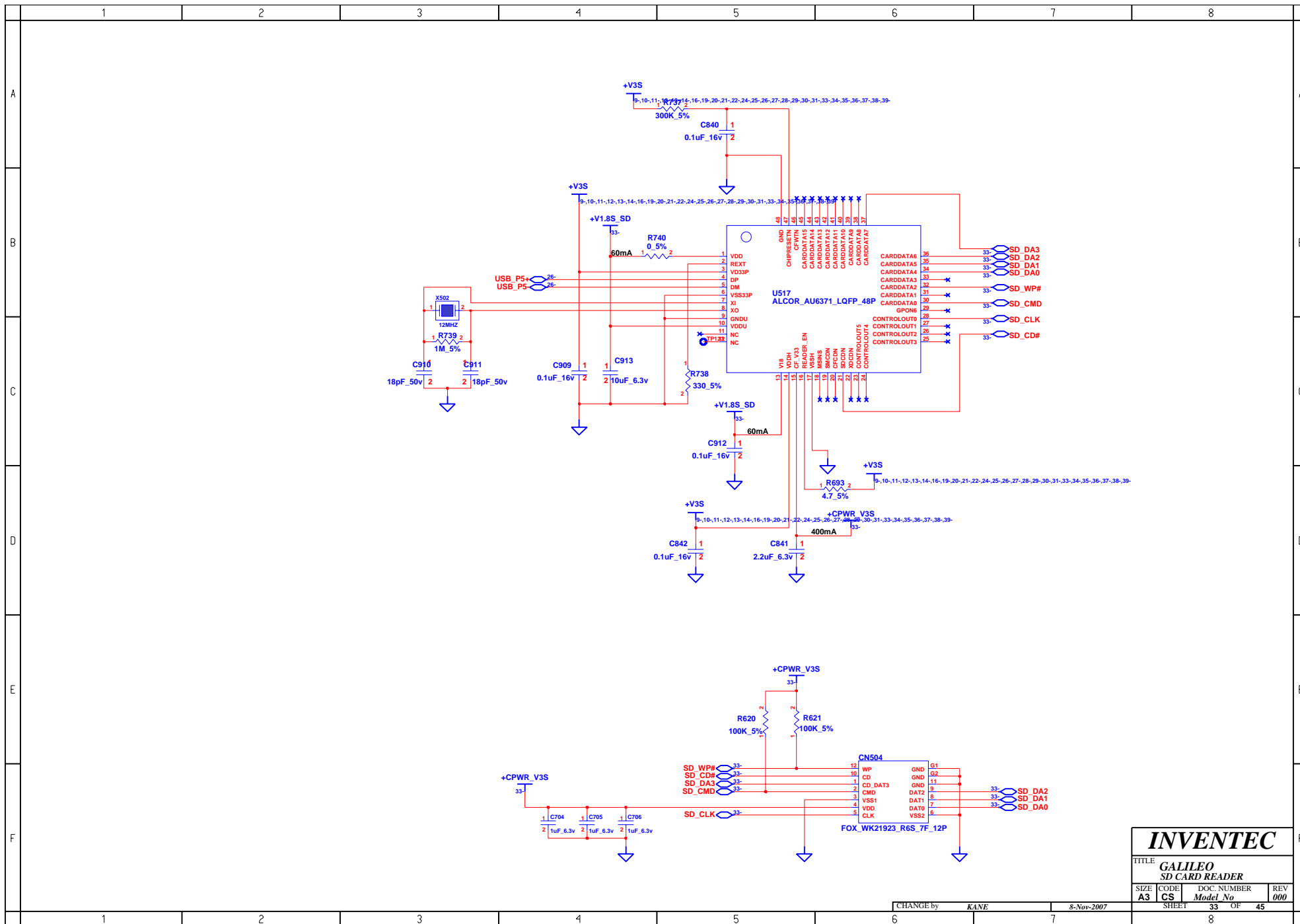
Lead Free Parts
FF:BCM5788MKFBG
DF:BCM4401KFBG

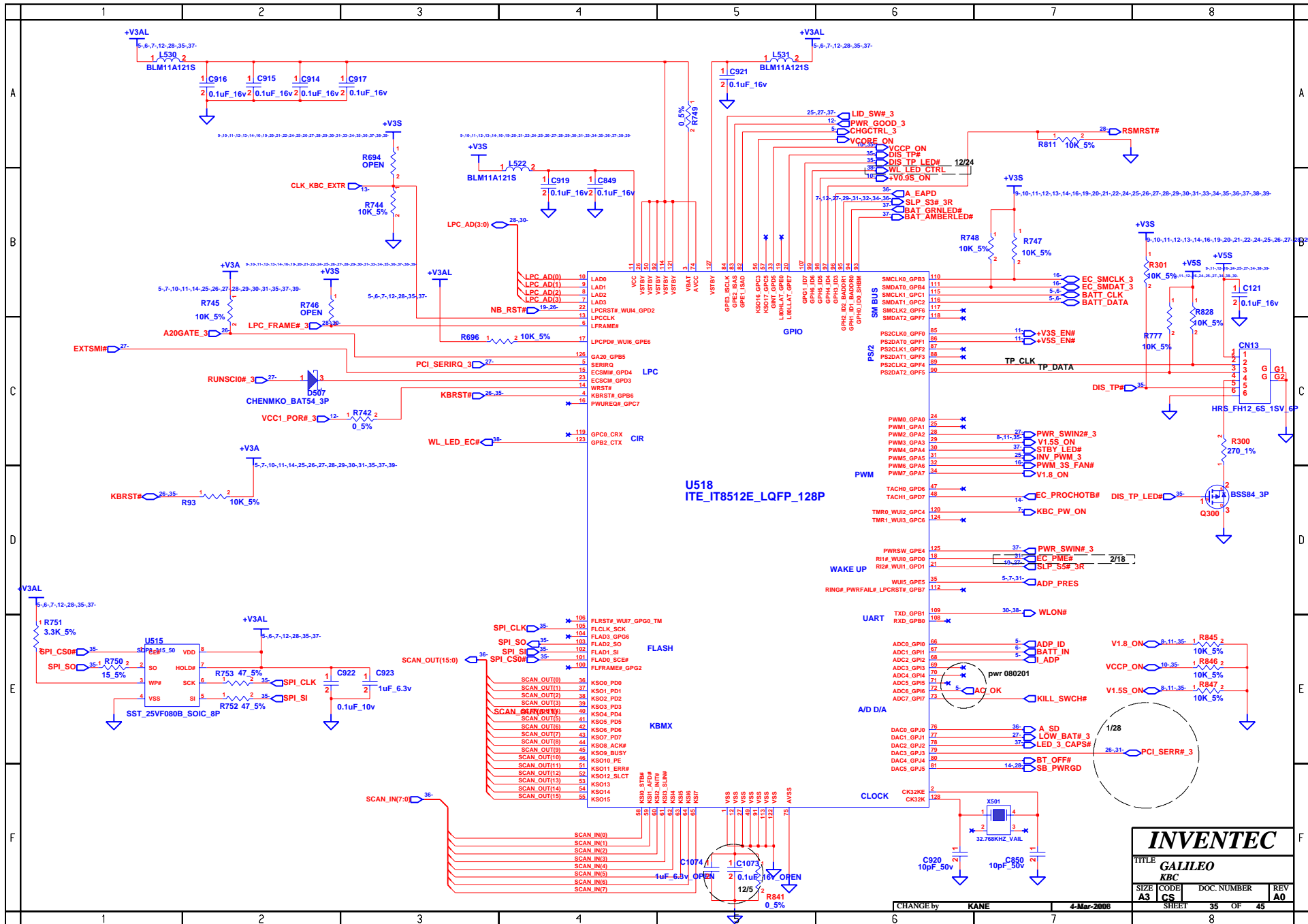


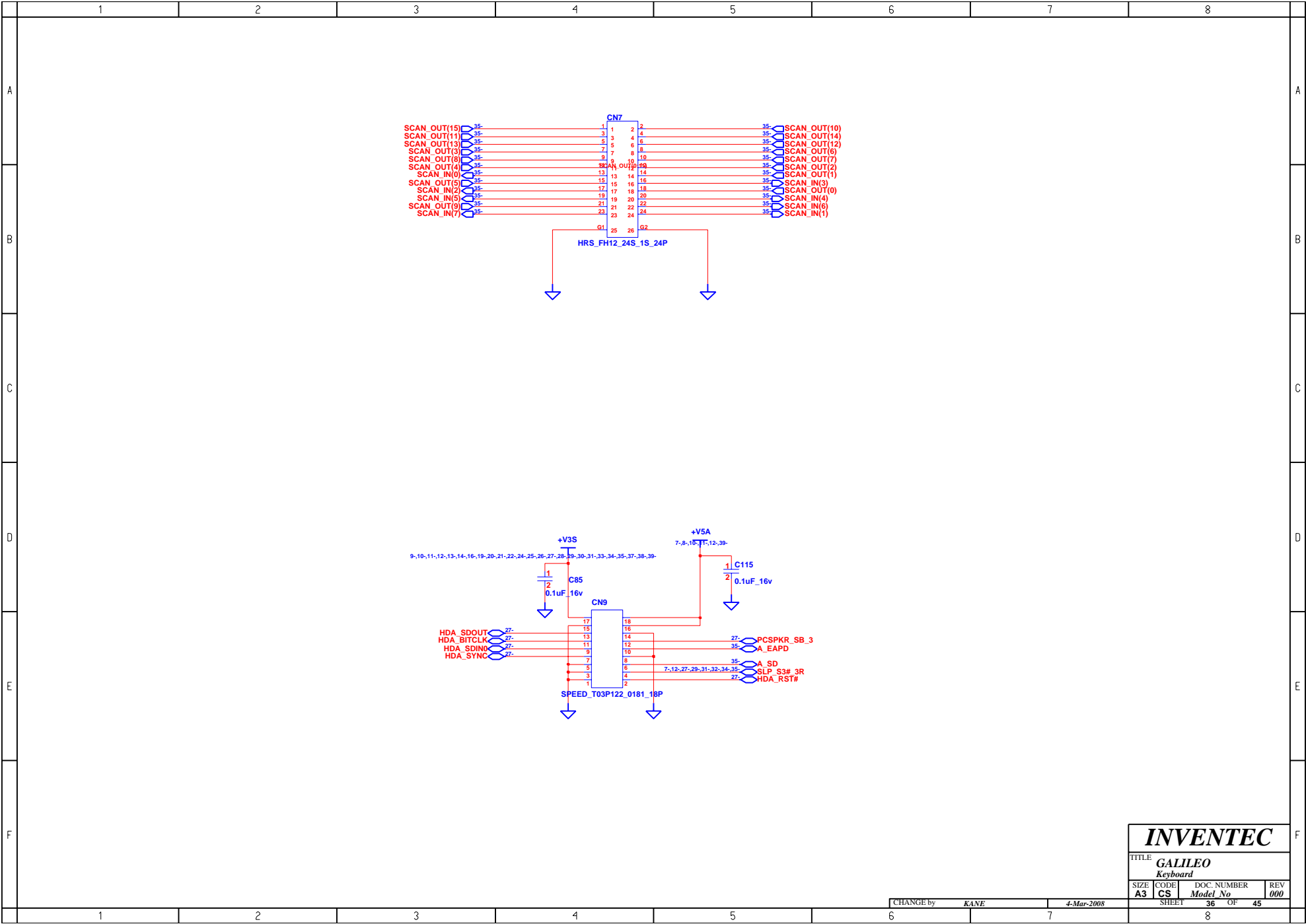
BCM_BCM5788_FPGA_196P

INVENTEC			
TITLE GALILEO LAN-1			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		4-Mar-2008	
SHEET 31		OF 45	









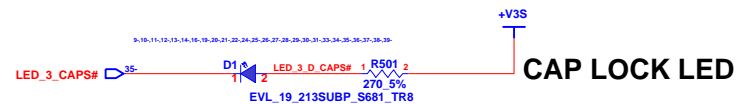
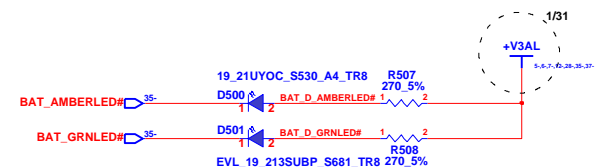
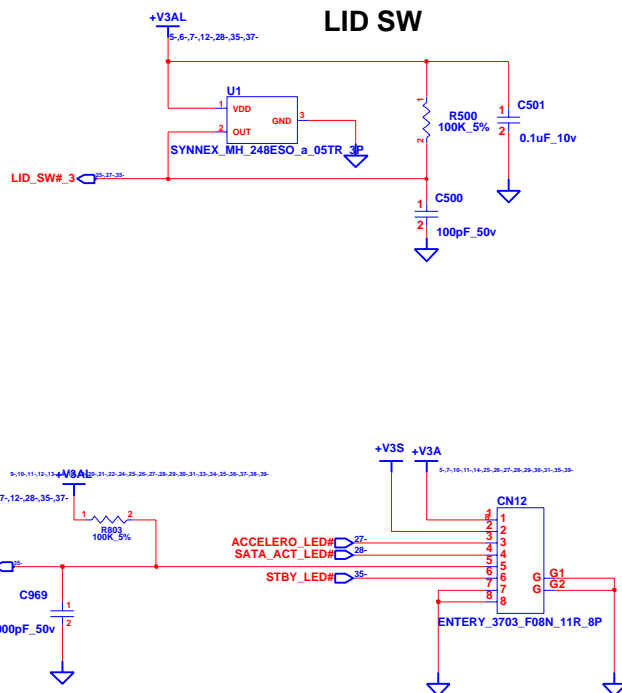
INVENTEC

TITLE
GALILEO
Keyboard

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

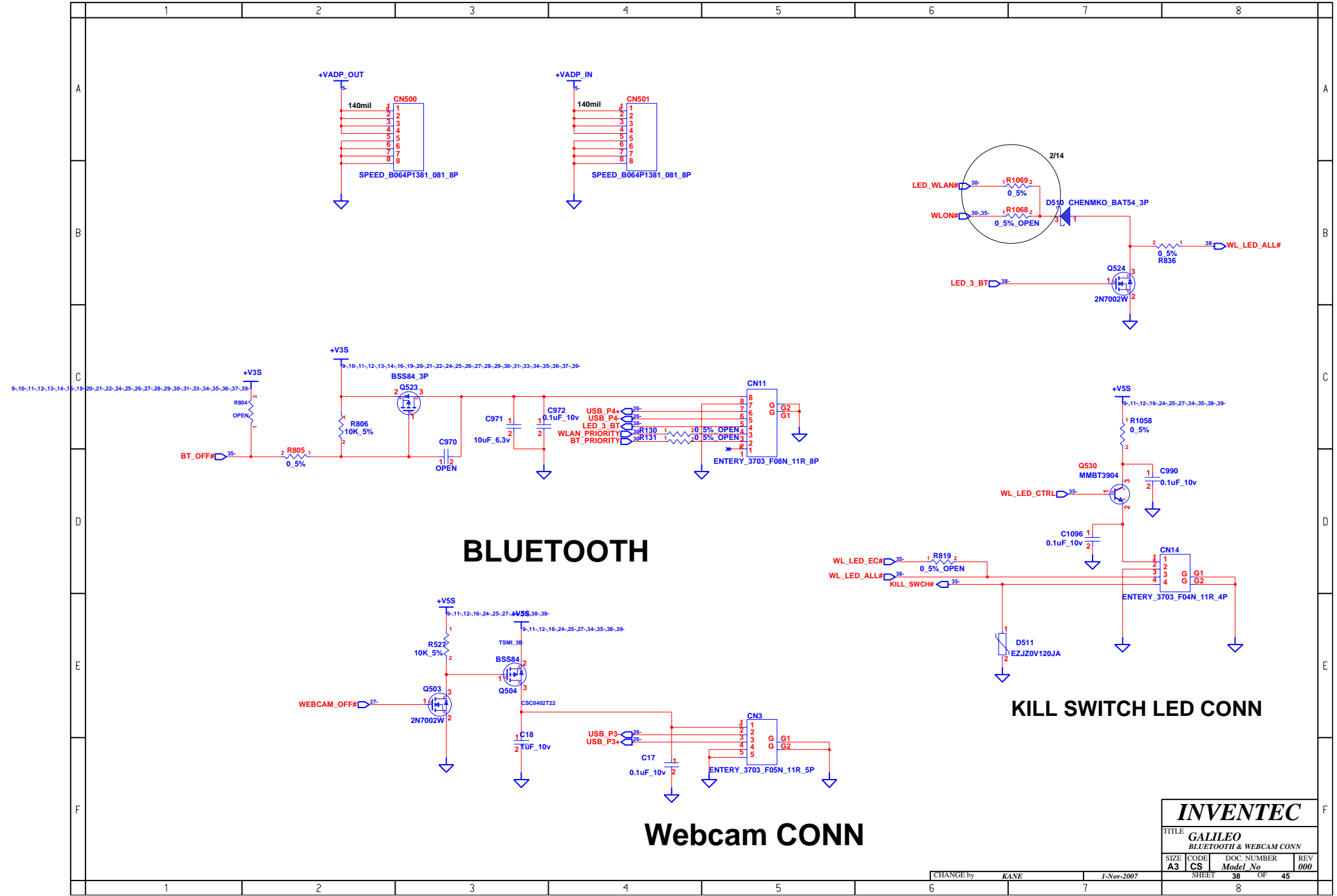
SHEET	OF	45
36	8	

CHANGE by KANE 4-Mar-2008



INVENTEC			
TITLE GALILEO-MB-X01-EE			
Button Board			
SIZE A3	CODE CS	DOC. NUMBER	REV
SHEET		37 OF	45

CHANGE by GALILEO 4-Mar-2008

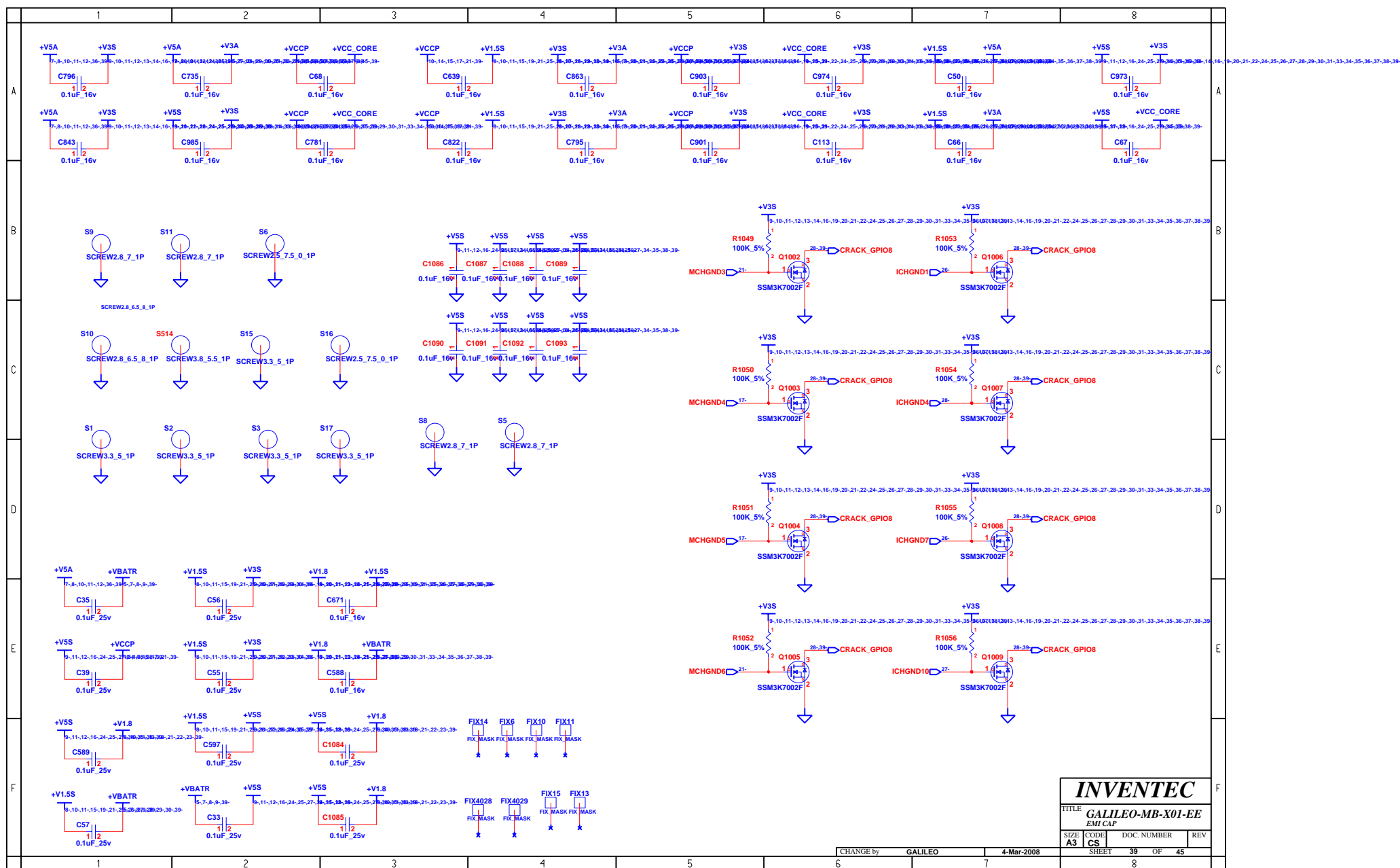


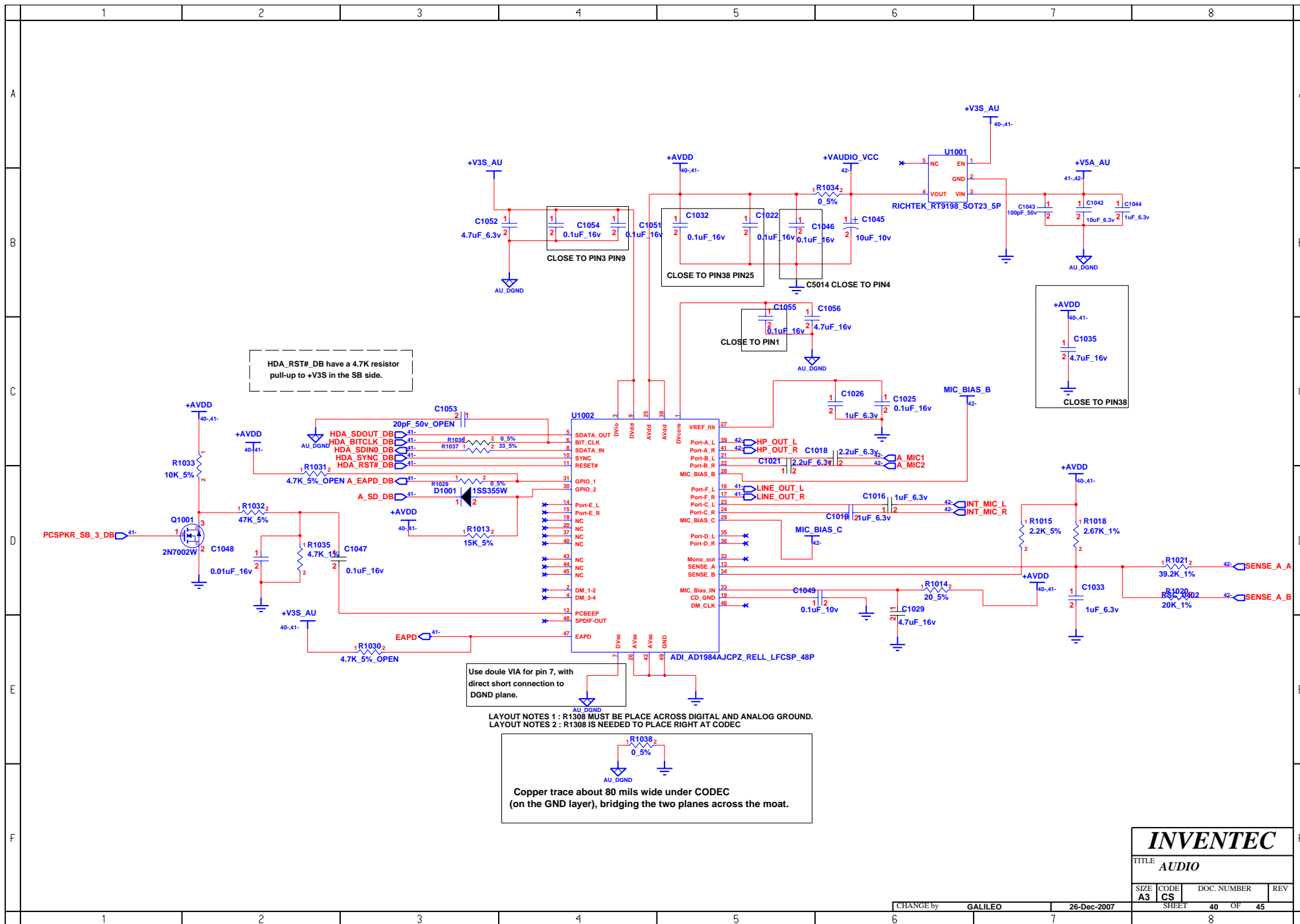
BLUETOOTH

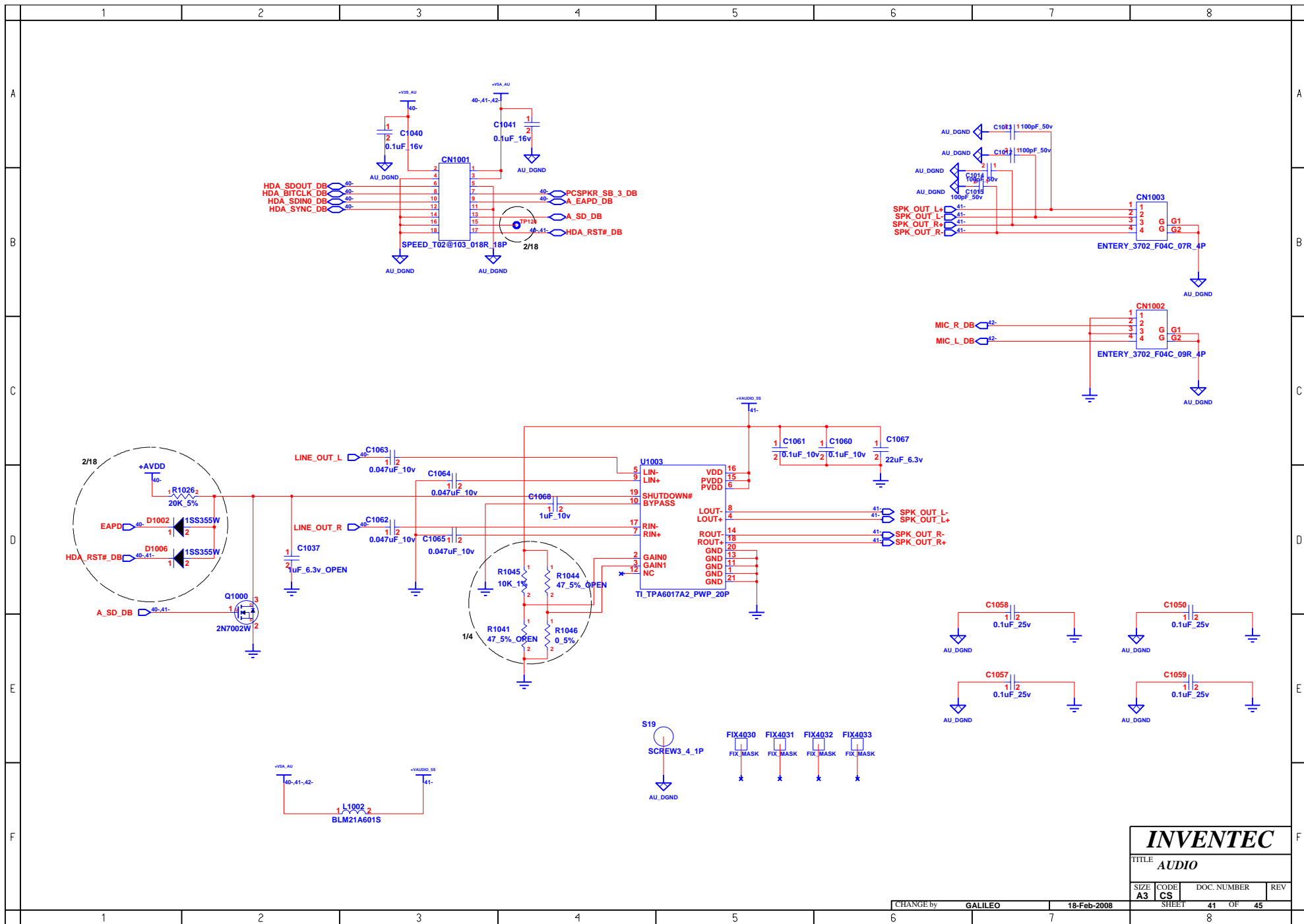
Webcam CONN

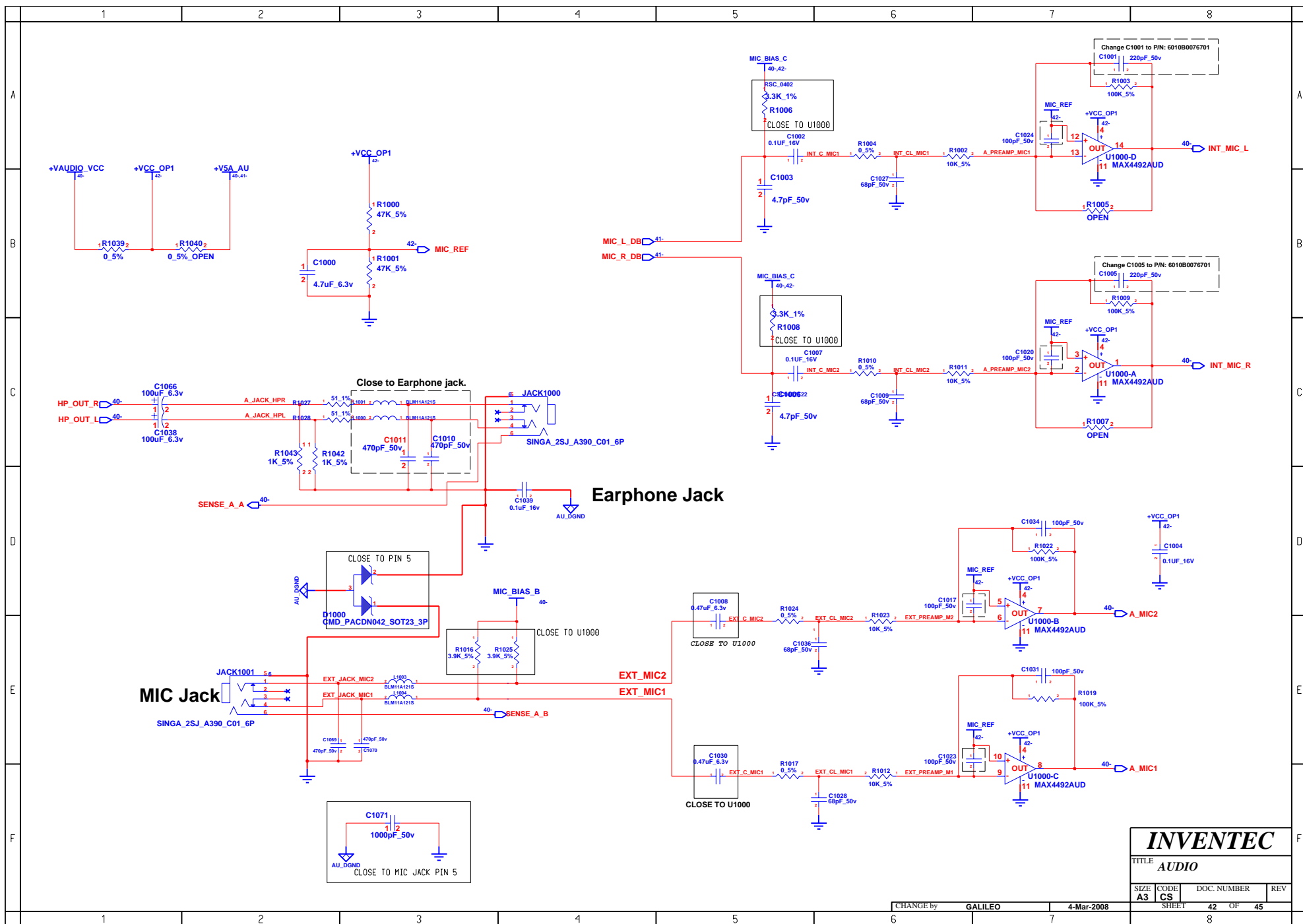
KILL SWITCH LED CONN

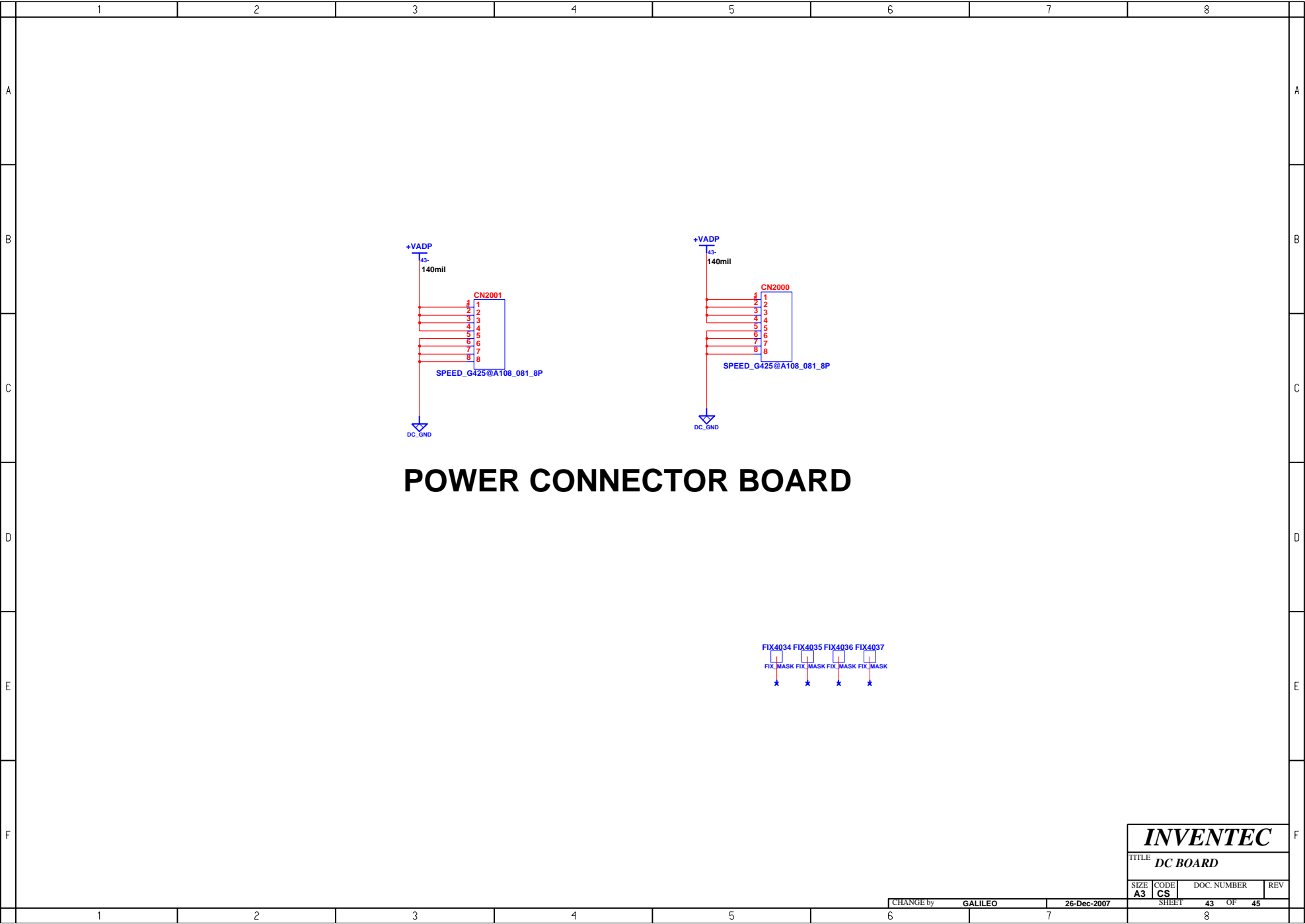
INVENTEC			
TITLE GALILEO BLUETOOTH & WEBCAM CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		1-Nov-2007	
SHEET 38		OF 45	











POWER CONNECTOR BOARD

INVENTEC			
TITLE DC BOARD			
SIZE A3	CODE CS	DOC. NUMBER	REV
CHANGE by GALILEO		26-Dec-2007	
SHEET 43		OF 45	

	1	2	3	4	5	6	7	8							
A	VERSION DB TO SI				VERSION SI-2 TO PV					A					
	10/30 1. PAGE 8 --- change R57 to 11.3K 2. PAGE 9 --- Add R28~R35 (0 ohm_5%) 3. PAGE 9 --- Add C1072 (330uF_OPEN) . Add a POWERPAD 4. PAGE 11 --- Delete R17(47ohm) , R15 (100K), C17 (0.1uF_25V) 5. PAGE 11 --- Delete Q753, Q748(NMOS), Q4 (PMOS) 6. PAGE 11 --- Add a PAD and connects +V3A and +V3 7. PAGE 14 --- Q510, Q514 change to NMOS (SSM3K17FU) 8. PAGE 28 --- change R659 to 1K 9. PAGE 28 --- change SEEDI from GND to +V3S 10. PAGE 34 --- change C700,C606 to 150uF_6.3V 11. PAGE 20 --- change R673 to OPEN, change R736 to 10K_1% 12. PAGE 35 --- change U515 to SST25V080B (1M FLASH) 13. PAGE 38 --- change pin1 from +V3A to +V3S				11/16 1 PAGE 5 --- D503 change to PDS1040S 2. PAGE 9 --- C987 change to 22nF ; R128 change to 200ohm 3. PAGE 24 --- C11 change to 0.1uF_6.3V ; C512 change to 0.22uF_6.3V 4. PAGE 24 --- R3,R9 change to 33ohm ; R5, R21 change to 4.7Kohm 5. PAGE 24 --- U500 change to 74LVC2G126DP 6. PAGE 24 --- Q1,Q3 change to SSM3K17FU 7. PAGE 24 --- Add R350,R351 (10Kohm) ; Add R121 (100Kohm) 8. PAGE 24 --- D502 change to LL4148 9. PAGE 24 --- Delete R6,R22 (10ohm) 10. PAGE 24 --- Change R1,R19 to 0 ohm ; R4,R10 change to 4.7Kohm					12/21 1. PAGE 6 --- CN1 change geometry 2. PAGE 27 --- +VBATR change to +V3S 3. PAGE 27 --- Delete Q303 , R307 4. PAGE 29 ---U516 change geometry 5. PAGE 40 ---U1002 change geometry 6. PAGE 37 ---U1 change to MH248ES0					
B	11/1 1. PAGE 25 --- Add CN15 (6 pin connector) for LVDS 2. PAGE 38 --- change CN14 to 4pin connector				11/19 1. PAGE 31 --- R698, R701 change size to 0805 2. PAGE 41 --- C1050,C1057,C1058,C1059 change to 1000pF_50V					B					
	11/2 1. PAGE 41 --- change 8 pin connector to 4pin connector*2 for SPK and MIC 2. PAGE 28 --- Delete R89 ; change D10 to BAT54C 3. PAGE 28 --- Change R90 to 1K ohm ; change R92 to 510 ohm.				11/21 1. PAGE 12 --- Delete U513, C918, R741 2. PAGE 12 --- Add D600 (CHENMDO_BAT54_3P) 3. PAGE 39 --- S14change to SCREW3.8_5.5 size 4. PAGE 41 --- S18,S19 change to SCREW3_4 size										
C	11/5 1. PAGE 30 --- Add Q20(PMOS), change R23 to R1047(100K) 2. PAGE 37 --- Delete D509 3. PAGE 24 --- Add D1003				VERSION SI TO SI-2					C					
	11/6 1. PAGE 25 --- change pin 3,4 and pin 5,6 of CN15 2. PAGE 5 --- add R1048 3. PAGE 5 --- change c24 to 0.1UF 4. PAGE 5 --- change C16 to 2.2uF 5. PAGE 35 --- change KBRST#, A20GATE pull high to +V3A				11/26 1. PAGE 27 --- R110, R111 change to 4.7K_5% and pull high to +V3A 2. PAGE 27 --- Add Q17(2N7002DW) 3. PAGE 27 --- Add R6 (4.7K_5%) 4. PAGE 28 --- WL_OFF# change to WL_OFF 5. PAGE 30 --- Q20 Change to NMOS (2N7002W) 6. PAGE 30 --- WL_OFF# change to WL_OFF 7. PAGE 35 --- U518 change to IT8512E					12/25 1. PAGE 5 --- Del C31,C32,C105,C106 2. PAGE 5,9 --- Add C1075,C1076,C1077,C1078,C1081,C1082,C1083 3. PAGE 9 --- Add C1079,C1080 4. PAGE 9 --- Add R1057					
D	11/7 1. PAGE 6 --- R12 change to 100ohms 2. PAGE 6 --- add D1006 3. PAGE 6 --- add D1007 4. PAGE 6 --- change C15 47PF				12/3 1. PAGE 25 --- change CN15 pin name 2. PAGE 24 --- change CN503 (6012B0244701) 3. PAGE 23 --- change C539,C540,C567,C568,C569 (6010B0075501) 4. PAGE 14 --- R651 change to 220_5% 5. PAGE 5 --- change C3 (6010B067601) 6. PAGE 30 --- Delete Q13,Q20,R1047 7. PAGE 30 --- +V3S_WL change to +V3S 8. PAGE 11 --- R67 change to 470 ohm(60130B47102T) 9. PAGE 11 --- R95 change to 200 ohm(6013A0051201) 10. PAGE 25 --- R583 change to 0 ohm(60130B000002T) 11. PAGE 30 --- C83 change to 10 uF(6010B0044901)					12/27 1. PAGE 34 --- CN5 change to 1001-F12E-04R 2. PAGE 36 --- CN9 change to T03P122-0181 3. PAGE 41 --- CN1001(24pin) change to 18 pin					D
	11/8 1. PAGE 27 --- change ACCELERO_LED# pull high to +V3A				1/4 1. PAGE 9 --- Add 0.1uF capacitor in PAD503's pin1,2 2. PAGE 9 --- Add C1049,C1050,R144,C127 3. PAGE 26 --- Change USB port 4. PAGE 42 --- C1011,C1010 change to 470uF 5. PAGE 39 --- Add C1084~C1093. (10 pcs capacitor) 6. PAGE 40 --- EMI capacitor change to 0.1uF_25V 7. PAGE 35 --- V1.8_ON add 10Kohm to DGND 8. PAGE 41 --- Add R1045, R1046 ; Del R1041, R1044 9. PAGE 42 --- C1038,C1066 change to 100uF_6.3V 10. PAGE 42 --- Change MIC net name										
E	11/9 1. PAGE 38 --- LED_3 WLAN change to WLAN# 2. PAGE 38 --- Delete U520, KILL_SWCH#. R805 change to 0 ohm 3. PAGE 34 --- ACCEL_INT# change to ACCEL_INT#_L 4. PAGE 30 --- CN505 pin 44 NC. Delete U7, R71 change to 0 ohm 5. PAGE 27 --- Delete R115, R81 . Add Q303, R307. 6. PAGE 27 --- SMCLK, SMDAT change pull high to +V3S 7. PAGE 13 --- C981, C982 change to 15pF				1/7 1. PAGE 13 --- R833,R832 change to 22ohm ; Delete C981,C982 2. PAGE 38 --- Q530(NMOS change to BJT) ; Add C1096, R1085 3. PAGE 38 --- R1085 pin 1 connect to +V5S 4. PAGE 41 --- Delete C1037					1/8 1. PAGE 35 --- V1.5S_ON, VCCP_ON connect 10Kohm to GND					E
	11/14 1. PAGE 34 --- C606 change to 100uF_10V (Low ESR) 2. PAGE 34 --- C700 change to 150uF_6.3V (Low ESR) 3. PAGE 39 --- Del S1 4. PAGE 34 --- SWAP L520 D+ D- 5. PAGE 41 --- CHANGE CN1002 TO 6012B0239001 (BLACK) 6. PAGE 32 --- SWAP LED_LANACT# & LED_LANLINK# 7. PAGE 27 --- CHANGE R81 ,R115 TO 0402 TYPE				12/4 1. PAGE 9 --- delete PAD505										
F					12/5 1. PAGE 35 --- add C989(0.1uF),C990(1uF),R841(0 ohm) 2. PAGE 34 --- change CN8 3. PAGE 39 --- add S1										
					12/7 1. PAGE 34 --- change CN5 2. PAGE 39 --- Add Q1002~Q1009(SSM3k7002) 3. PAGE 39 --- Add R1049~R1056(100Kohm)										
										INVENTEC History SIZE CODE DOC. NUMBER REV A3 CS 44 OF 45					F
	1	2	3	4	5	6	7	8	CHANGE by GALILEO 4-Feb-2008 SHEET 44 OF 45 8						

INVENTEC

TITLE			
History			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		

	1	2	3	4	5	6	7	8	
A	<div>VERSION PV TO PVR</div> <div>1/28 1. PAGE 35 --- U518 PIN 79 connects to PCI_SERR#_3</div> <div>1/31 1. PAGE 32 --- L5 power +V2.5_3.3_LAN add BLM11A601S</div> <div>2. PAGE 37 --- D500 and D501 power change to +V3AL</div> <div>2/12 1. PAGE 27 --- U1005 and R1067 for the Susb# buffer</div> <div>2. PAGE 32 --- R1066 for the SLP_S3#_3R pull high</div> <div>2/14 1. PAGE30 --- Add a signal "LED_WLAN#" at CN505 pin 44</div> <div>2. PAGE38 --- Add R1069 and connect to LED_WLAN#</div> <div>2/18 1. PAGE35 --- Add a signal "EC_PME#" at pin 18</div> <div>2. PAGE31 --- Add 0 ohm resistor, and 0ohm_OPEN connect to EC_PME#</div> <div>3. PAGE6 --- Add a diode D1004</div> <div>4. PAGE32 --- Add a diode D1005</div> <div>5. PAGE41 --- Del SLP_S3#_3R_DB and add a test point.</div> <div>6. PAGE41 --- Add a diode D1006 and connect to HDA_RST#_DB</div> <div>7. PAGE41 --- R1026 pin1 change to connets +AVDD</div> <div>8. PAGE27 --- Add Q1010. R22</div>								A
B	<div>VERSION PVR TO MV</div> <div>3/4 1. PAGE 31 --- Add C1099</div> <div>2. PAGE 31 --- OPEN the R596, Q512</div> <div>3. PAGE 31 --- Del Q520</div>								B
C									C
D									D
E									E
F	<div>CHANGE by GALILEO 4-Mar-2008</div> <div><div>INVENTEC</div><div>TITLE GALILEO-6050A217490A-MB-A02-power</div><div><div>SIZE A3</div><div>CODE CS</div><div>DOC. NUMBER</div><div>REV</div><div>SHEET 45 OF 45</div></div></div>								F
	1	2	3	4	5	6	7	8	