

<https://eleccelerator.com/fusecalc/fusecalc.php?chip=attiny45&LOW=62&HIGH=DF&EXTENDED=F9&LOCKBIT=FF>

Select Chip: ATTiny45 (current) Go

LOW Fuse Presets:
 Int. RC Osc. 8 MHz; Start-up time PWRDWN/RESET: 6 CK/14 CK + 64 ms; [CKSEL=0010 SUT=10]; default value
☐ Clock output on PORTB4; [CKOUT=0]
☒ Divide clock by 8 internally; [CKDIV8=0]

HIGH Fuse Presets:
 Brown-out detection disabled; [BODLEVEL=111]
☐ Debug Wire enable; [DWEN=0] *
☐ Preserve EEPROM memory through the Chip Erase cycle; [EESAVE=0]
☐ Reset Disabled (Enable PB5 as i/o pin); [RSTDISBL=0] *
☒ Serial program downloading (SPI) enabled; [SPIEN=0] *
☐ Watch-dog Timer always on; [WDTON=0]

EXTENDED Fuse Presets:
☐ Self Programming enable; [SELFPRGEN=0]

LOCKBIT Fuse Presets:
 Mode 1: No memory lock features enabled

Manual Fuse Bit Manipulation
 Remember: ☒ = programmed = 0; ☐ = unprogrammed = 1

Bit	LOW	HIGH	EXTENDED	LOCKBIT
7	<input checked="" type="checkbox"/> CKDIV8	<input type="checkbox"/> RSTDISBL *	<input type="checkbox"/> Bit 7	<input type="checkbox"/> Bit 7
6	<input type="checkbox"/> CKOUT	<input type="checkbox"/> DWEN *	<input type="checkbox"/> Bit 6	<input type="checkbox"/> Bit 6
5	<input type="checkbox"/> SUT1	<input checked="" type="checkbox"/> SPIEN *	<input type="checkbox"/> Bit 5	<input type="checkbox"/> Bit 5
4	<input checked="" type="checkbox"/> SUT0	<input type="checkbox"/> WDTON	<input type="checkbox"/> Bit 4	<input type="checkbox"/> Bit 4
3	<input checked="" type="checkbox"/> CKSEL3	<input type="checkbox"/> EESAVE	<input type="checkbox"/> Bit 3	<input type="checkbox"/> Bit 3
2	<input checked="" type="checkbox"/> CKSEL2	<input type="checkbox"/> BODLEVEL2	<input checked="" type="checkbox"/> Bit 2	<input type="checkbox"/> Bit 2
1	<input type="checkbox"/> CKSEL1	<input type="checkbox"/> BODLEVEL1	<input checked="" type="checkbox"/> Bit 1	<input type="checkbox"/> Bit 1
0	<input checked="" type="checkbox"/> CKSEL0	<input type="checkbox"/> BODLEVEL0	<input type="checkbox"/> SELFPRGEN	<input type="checkbox"/> Bit 0
Default	0x6A	0xDF	0xFF	0xFF
Apply	0x62 → 0xE2	0xDF	0xF9	0xFF

AVRDUDE

-U lfuse:w:0x62:m

-U hfuse:w:0xDF:m

-U efuse:w:0xF9:m

-U lock:w:0xFF:m

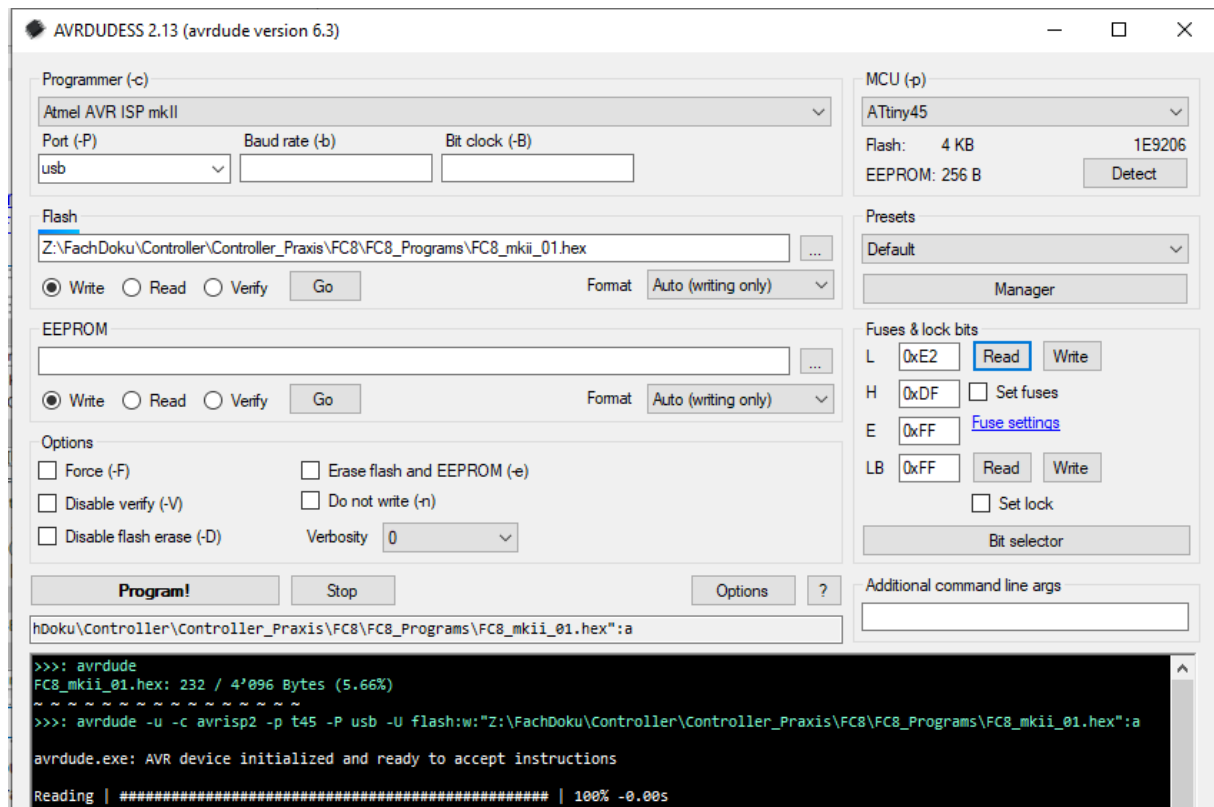
Compiler | Linker / Assembler | **Programmer** | File Viewer

Location:
 Browse...

Parameters:

-c avrisp2 -p t45 -P usb -U

flash:w:"Z:\FachDoku\Controller\Controller_Praxis\FC8\FC8_Programs\FC8_mkii_01.hex":a



FlowCode 8 Example

