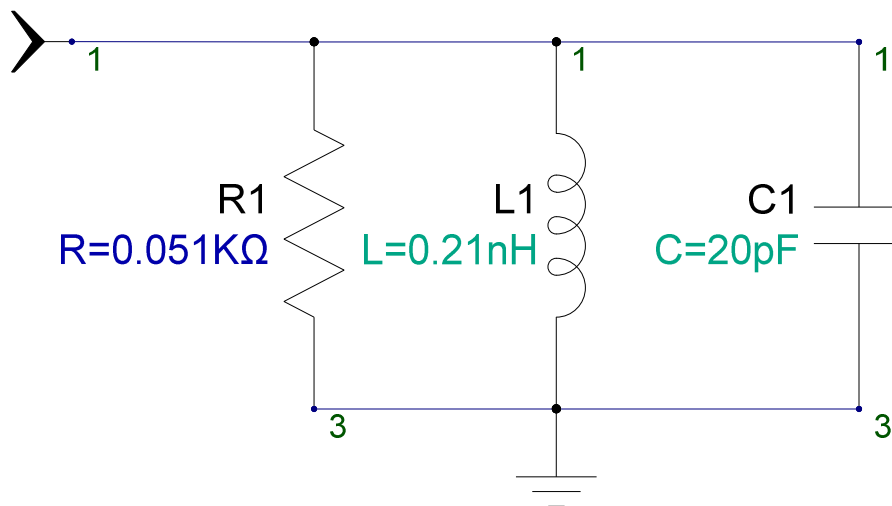
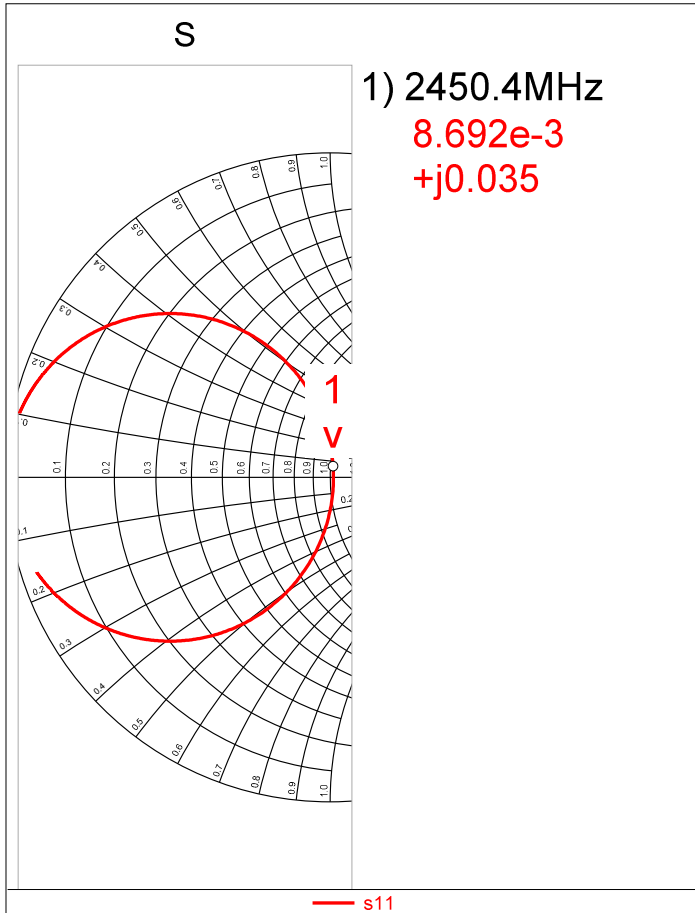


Port_1
 $Z_0=50\Omega$



Linear1_S



Linear1_S11

