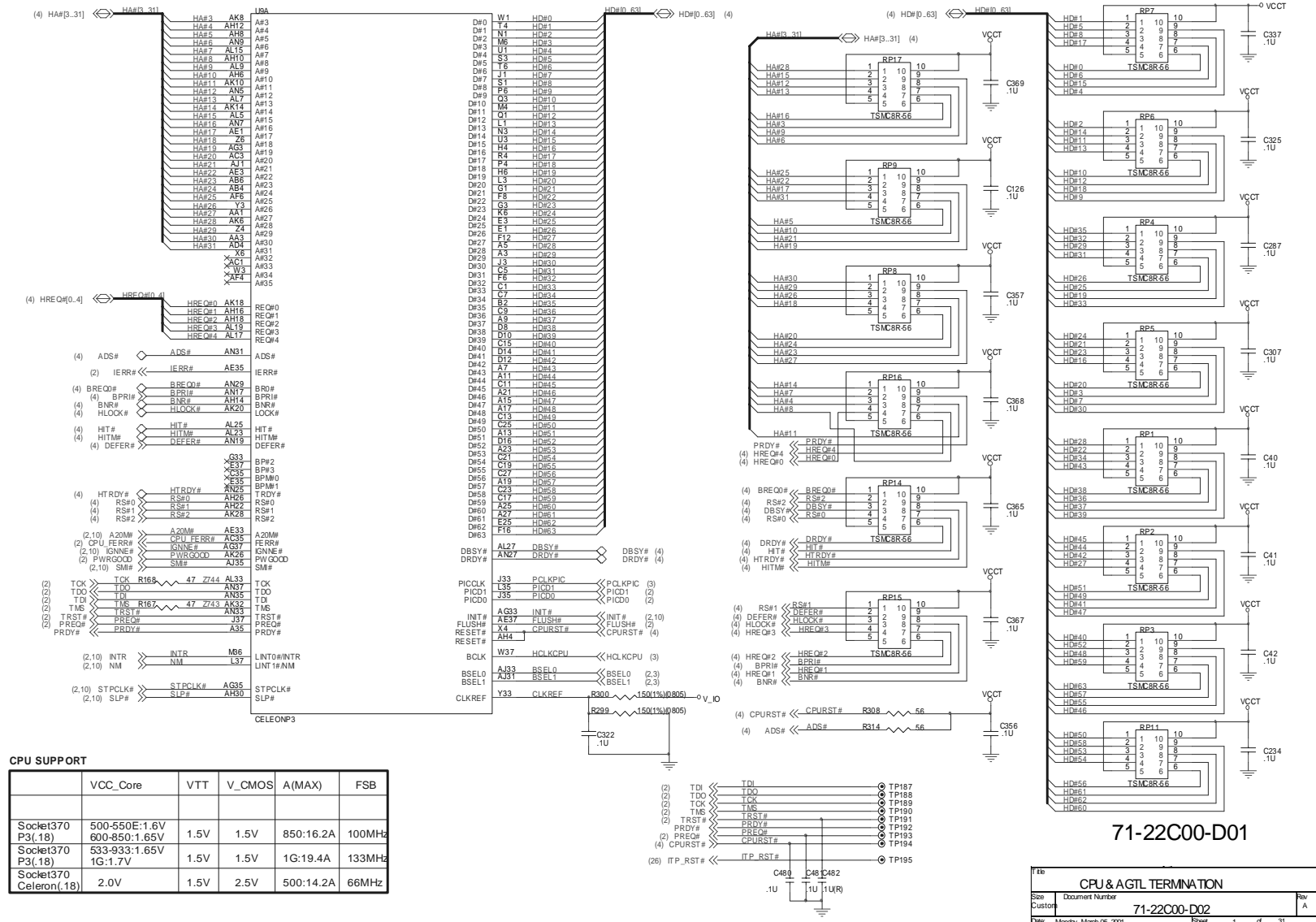
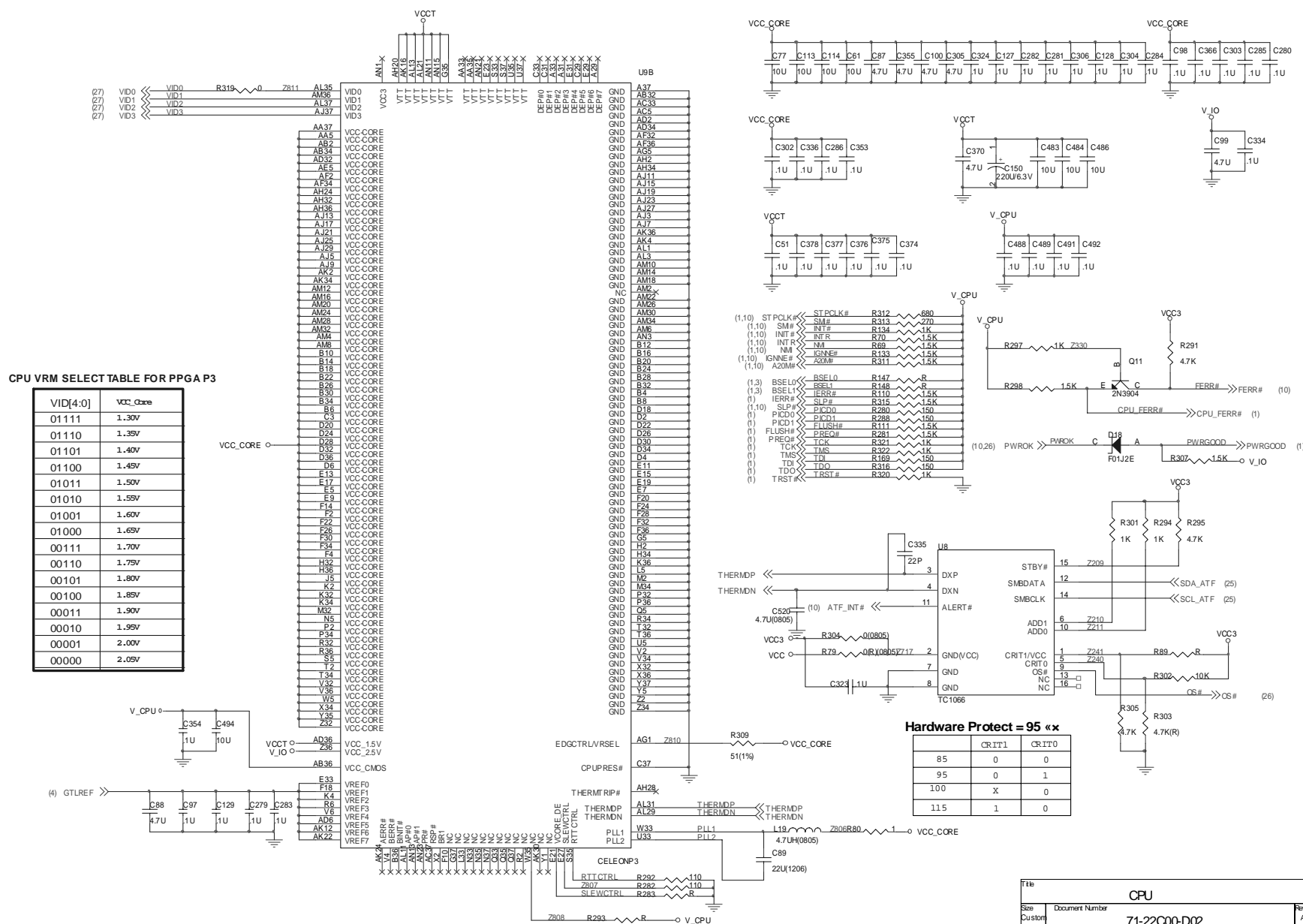
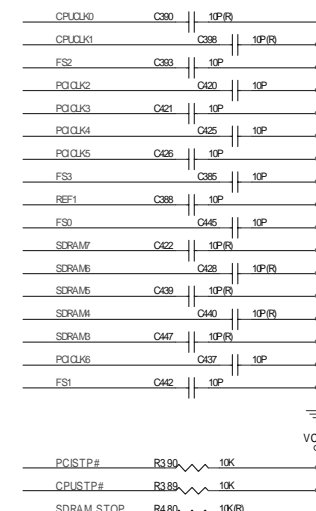
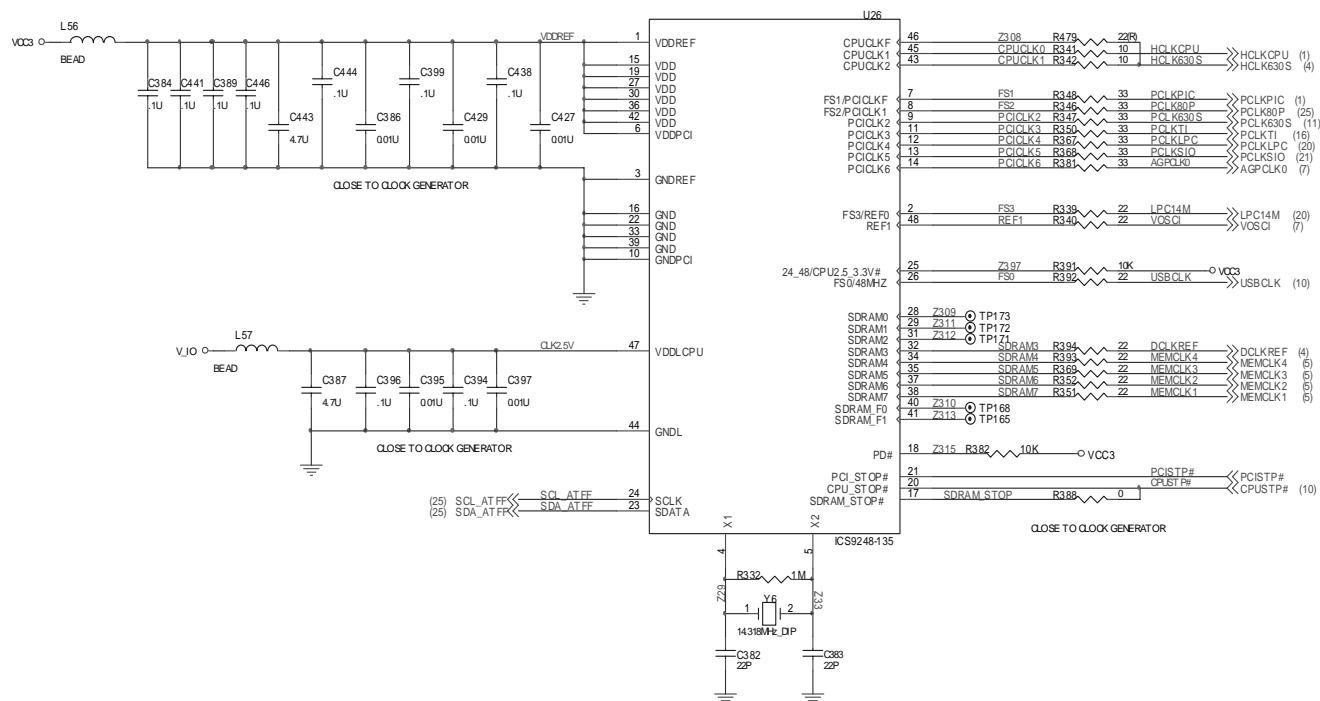


Appendix B: Schematic Diagrams





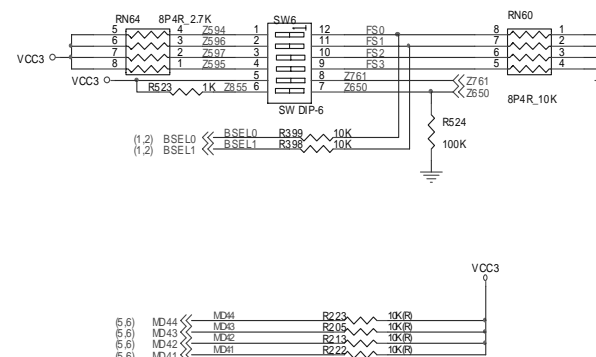
Schematic Diagrams

CLOCK SELECT

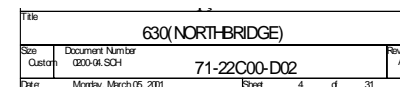
FS3	FS2	FS1	FS0	CPU	SDRAM	PG
0	0	0	0	66	100	33.3
0	0	0	1	100	100	33.3
0	0	1	0	150	100	37.5
0	0	1	1	133	100	33.3
0	1	0	0	66.8	133.6	33.4
0	1	0	1	100	133	33.3
0	1	1	0	100	150	37.5
0	1	1	1	133	133	33.3
1	0	0	0	66.8	66.8	33.4
1	0	0	1	97	97	32.3
1	0	1	0	70	105	36
1	0	1	1	95	95	31.7
1	1	0	0	95	126.7	31.7
1	1	0	1	112	112	37.3
1	1	1	0	97	129.3	32.2
1	1	1	1	126.7	126.7	32.2

CPU RATIO SELECT

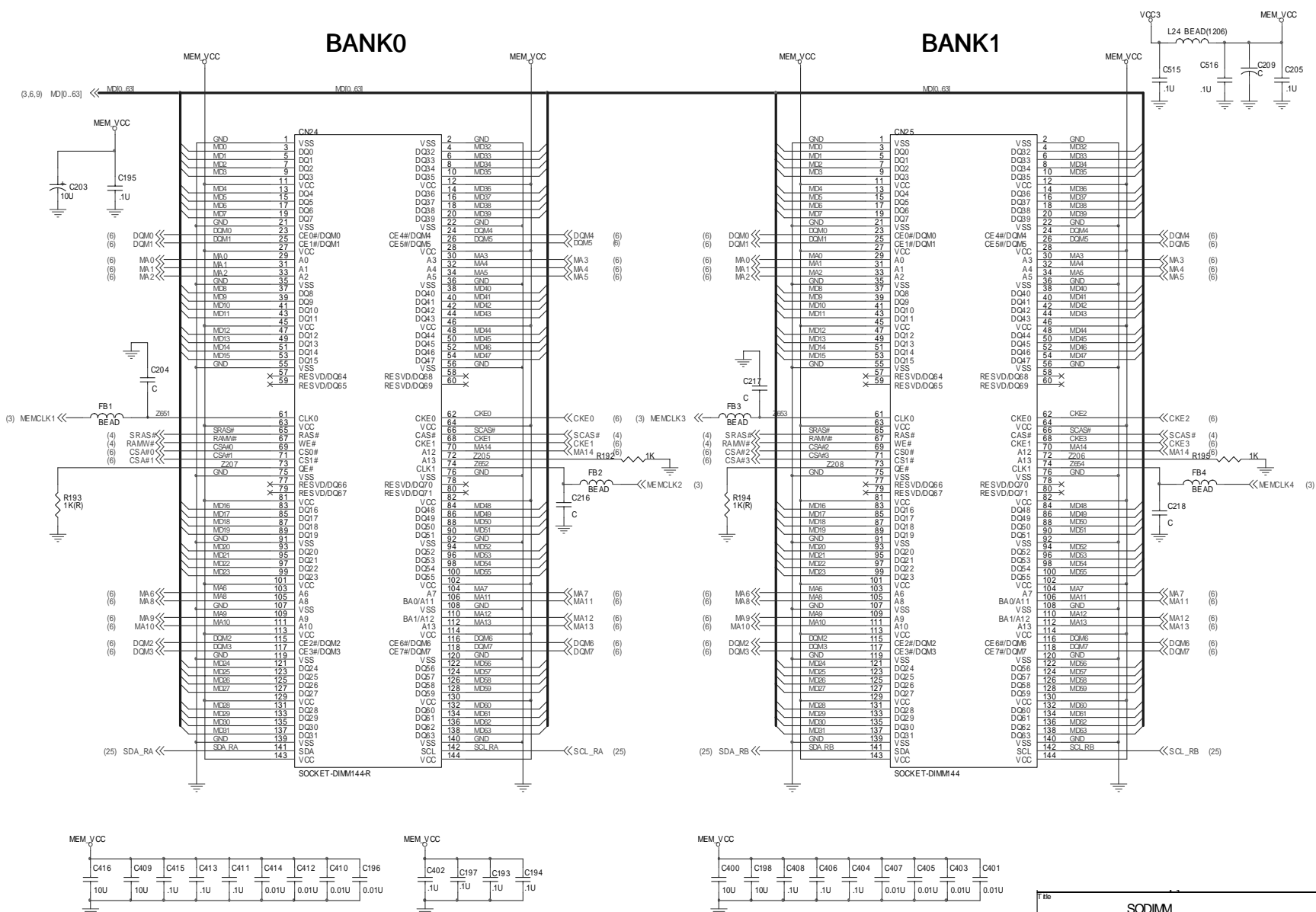
	MD4 (N4)	MD3 (N3 F)	MD2 (A20)F	MD1 (IGNE)
12	0	0	0	0
13	0	0	0	1
14	0	0	0	1
15	0	0	1	0
26	0	1	0	0
27	0	1	0	1
29	0	1	1	0
211	0	1	1	1
16	1	0	0	0
17	1	0	0	1
18	1	0	1	0
Reserved	1	0	1	1
213	1	1	0	0
215	1	1	0	1
23	1	1	1	0

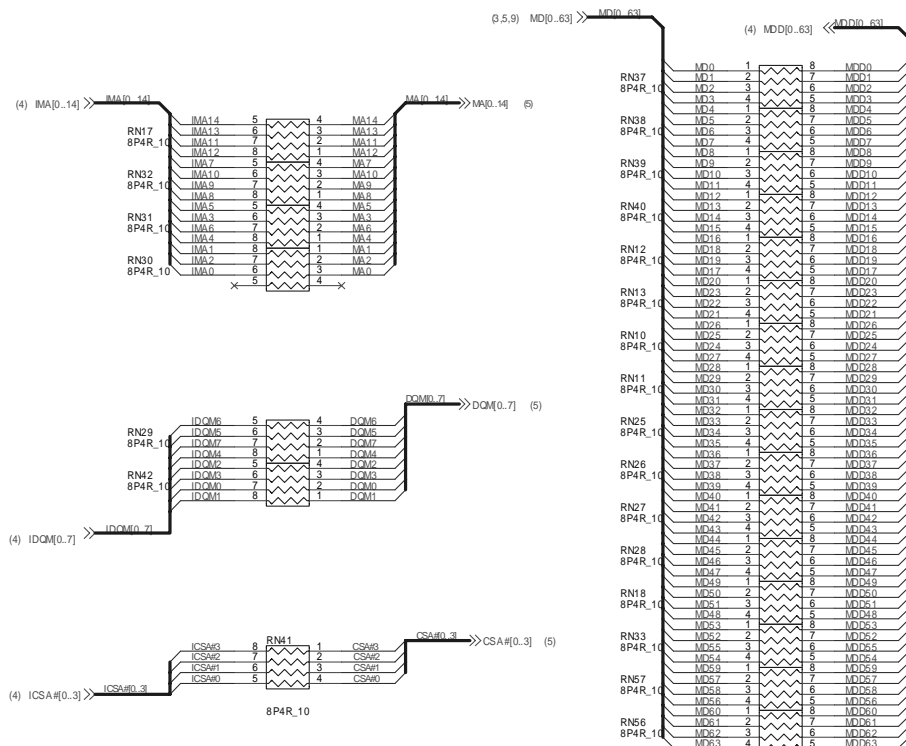


Title					CLOCK GENERATOR				
Size	Document Number								Rev
Custom	0200-03.SCH				71-22C00-D02				2/
Date	Monday, March 15, 2001				Sheet	3	of	31	

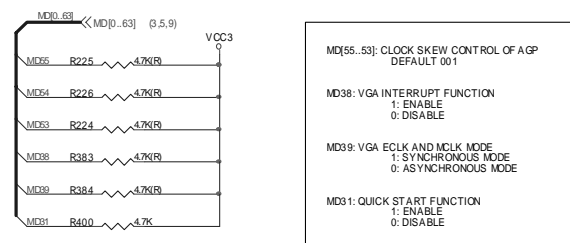
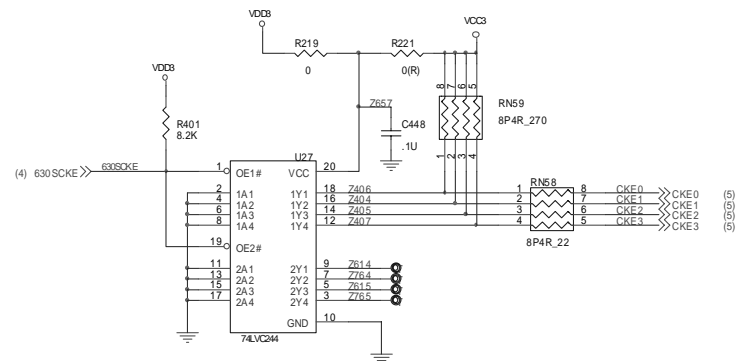


Schematic Diagrams



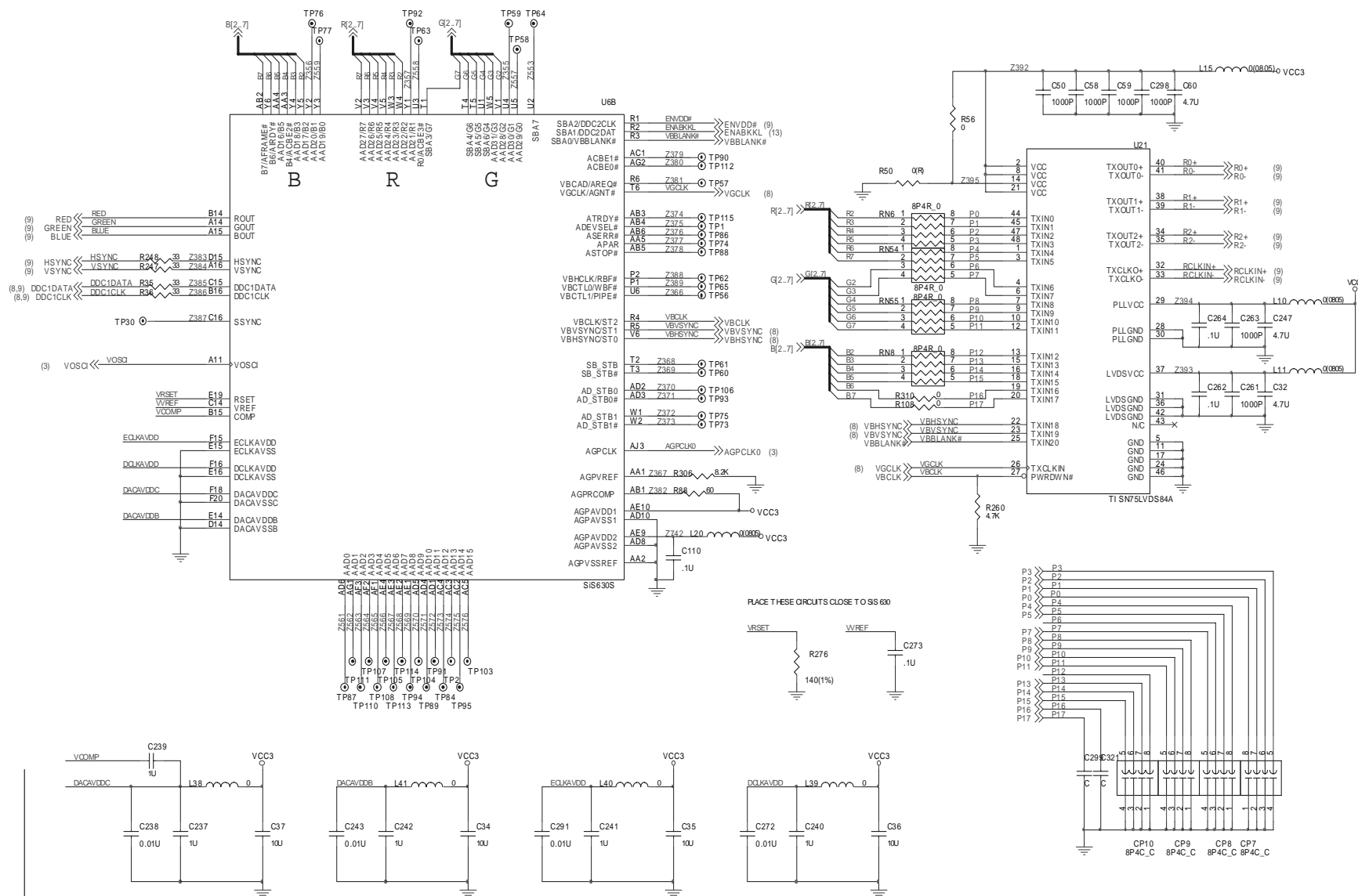


DRAM BUS DAMPING RESISTOR NEAR SIS630S

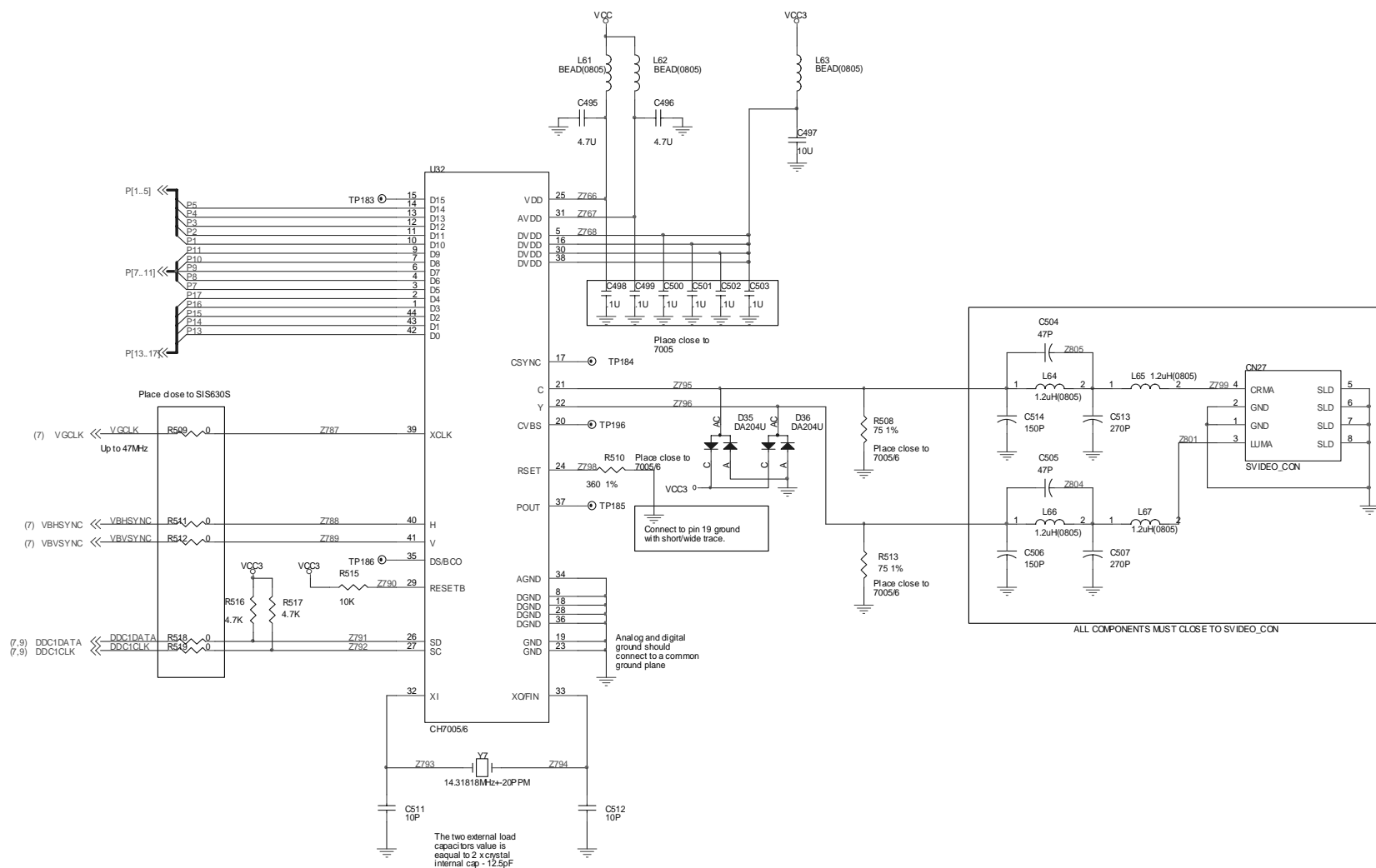


Title				RESISTOR				Rev	
Size		Document Number							
Custom		0200-06.SCH		71-22C00-D02				A	
Date Monday, March 05, 2001				Sheet		6		of 31	

Schematic Diagrams

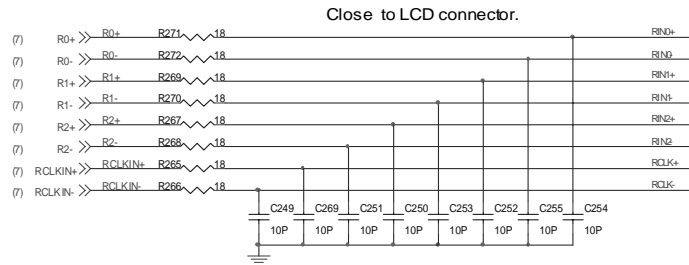


Title			
630(VGA) & LVDS			
Size	Document Number	Rev	
Cust Item	000-07 SCH	71-22C00-D02	
Date	Monday, March 05, 2001	Sheet	7 of 31



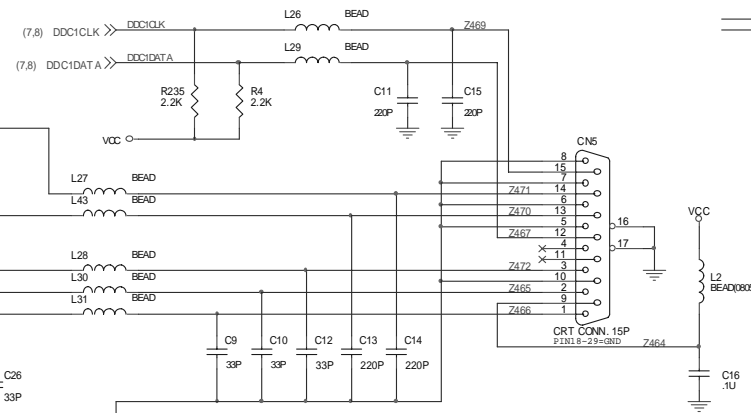
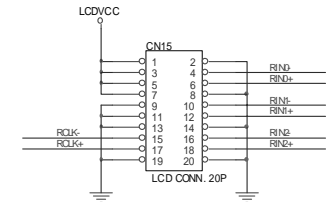
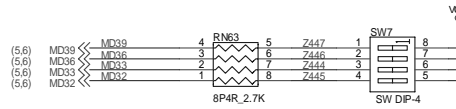
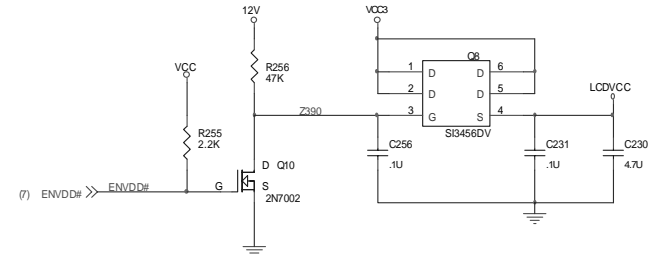
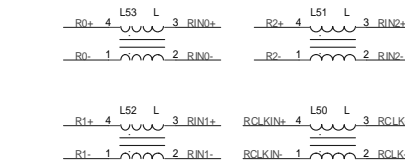
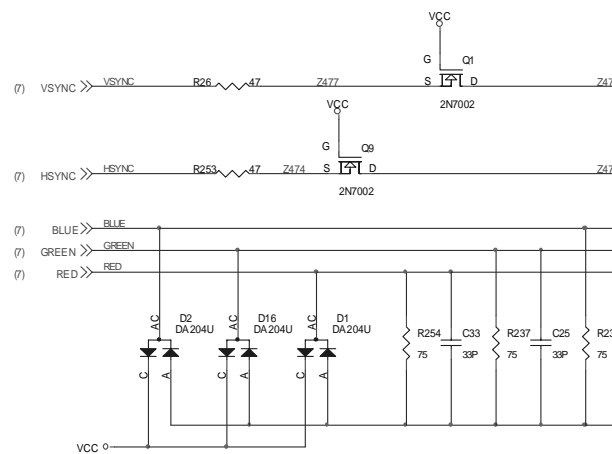
Title		Chrontel CH7005 TV-OUT	
Size	Document Number		Rev
Custom	020-08.SCH	71-22C00-D02	A
Date	Monday, March 05, 2001	Sheet	8 of 21

Schematic Diagrams



Panel ID	SWF-1 VM039	SWF-2 VM036	SWF-3 VM033	SWF-4 VM032
1 12.1" SANYO TM121SV-02L04	1	0	1	0
2 Samsung LT121SU-121	1	0	1	0
3 ADI AA121SJ03	0	0	0	0
4 IBM ITS V53C1	0	0	0	0
5 13.3" LG LP133X7-A2	1	1	0	0
6 Unipac UP133X01	1	1	0	0
7 Acer L133X2-1	1	1	0	0
8 Hyundai HT13X14	1	1	1	0
9 Samsung LT133X5-122	1	1	1	0
10 14.1" Samsung LT141X5-124	1	1	1	0
11 Hyundai HT14X12	1	1	1	0
12 LG LP141X5	1	1	0	0
13 NEC NL10276BC28-11E	1	1	0	0
14 HITACHI TX36D01 VC0CAA TX36D11 VC0CAA	X	X	X	X
15 Samsung LT141X5-122				

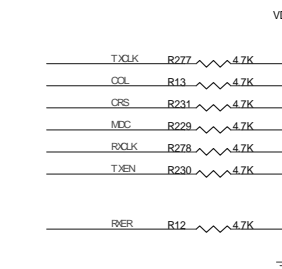
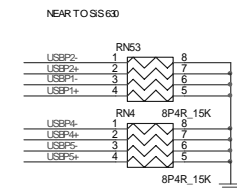
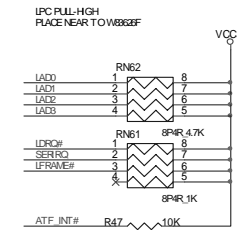
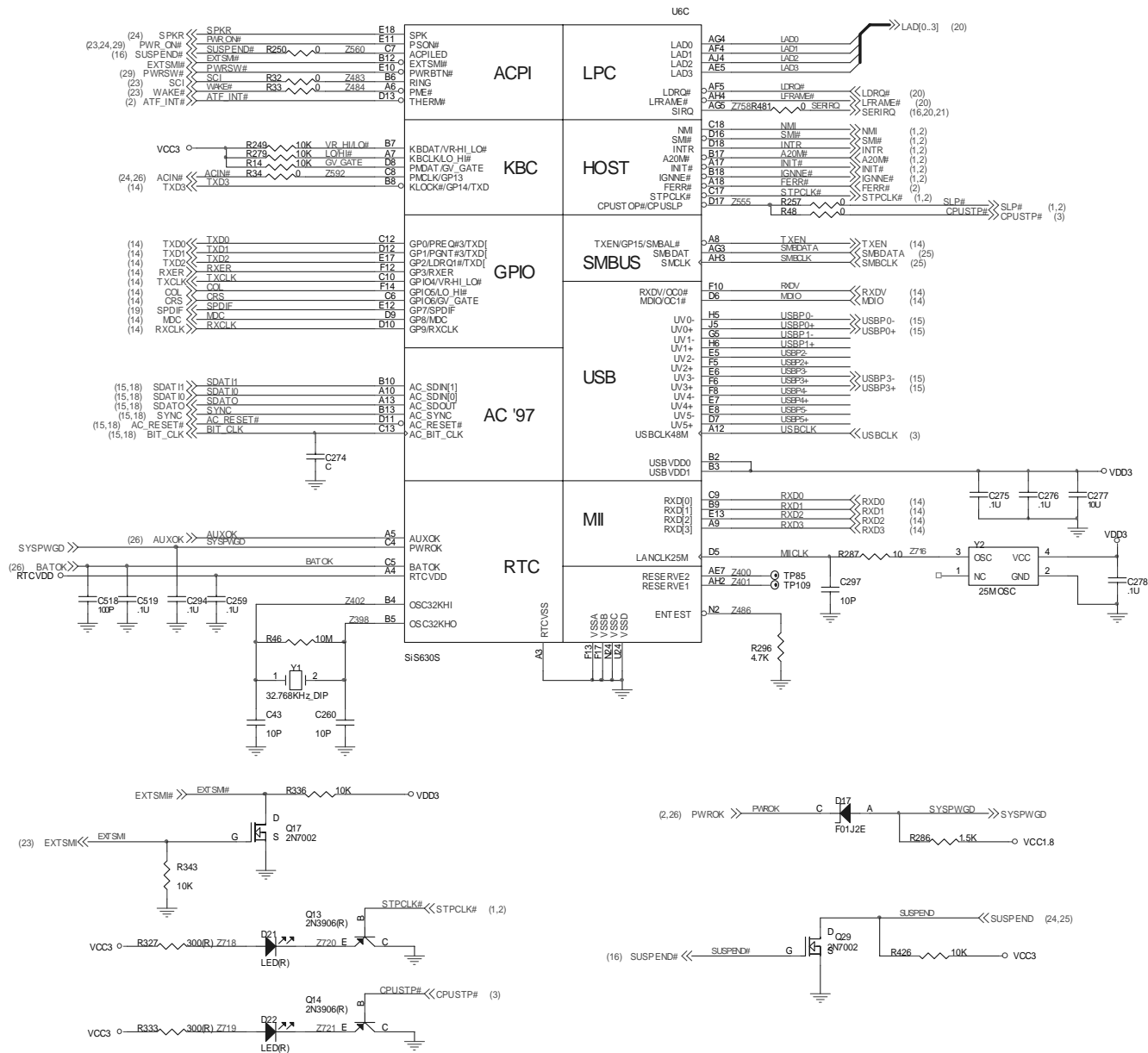
ON=H=1 OFF=L=0



LAYOUT NOTE:
Make sure VIDEOGND is as clean as possible
Also the RED, GREEN and BLUE signal traces
should be shielded with VIDEOGND traces.

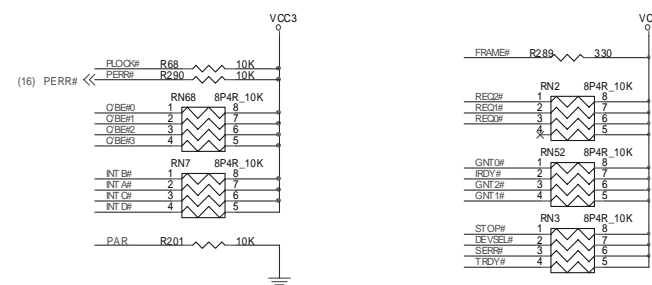
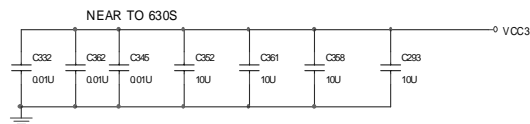
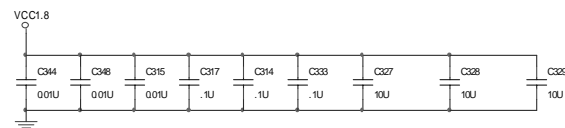
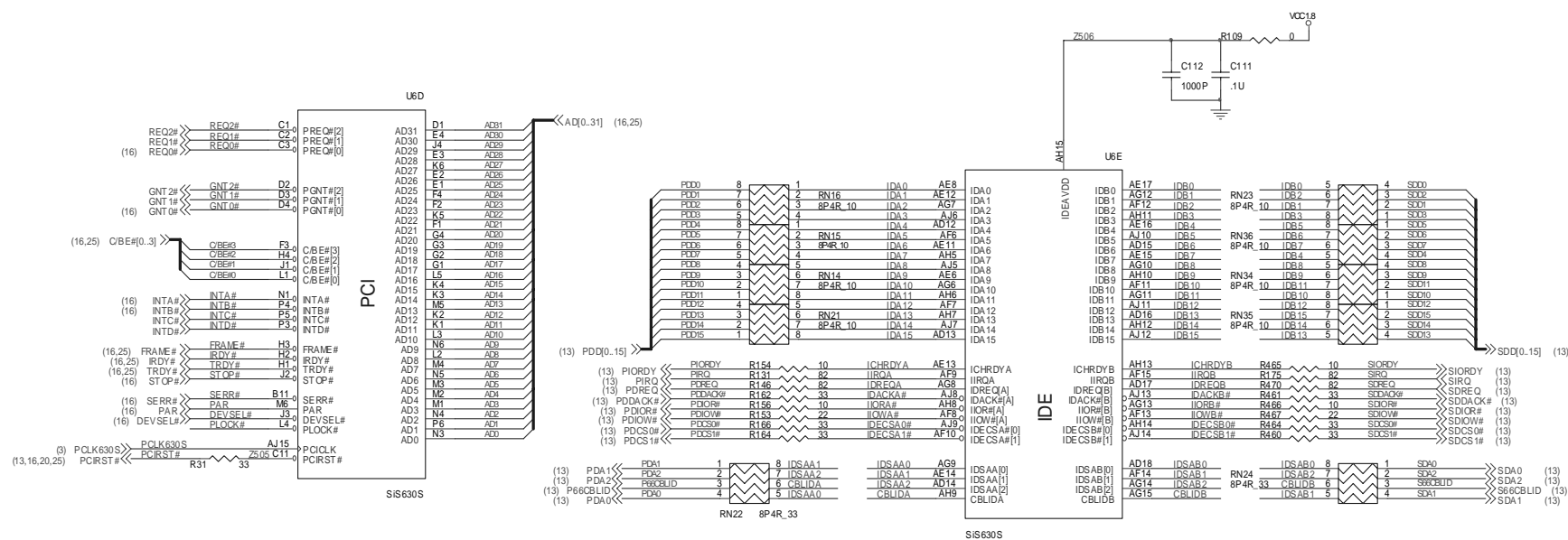
Title	Document Number	Rev
LCD & CRT CON	71-22C00-D02	A
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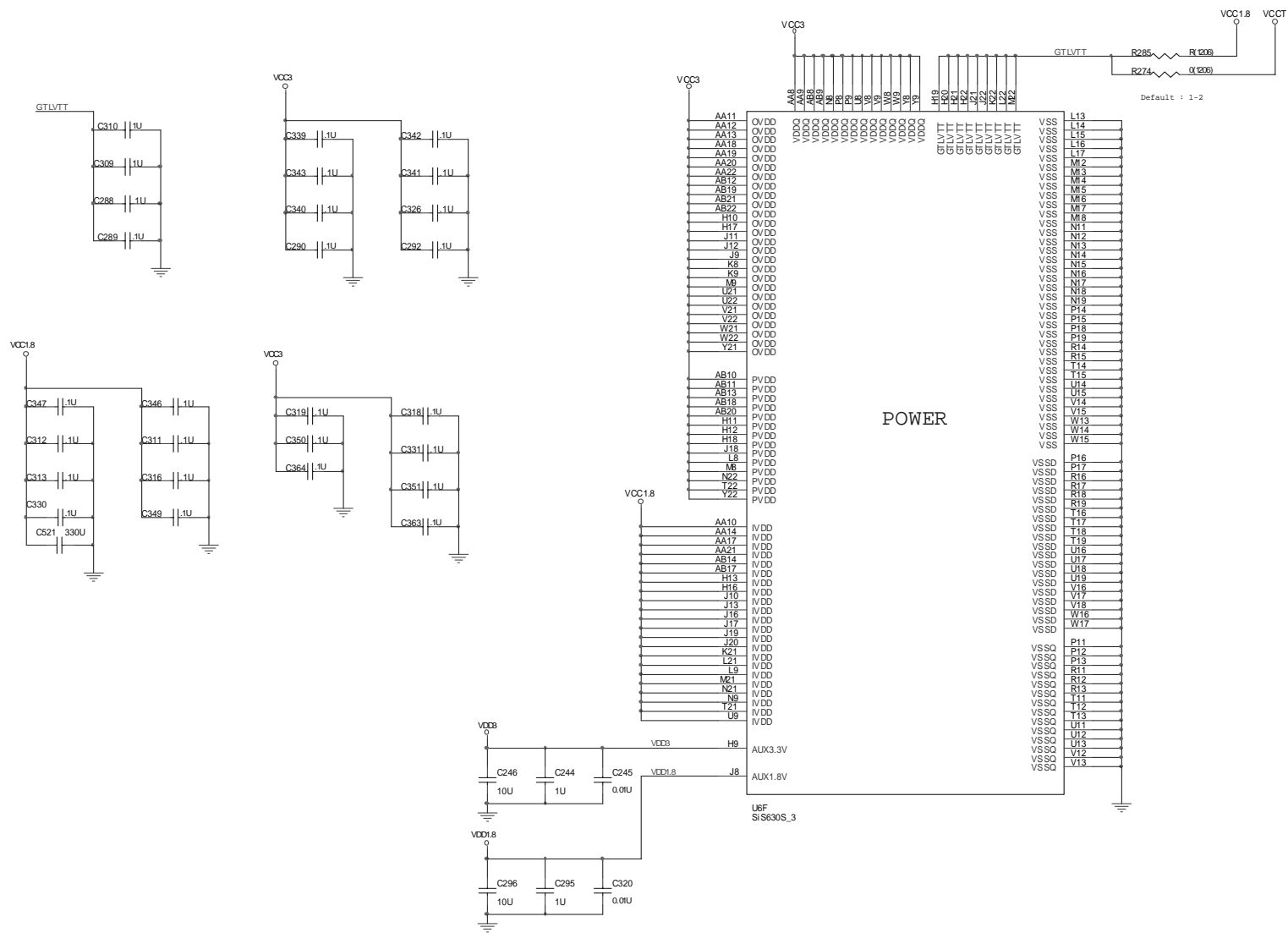


Title				630S(SOUTHBIDGE)			
Size	Custom	Document Number	71-22C00-D02				Rev
		0200-09.SCH					A
Date	Monday, March 05, 2001		Sheet	10	of	31	

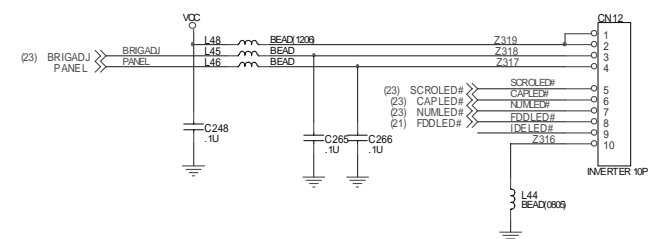
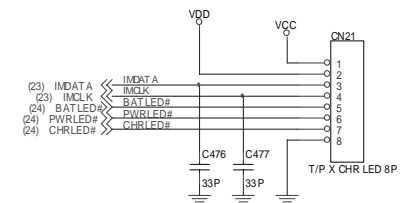
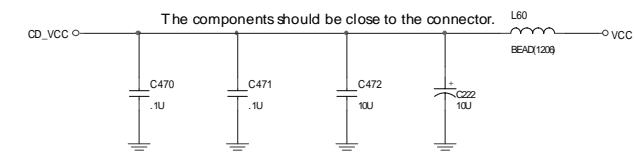
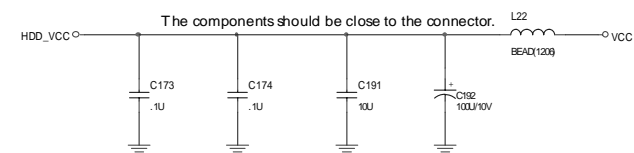
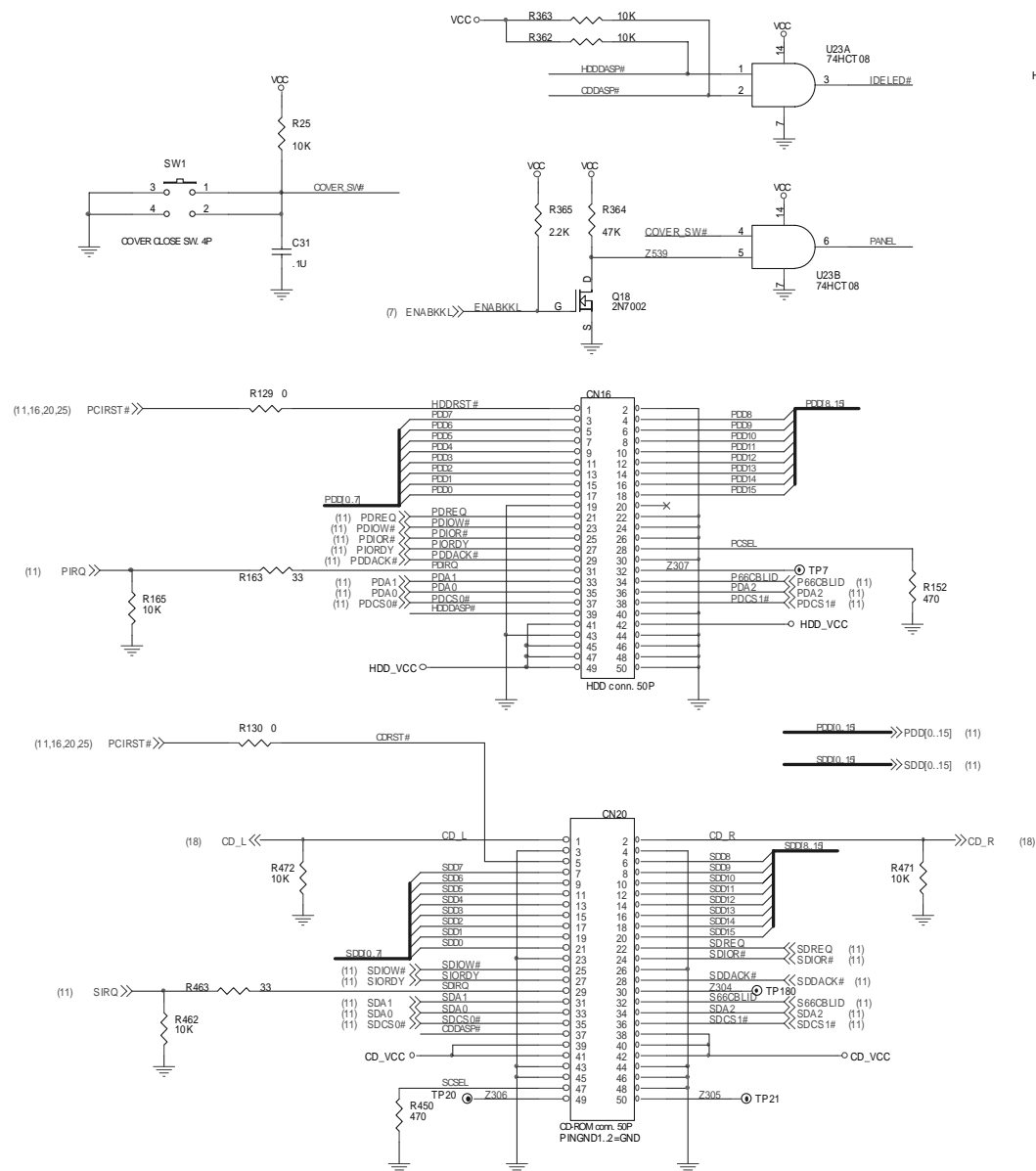
Schematic Diagrams



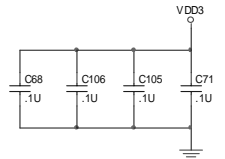
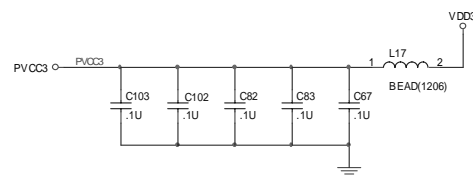
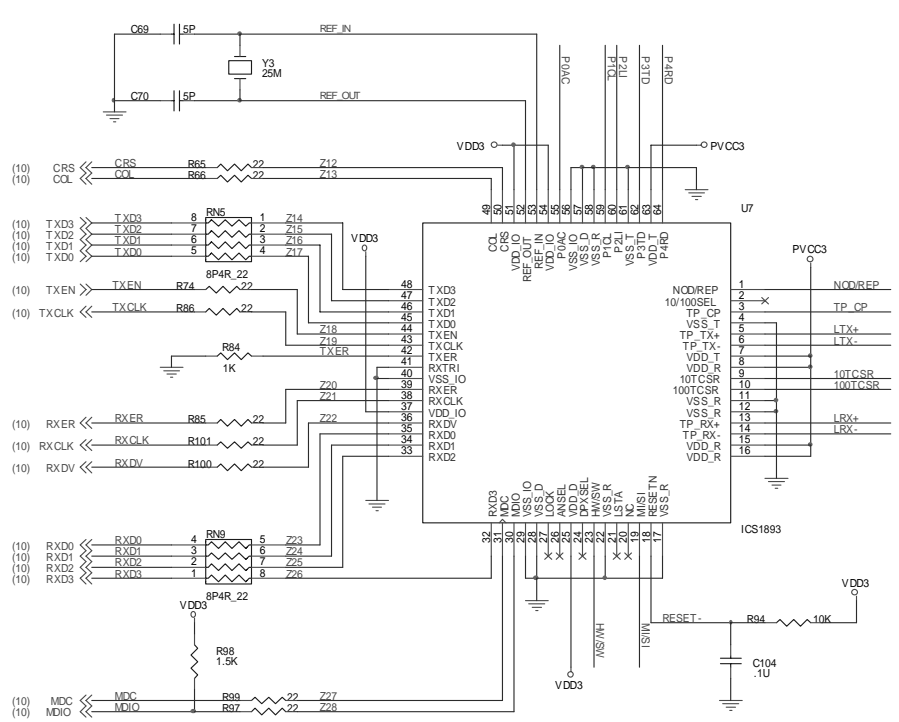
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Size	Document Number	71-22C00-D02			
Custom	0200 10.SCH				
Date	Monday, March 05, 2001	Sheet	11	of	31



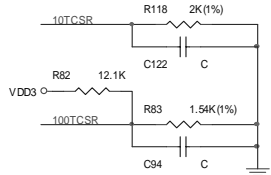
Schematic Diagrams



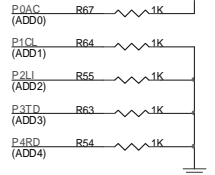
Title		IDE		Rev
Size	Document Number			A
Custom	0200-12 SCH	71-22C00-D02		
Date	Monday, March 05, 2001	Sheet	13 of 31	



SET TRANSMITTING DRIVING CURRENT



SET PHY ADDRESS=00001

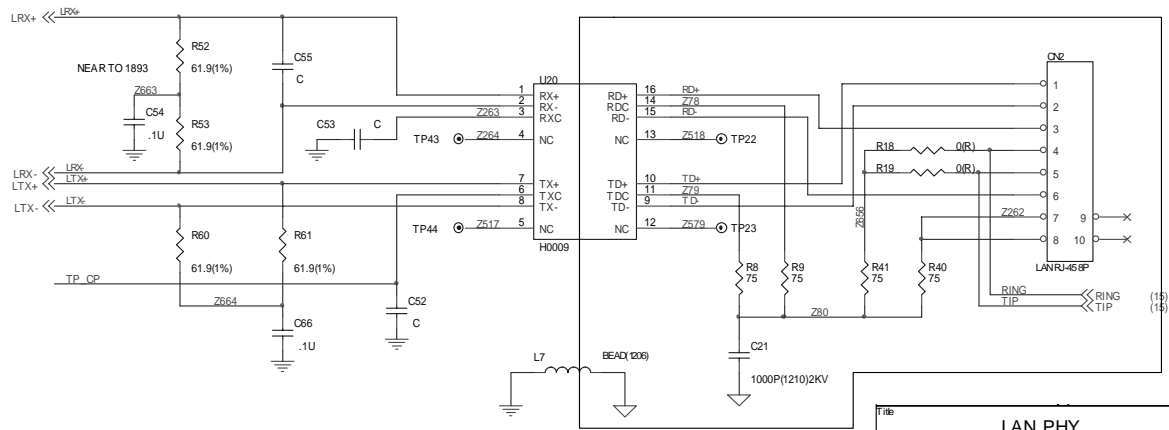


- 1.HW/SW=1
SWMODE
- 2.MI/SI=0
MII MODE
- 3.NOD/REP=0
NODE OPERATION

10/100SEL: 10/100MHZ INDECATED
LOW=10BASE-T
HIGH=100BASE-T

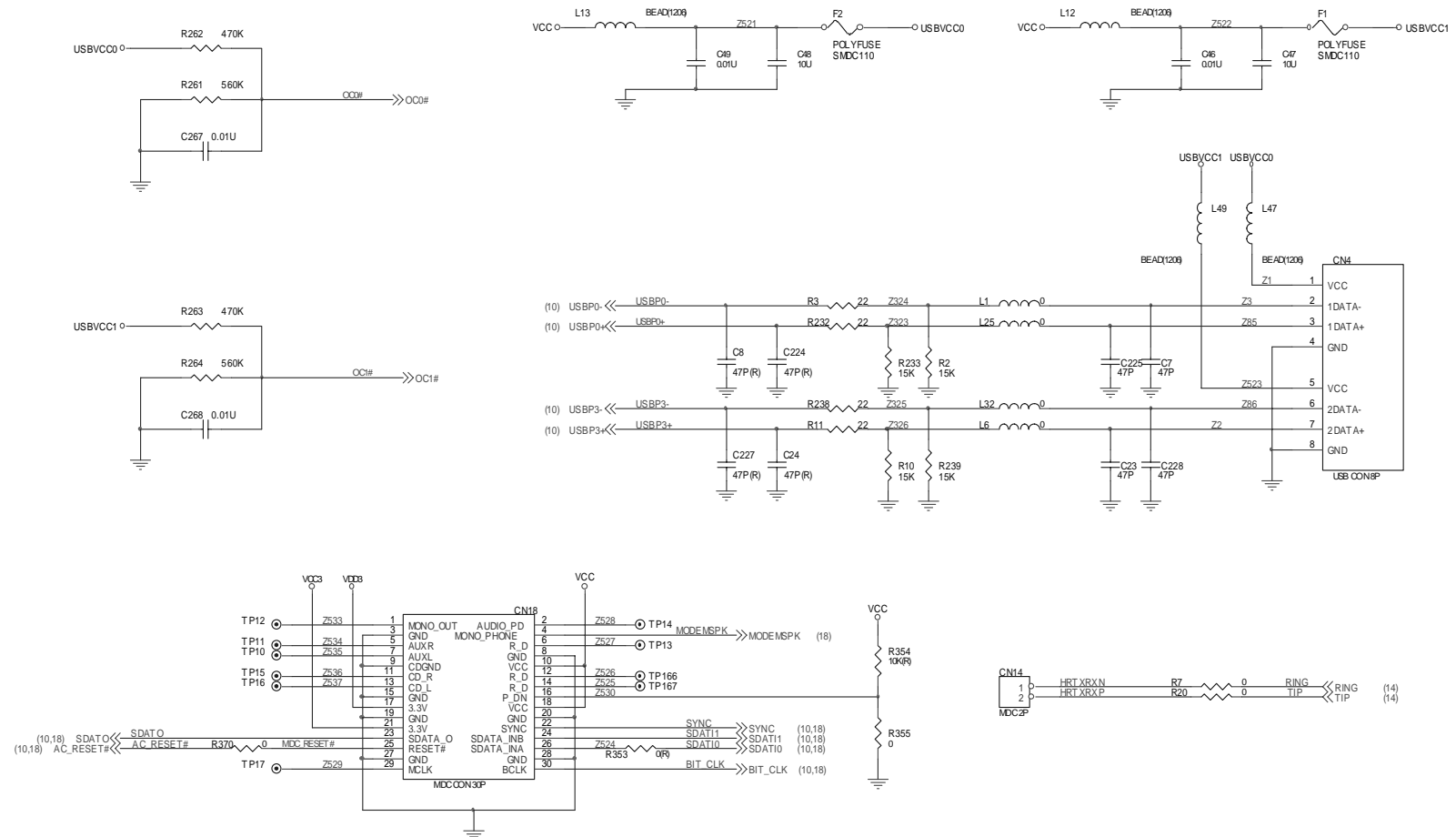
DPXSEL: HALF OR FULL INDECATED
LOW=HALF-DUPLEX,
HIGH=FULL-DUPLEX

ANSEL: AUTO-NEGOTIATION INDECATED
LOW=DISABLE
HIGH=ENABLE



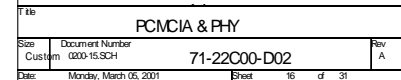
Title		
LAN PHY		
Size	Document Number	Rev
Custom	000-13-SCH	A
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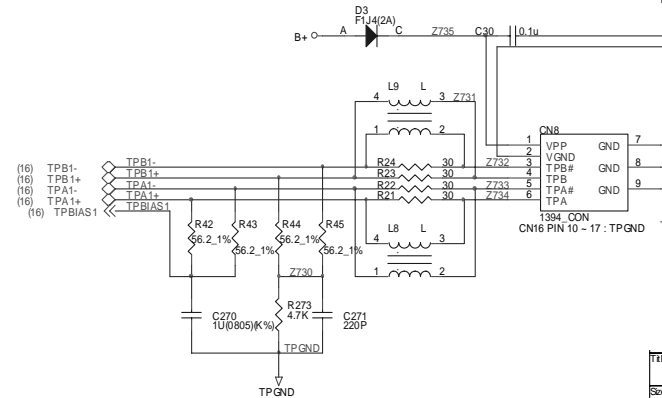
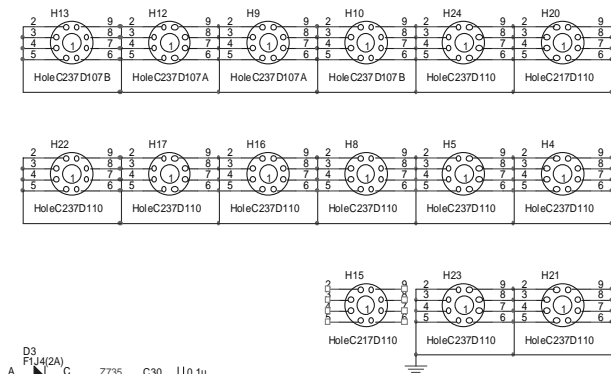
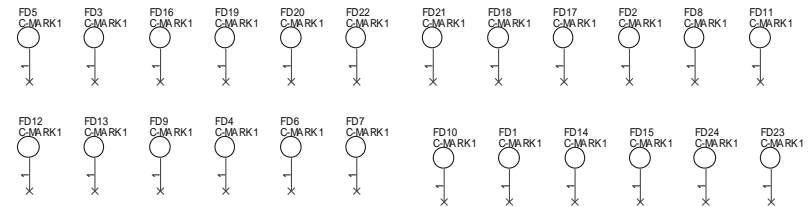
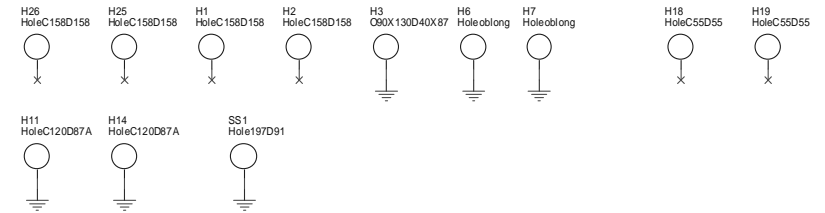
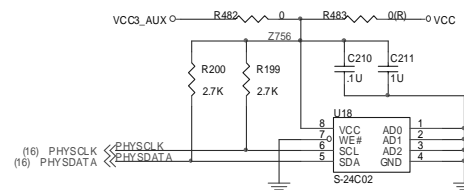
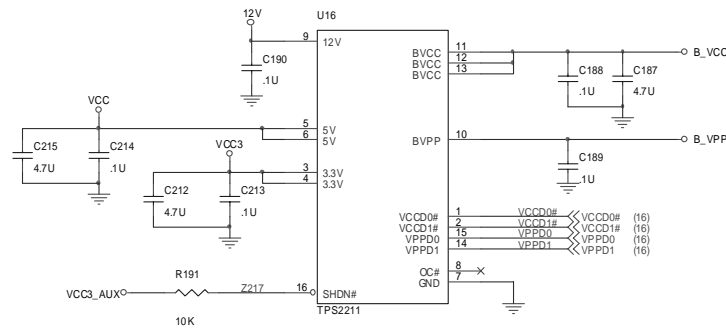
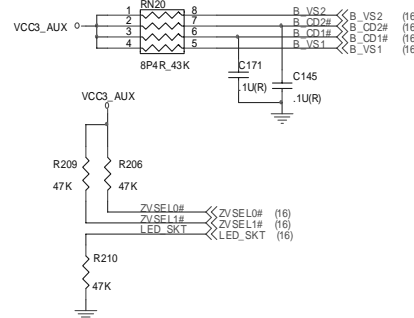
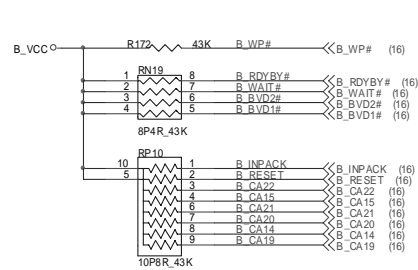
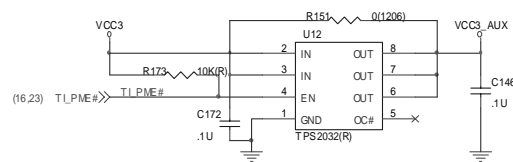


Title			
USB & MDC CON			
Doc Custom	Document Number 000-94504	71-22C00-D02	Rev A
Date	Friday, November 10, 2000	Sheet	15 of 31

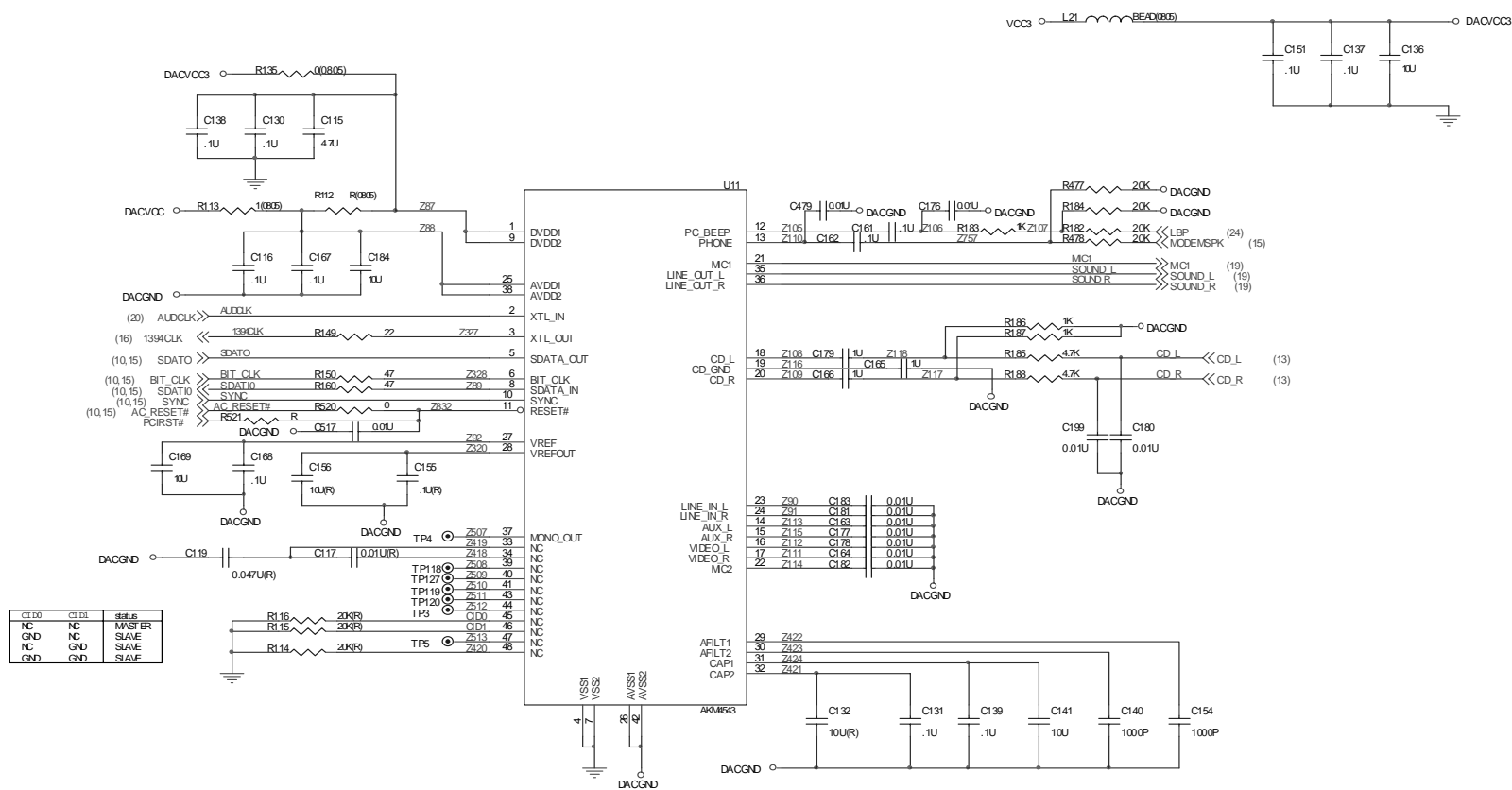
B - 16



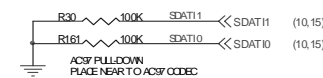
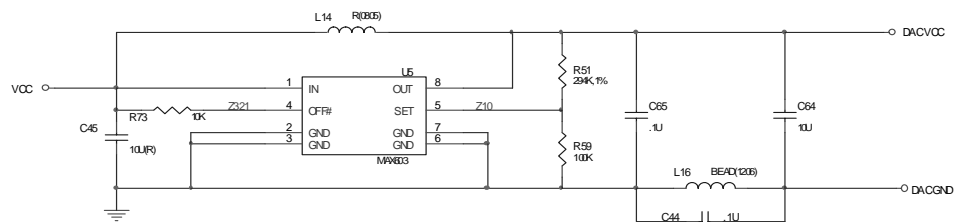
Schematic Diagrams



Title			
PCMCIA POWER			
Size	Document Number	Rev	
Cust Id	0000-16.SCH1	A	
Date	Monday, March 06, 2001	Sheet	17 of 31

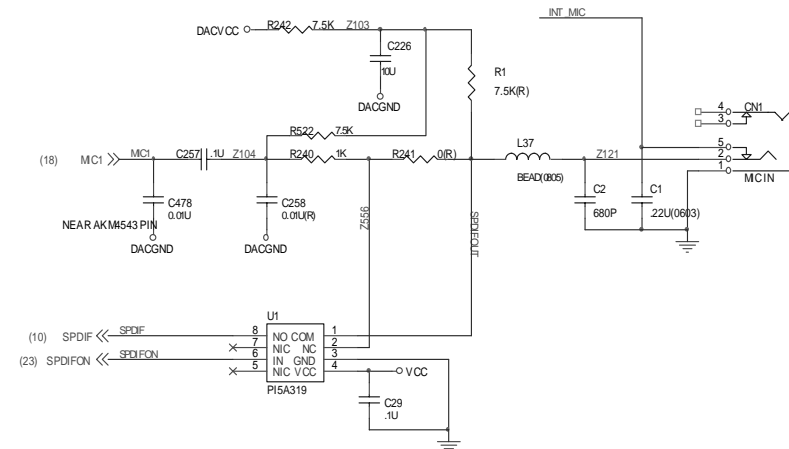
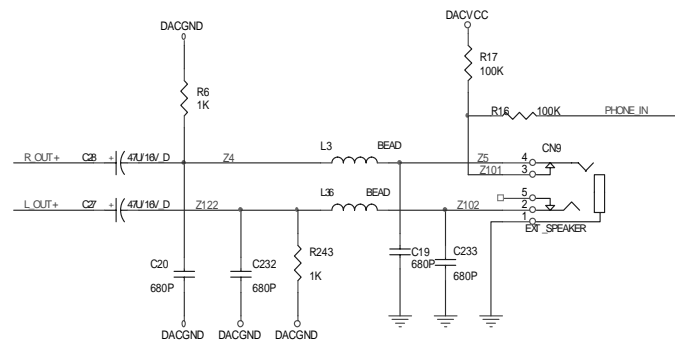
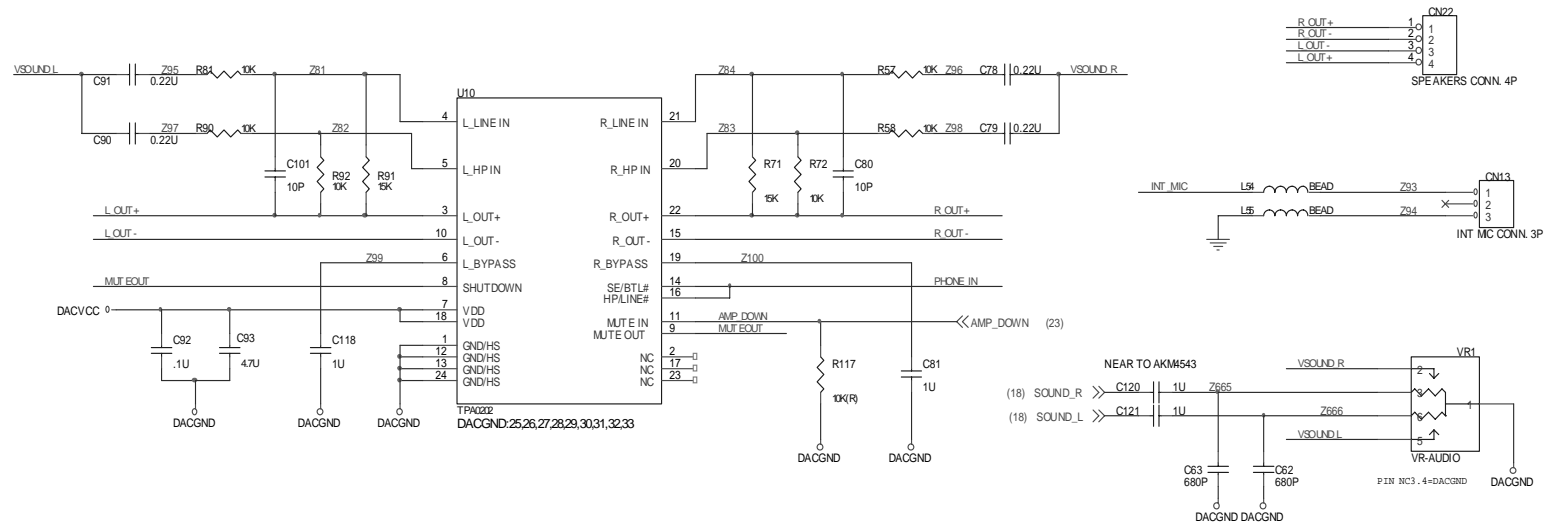


CS10	CS11	status
NC	NC	MASTER
GND	NC	SLAVE
NC	GND	SLAVE
GND	GND	SLAVE



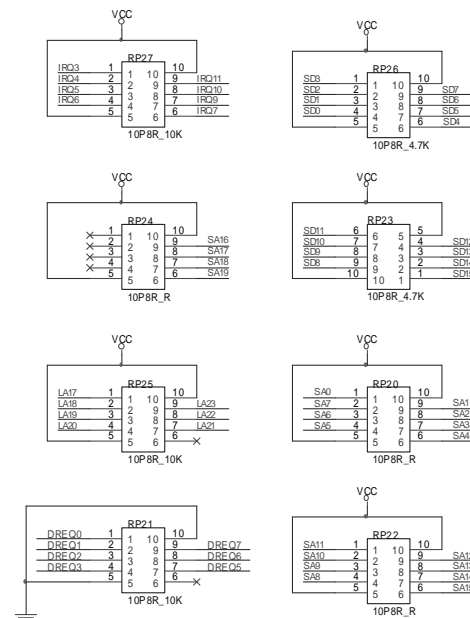
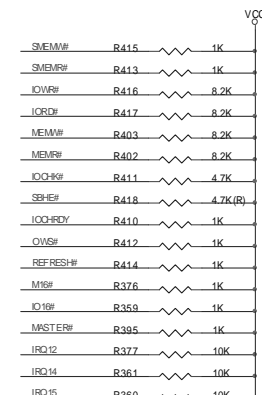
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AUDIO CODEC			
Size	Document Number	Rev	A
Custom	000-1750-1	71-22C00-D02	
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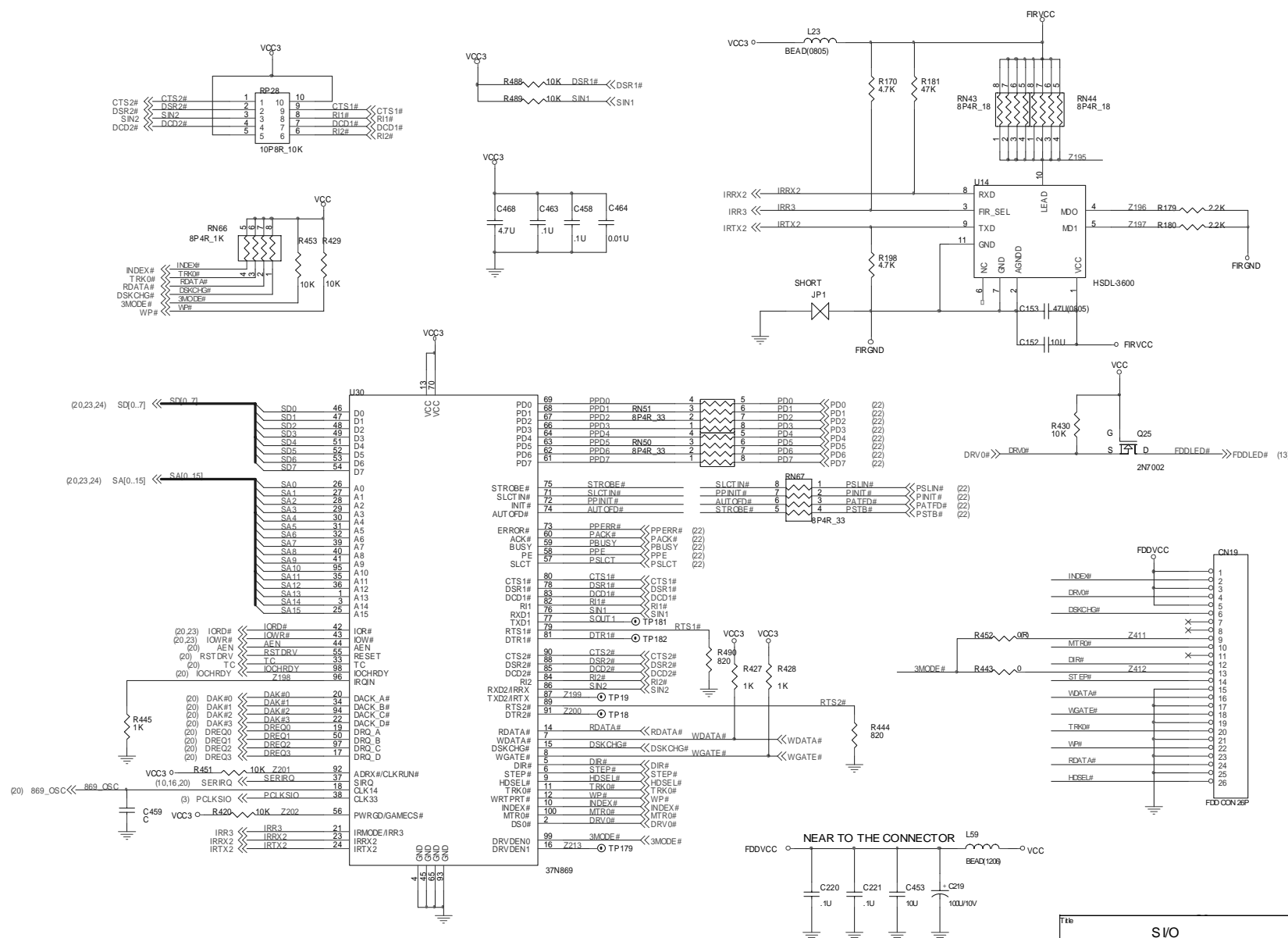
Title			
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Sub	Document Number	Rev	
Custom	020-18504	A	
Date	Monday, March 05, 2007	Sheet	19 of 31

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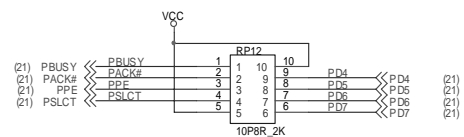
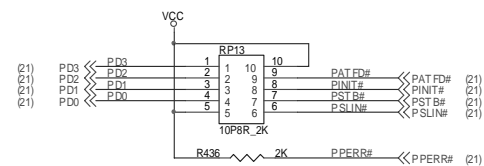
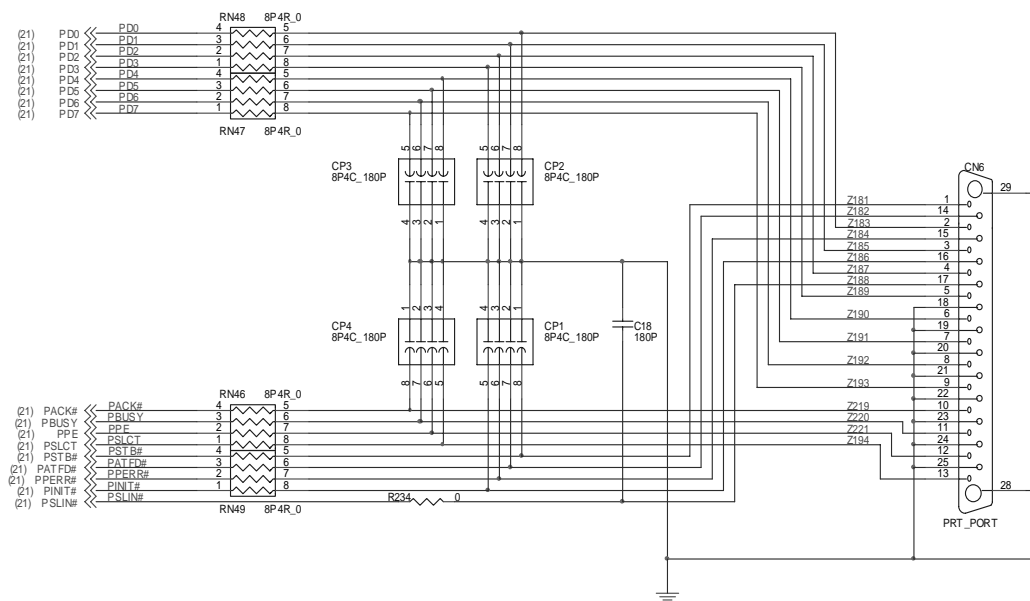
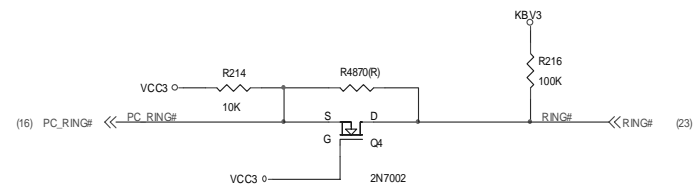


Title				LPC-ISA BRIDGE			
Size	Document Number						Rev
Custom	0200-19.SCH						A
71-22C00-D02							
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Schematic Diagrams

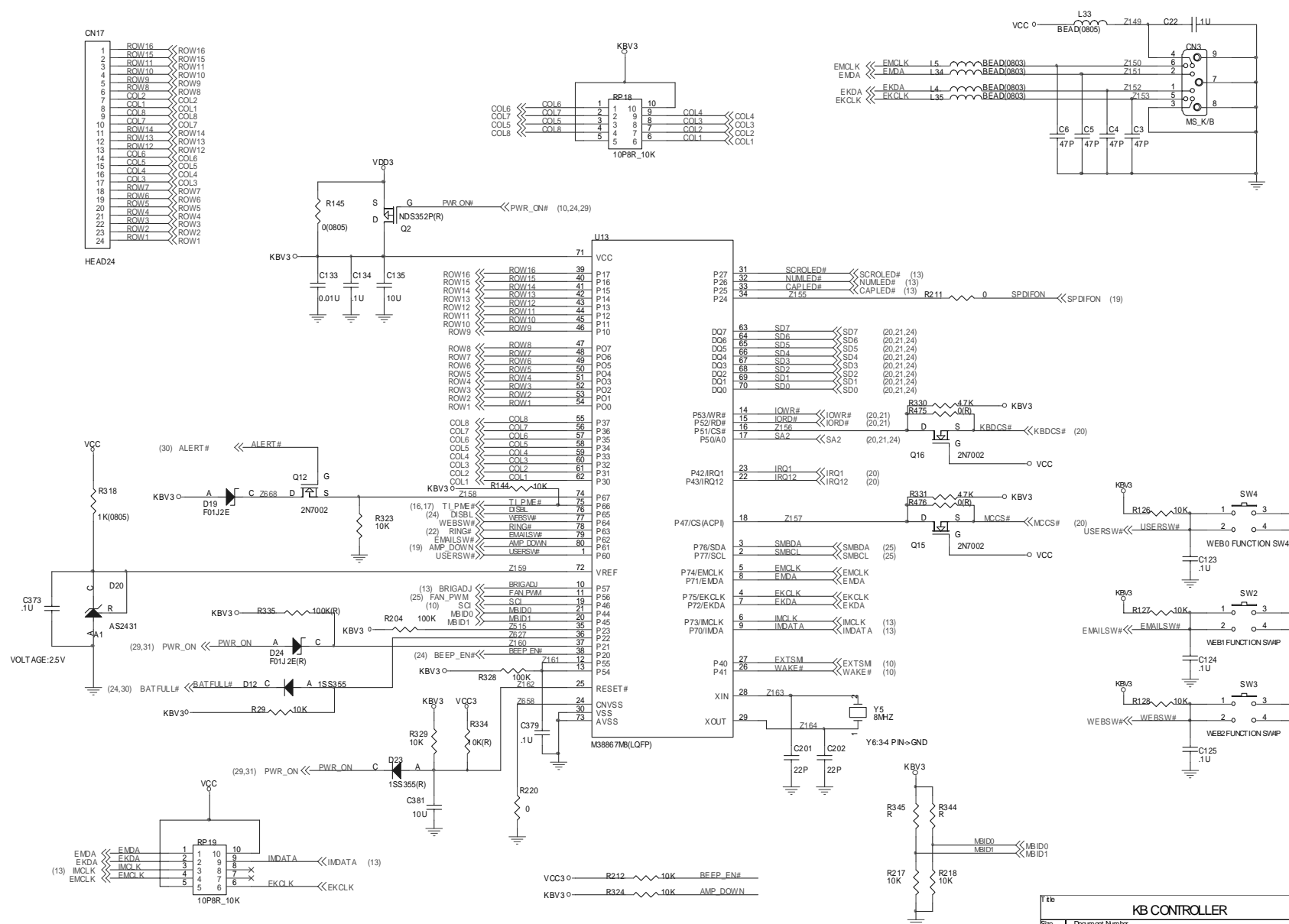


Service Manual

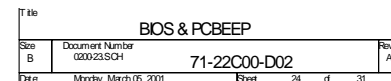


Title				I/O CON			
Size	Custom	Document Number	71-22C00-D02				Rev
		020-21.SOI					A
Date	Monday, March 05, 2001		Sheet	22	of	31	

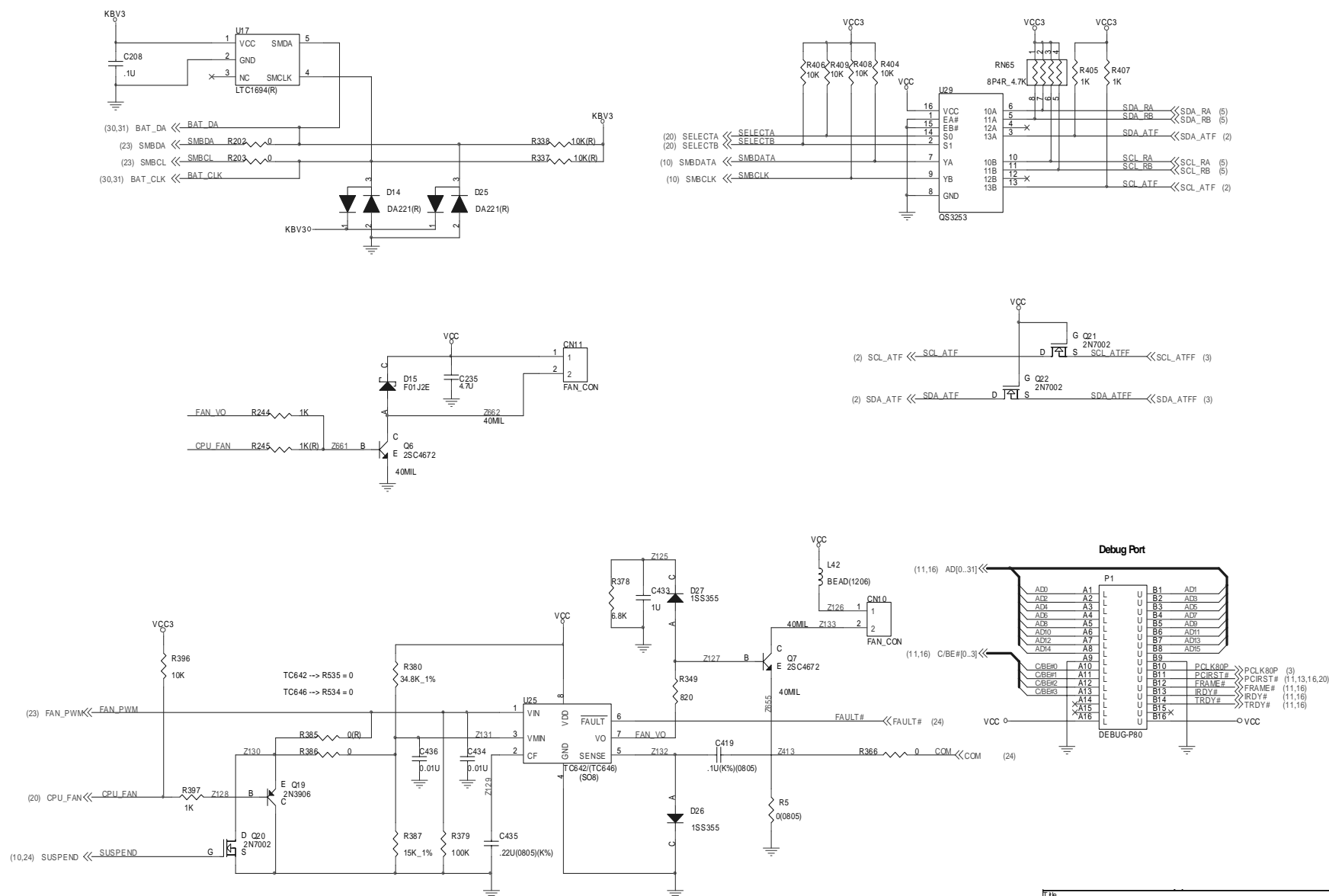
Schematic Diagrams



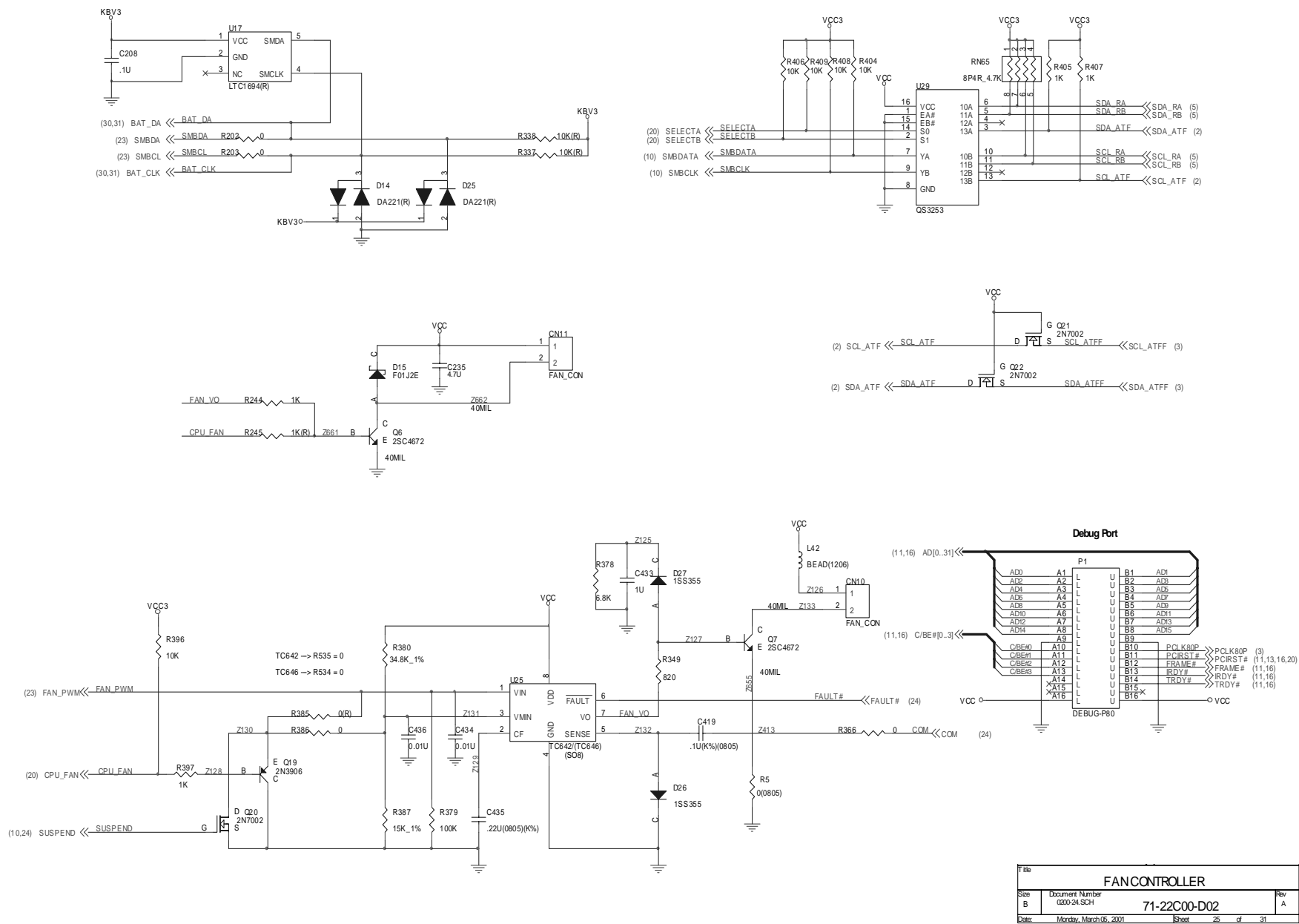
B - 24



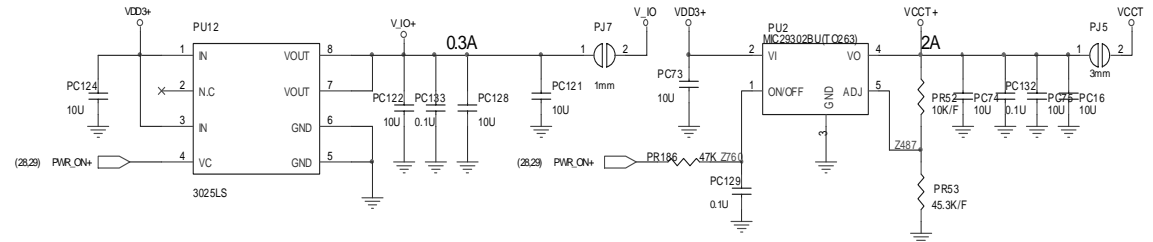
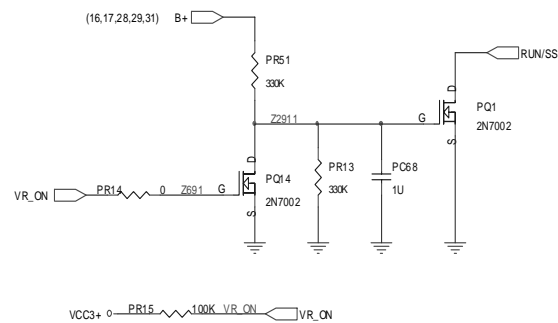
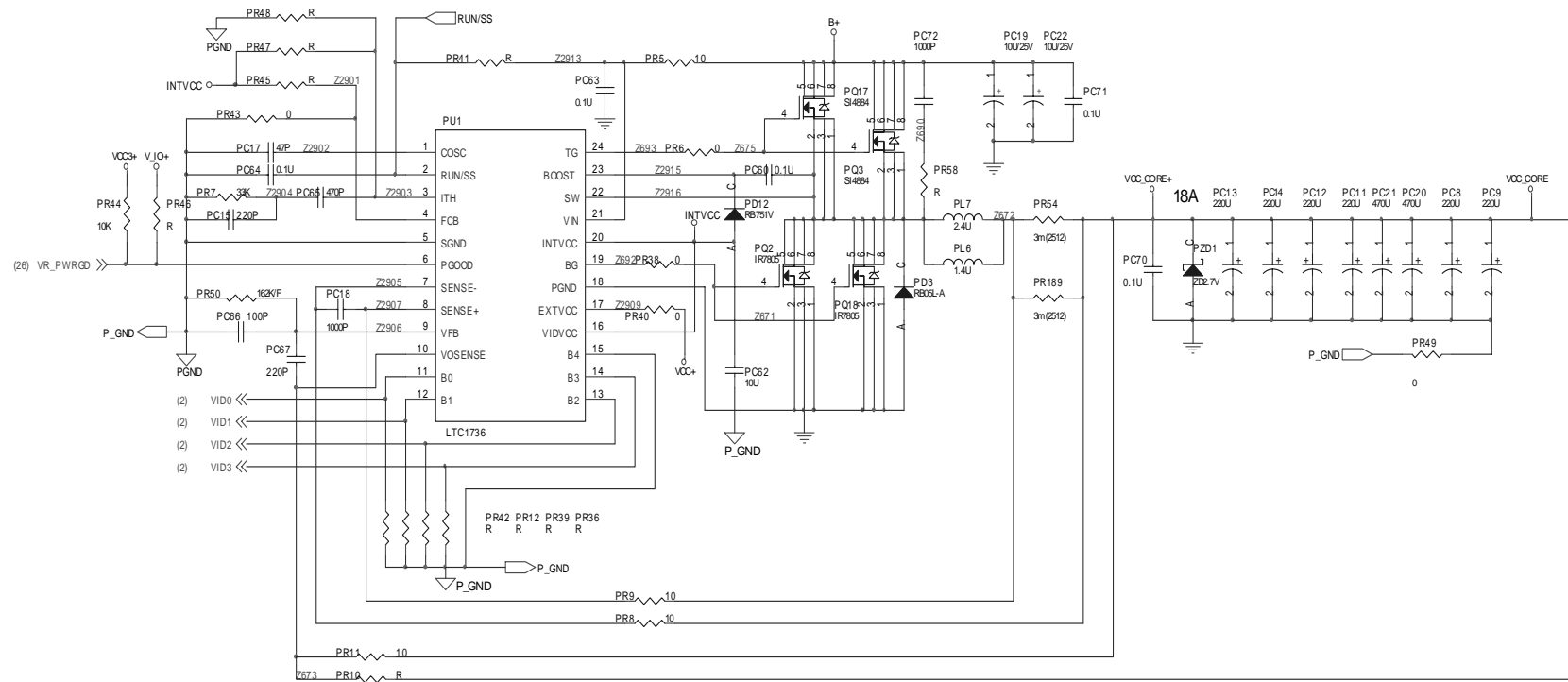
Schematic Diagrams



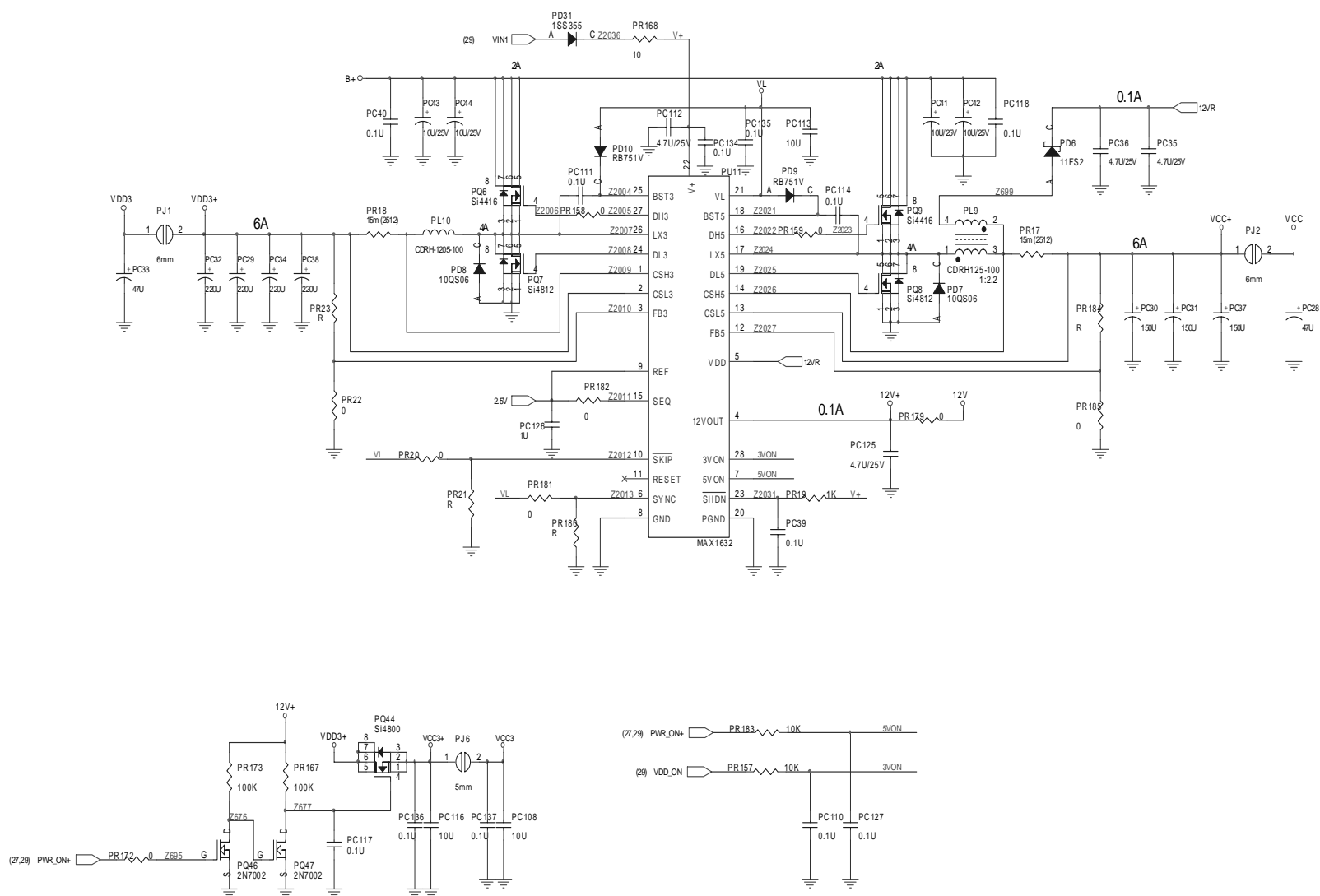
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Size	Document Number	71-22C00-D02		Rev	
B	0200-24 SCH			A	
Date	Monday, March 05, 2001	Sheet	25	of	25



Schematic Diagrams

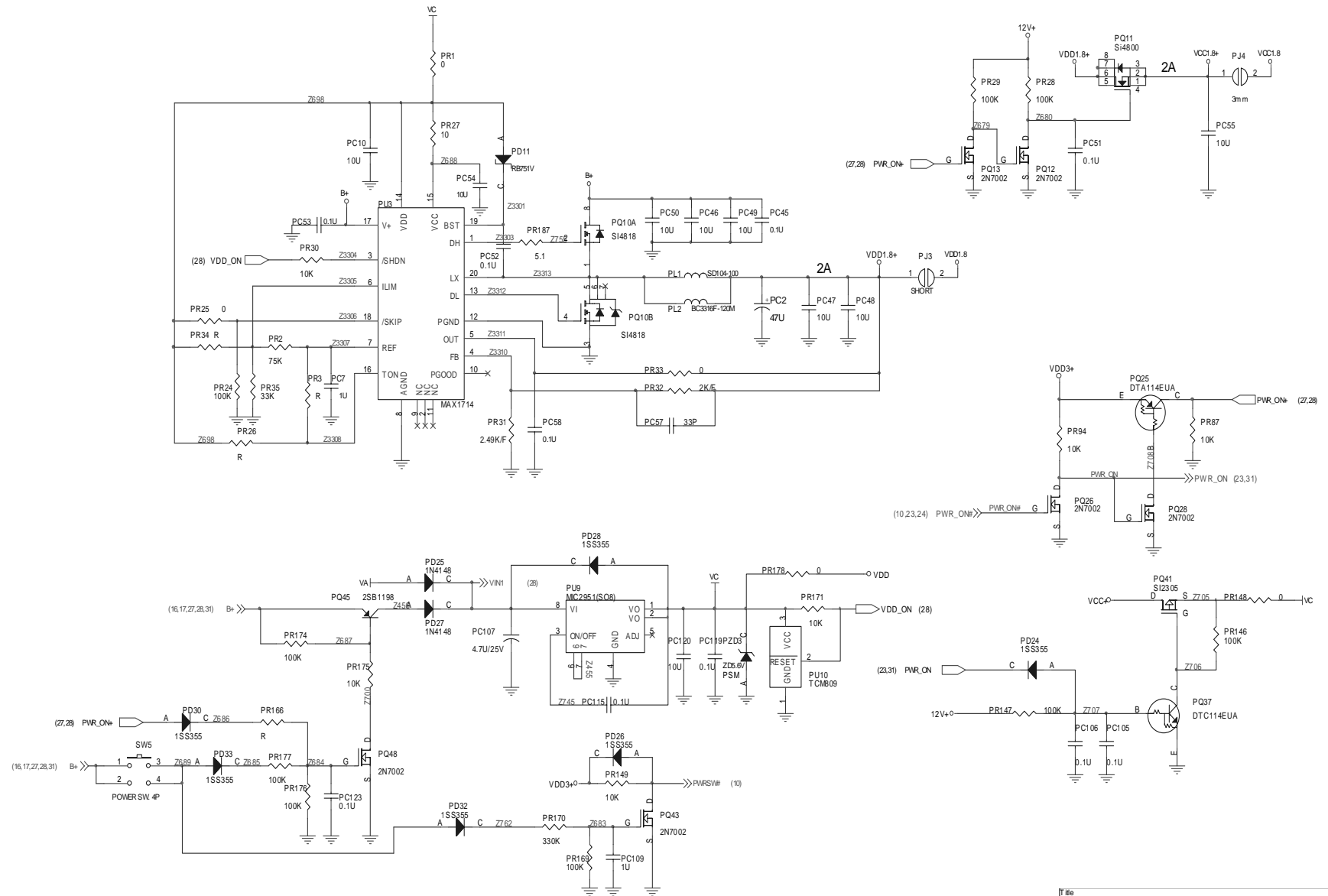


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Size	Document Number	Rev	
Custom	0200-28 SCH	A	
Date	02-10-04	Sheet	27 of 31

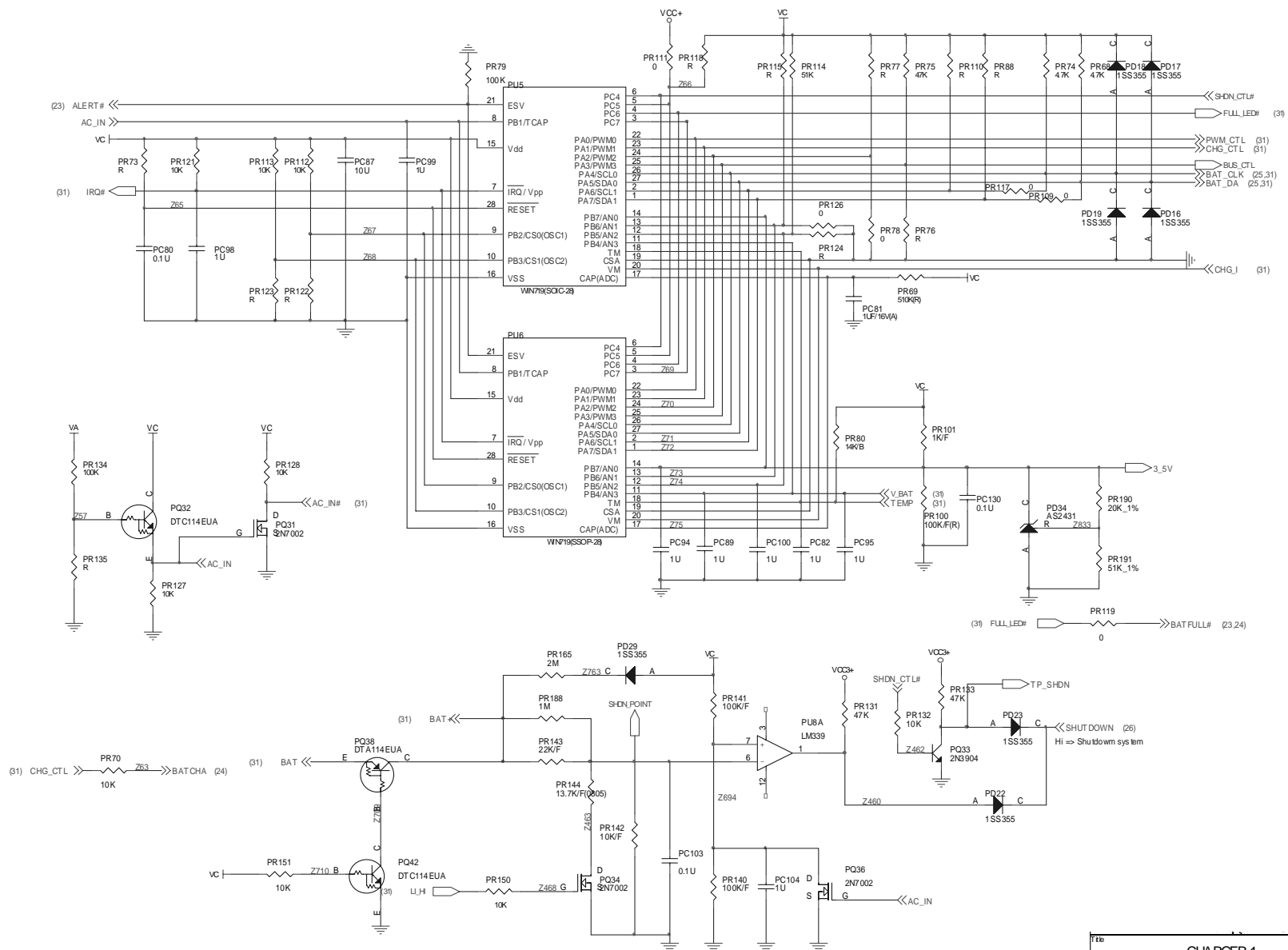


Title		VDD3,VCC3,VCC,12V	
Size	Document Number	Rev	
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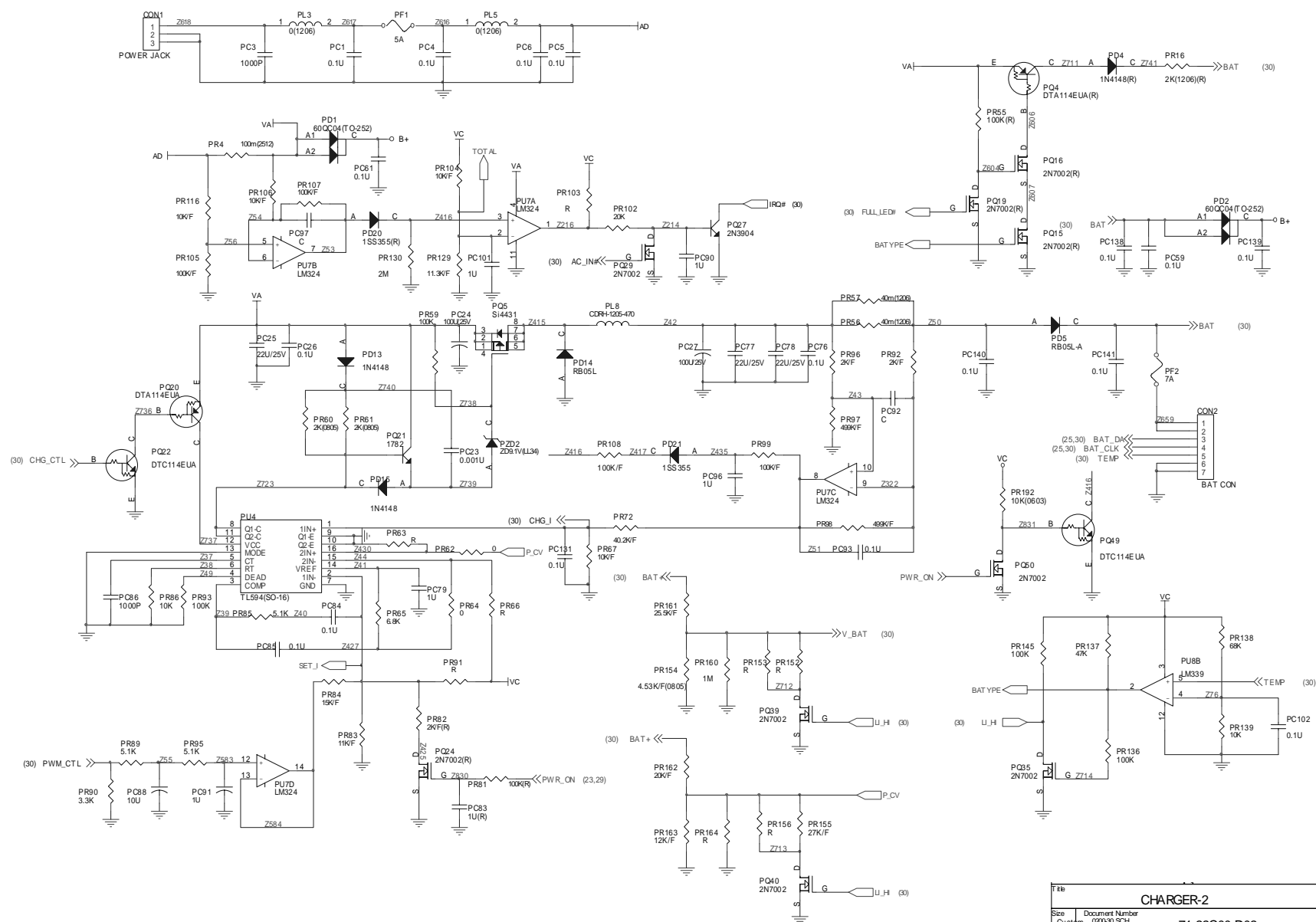


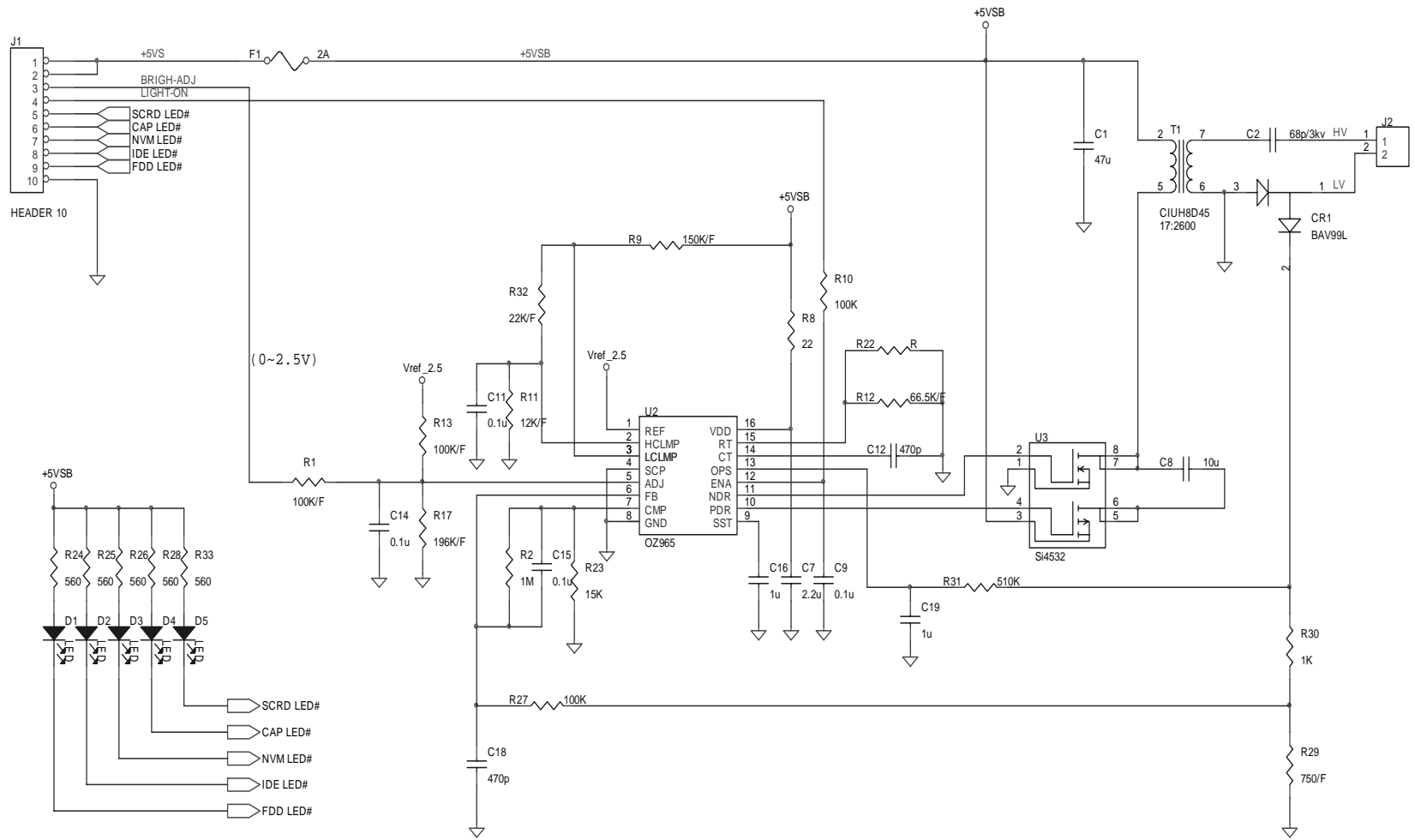
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